**Vishay Siliconix** 

# Load Switch with Level-Shift

PRODUCT SUMMARY					
$V_{DS2}$ (V) ( $V_{IN}$ )	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)			
	0.060 at V <sub>IN</sub> = 4.5 V	2.8			
1.8 to 12	0.095 at V <sub>IN</sub> = 2.5 V	2.2			
	0.130 at V <sub>IN</sub> = 1.8 V	1.9			

### FEATURES

- Halogen-free According to IEC 61249-2-21
  Definition
- 60 m $\Omega$  Low R<sub>DS(on)</sub> TrenchFET<sup>®</sup>
- 1.8 V to 12 V Input
- 1.5 V to 8 V Logic Level Control
- Low Profile, Small Footprint TSOP-6 Package
- 3000 V ESD Protection On Input Switch, V<sub>ON/OFF</sub>
- Adjustable Slew-Rate
- Compliant to RoHS Directive 2002/95/EC

#### **APPLICATIONS**

• Load Switching for Portable Devices

### DESCRIPTION

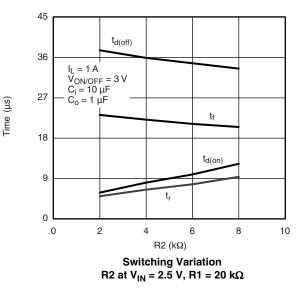
**APPLICATION CIRCUITS** 

The Si3865CDV includes a P- and N-Channel MOSFET in a single TSOP-6 package. The low on-resistance P-Channel TrenchFET is tailored for use as a load switch. The N-Channel, with an external resistor, can be used as a level-

#### Si3865CDV 2, 3 VOUT VIN റ Q2 R1 6 ON/OFF O LOAD Co Q1 $C_i >$ 1 R2 GND R2 0 O

COMPONENTS					
R1	Pull-Up Resistor	Typical 10 k $\Omega$ to 1 M $\Omega^*$			
R2	Optional Slew-Rate Control	Typical 0 to 100 k $\Omega^*$			
C1	<b>Optional Slew-Rate Control</b>	Typical 1000 pF			

shift to drive the P-Channel load-switch. The N-Channel MOSFET has internal ESD protection and can be driven by logic signals as low as 1.5 V. The Si3865CDV operates on supply lines from 1.8 to 12 V, and can drive loads up to 2.8 A.



The Si3865CDV is ideally suited for high-side load switching in portable applications. The integrated N-Channel level-shift device saves space by reducing external components. The slew rate is set externally so that rise-times can be tailored to different load types.

#### Note:

\* Minimum R1 value should be at least 10 x R2 to ensure Q1 turn-on at 1.8 V input.

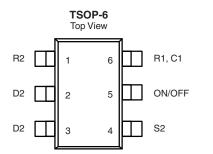
Pb-free RoHS

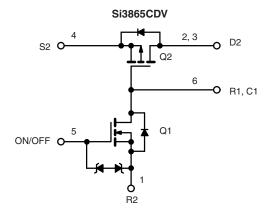
> COMPLIANT HALOGEN FREE Available

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### FUNCTIONAL BLOCK DIAGRAM





Ordering Information: Si3865CDV-T1-E3 (Lead (Pb)-free) Si3865CDV-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b> (T	<sub>A</sub> = 25 °C, unle	ess otherwise r	noted)		
Parameter	Symbol	Limit	Unit		
Input Voltage		V <sub>IN</sub> (V <sub>DS2</sub> )	12		
Gate-Source Voltage		V <sub>GS2</sub>	8	V	
ON/OFF Voltage		$V_{ON/OFF}$ ( $V_{GS1}$ )	8	-	
Load Current	Continuous <sup>a, b</sup>	Ŀ	± 2.8		
Load Current	Pulsed <sup>b, c</sup>		± 6	А	
Continuous Intrinsic Diode Conduction <sup>a</sup>		۱ <sub>S</sub>	- 1		
Maximum Power Dissipation <sup>a</sup>	PD	0.83	W		
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		
ESD Rating, MIL-STD-883D Human Body Model (10	ESD	3	kV		

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient (continuous current) <sup>a</sup>	R <sub>thJA</sub>	130	150	°C/W
Maximum Junction-to-Foot (Q2)	R <sub>thJF</sub>	75	90	0/11

<b>SPECIFICATIONS</b> ( $T_J = 25 \text{ °C}$ , unless otherwise noted)							
Parameter	Symbol	Test Conditions		Min.	Тур.	Max.	Unit
OFF Characteristics							
Reverse Leakage Current	I <sub>FL</sub>	$V_{IN} = 12 V, V_{ON/OFF} = 0 V$				1	μA
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 1 A			- 0.77	- 1	V
ON Characteristics							
Input Voltage Range	$V_{IN}$ ( $V_{DS2}$ )			1.8		12	V
On-Resistance (P-Channel) at 1 A	R <sub>DS(on)</sub>	V <sub>ON/OFF</sub> = 1.5 V, I <sub>D</sub> = 1 A	V <sub>IN</sub> = 4.5 V		0.050	0.060	Ω
			V <sub>IN</sub> = 2.5 V		0.073	0.095	
			V <sub>IN</sub> = 1.8 V		0.100	0.130	
On-State (P-Channel) Drain-Current		$V_{\text{IN-OUT}} {\leq} 0.2$ V, $V_{\text{IN}}$ = 5 V, $V_{\text{ON/OFF}}$ = 1.5 V		1			А
	I <sub>D(on)</sub>	$V_{\text{IN-OUT}} \leq 0.3$ V, $V_{\text{IN}}$ = 3 V, $V_{\text{ON/OFF}}$ = 1.5 V		1			

Notes:

a. Surface Mounted on FR4 board.

b.  $V_{IN}$  = 8 V,  $V_{ON/OFF}$  = 8 V,  $T_A$  = 25 °C. c. Pulse test: pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

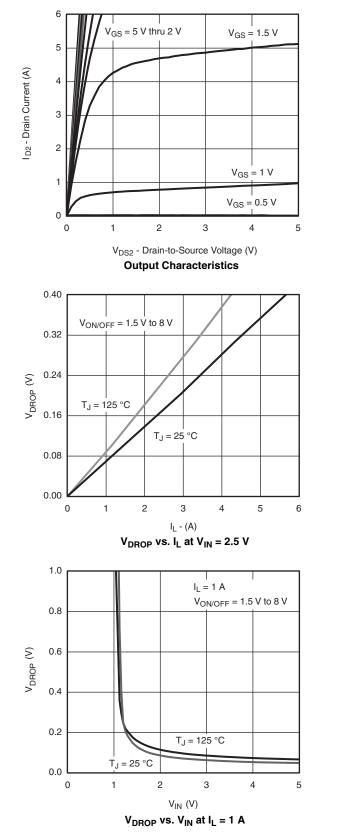
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

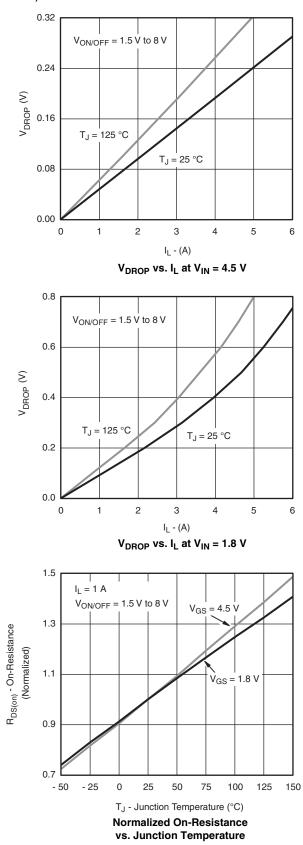


# Si3865CDV

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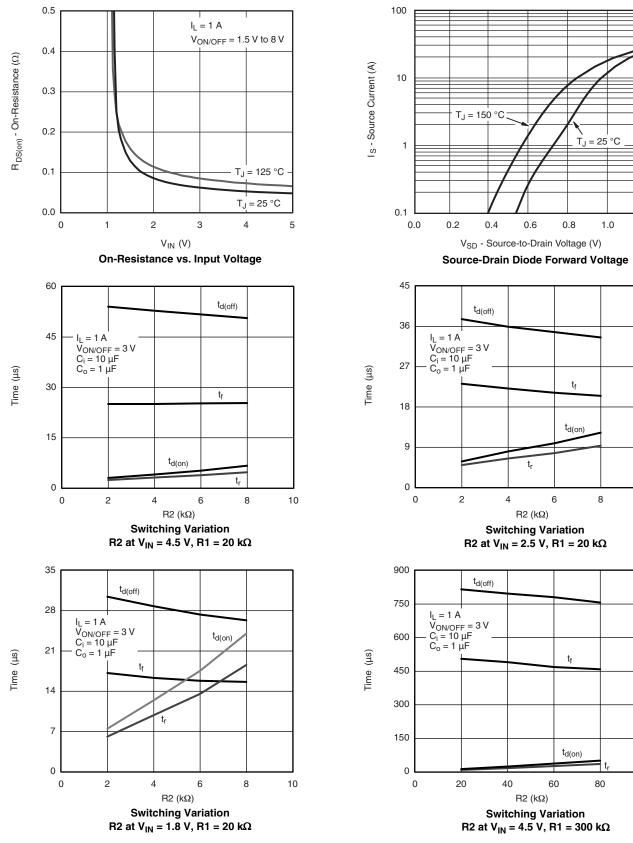




# Si3865CDV

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### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

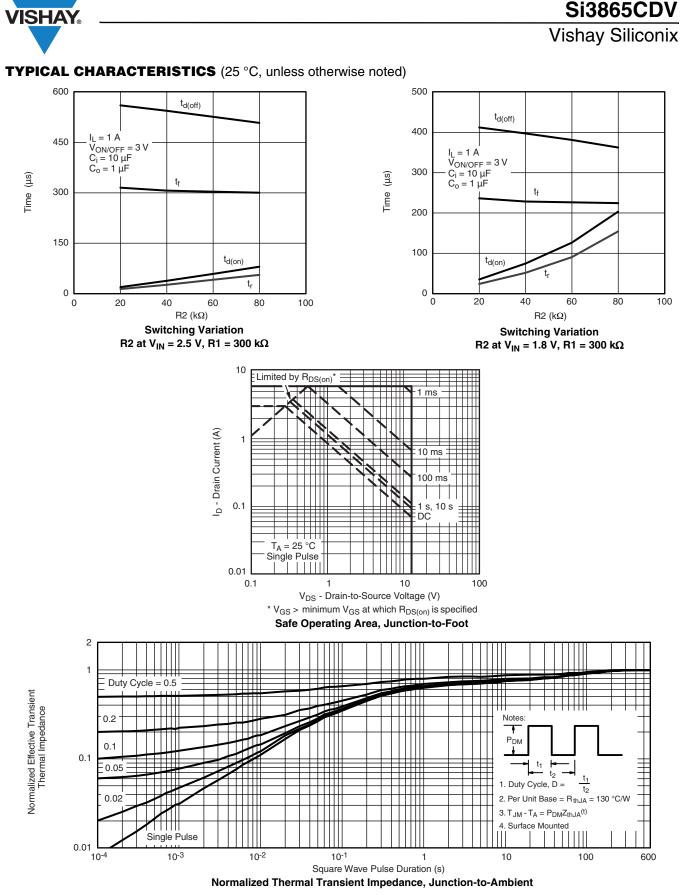




1.2

10

100



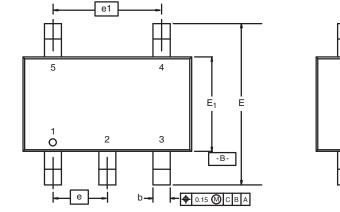
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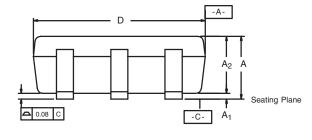
Package Information

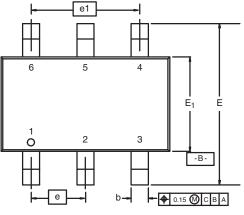
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TSOP: 5/6-LEAD JEDEC Part Number: MO-193C

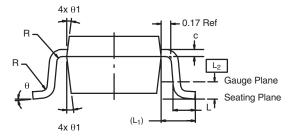








6-LEAD TSOP



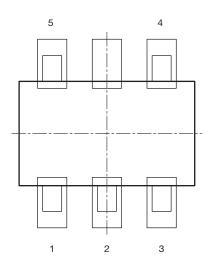
	MIL	LIMETER	RS	INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
<b>A</b> <sub>1</sub>	0.01	-	0.10	0.0004	-	0.004	
A <sub>2</sub>	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
Е	2.70	2.85	2.98	0.106	0.112	0.117	
E <sub>1</sub>	1.55	1.65	1.70	0.061	0.065	0.067	
е		0.95 BSC		0.0374 BSC			
<b>e</b> <sub>1</sub>	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L <sub>1</sub>		0.60 Ref			0.024 Ref		
L <sub>2</sub>	0.25 BSC				0.010 BSC		
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
$\theta_1$	7° Nom				7° Nom		
ECN: C DWG: 5		ev. I, 18-Dec	c-06				

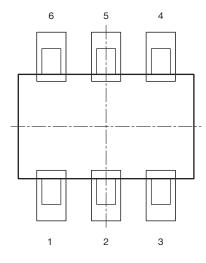
## **PAD** Pattern



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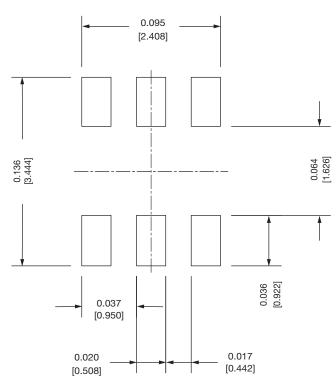
# **Recommended Land Pattern For TSOP-5L / TSOP-6L**





TSOP 5L





#### Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022	
DWG: 3010	

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