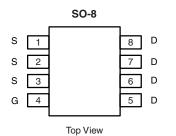


Vishay Siliconix

### N-Channel 12-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
	0.0017 at V <sub>GS</sub> = 4.5 V	50				
12	0.002 at V <sub>GS</sub> = 2.5 V	46	56 nC			
	0.0027 at V <sub>GS</sub> = 1.8 V	40				



Ordering Information: Si4448DY-T1-E3 (Lead (Pb)-free)

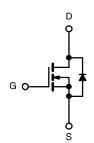
Si4448DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21
  Available
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % Rg Tested
- 100 % UIS Tested

#### **APPLICATIONS**

- POL
- DC/DC



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T<sub>A</sub> = 25 °C, unless otherwise noted Parameter Symbol Limit Unit 12 **Drain-Source Voltage** V<sub>DS</sub> v Gate-Source Voltage ± 8 V<sub>GS</sub> 50 T<sub>C</sub> = 25 °C T<sub>C</sub> = 70 °C 40 Continuous Drain Current (T<sub>J</sub> = 150 °C)  $I_D$ T<sub>A</sub> = 25 °C 32<sup>b, c</sup> T<sub>A</sub> = 70 °C 26<sup>b, c</sup> А Pulsed Drain Current 70 I<sub>DM</sub> T<sub>C</sub> = 25 °C 7 Continuous Source-Drain Diode Current  $I_S$ 3<sup>b, c</sup> T<sub>A</sub> = 25 °C Single Pulse Avalanche Current 20  $I_{AS}$ L = 0.1 mHAvalanche Energy 20 E<sub>AS</sub> mJ T<sub>C</sub> = 25 °C 7.8 T<sub>C</sub> = 70 °C 5.0  $P_D$ Maximum Power Dissipation W  $T_A = 25 \ ^{\circ}C$ 3.5<sup>b, c</sup> T<sub>A</sub> = 70 °C 2.2<sup>b, c</sup> Operating Junction and Storage Temperature Range T<sub>J</sub>, T<sub>stg</sub> - 55 to 150 °C

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	29	35	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	13	16	0/11		

Notes:

a. Based on T<sub>C</sub> = 25 °C.

b. Surface Mounted on 1" x 1" FR4 board.

c. t = 10 s.

d. Maximum under Steady State conditions is 80 °C/W.



### Si4448DY

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	12			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L 050 A		14			
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 3.3		mV/°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	0.4		1.0	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
		$V_{DS} = 12 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 12 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			10	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5$ V, $V_{GS} = 4.5$ V	40			Α	
		$V_{GS} = 4.5 \text{ V}, I_{D} = 20 \text{ A}$		0.0014	0.0017	+	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 15 A		0.0016	0.0020	Ω	
		V <sub>GS</sub> = 1.8 V, I <sub>D</sub> = 10 A		0.0022	0.0027	1	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 6 V, I_{D} = 20 A$		190		S	
Dynamic <sup>b</sup>				1			
Input Capacitance	C <sub>iss</sub>			12350			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 6 V, V <sub>GS</sub> = 0 V, f = 1 MHz		2775		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			1590			
		$V_{DS} = 6 V, V_{GS} = 4.5 V, I_{D} = 10 A$		99	150	+	
Total Gate Charge	Qg			56	85	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 6 V, V_{GS} = 2.5 V, I_{D} = 10 A$		10.3			
Gate-Drain Charge	Q <sub>gd</sub>			13.4			
Gate Resistance	R <sub>g</sub>	f = 1 MHz		0.75	1.5	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			38	70		
Rise Time	t <sub>r</sub>	$V_{DD} = 6 V, R_1 = 0.6 \Omega$		22	40	1	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 10 \text{ A}, V_{\text{GEN}} = 4.5 \text{ V}, \text{R}_{\text{g}} = 1 \Omega$		240	400		
Fall Time	t <sub>f</sub>			33	55		
Turn-On Delay Time	t <sub>d(on)</sub>			20	40	ns	
Rise Time	t <sub>r</sub>	$V_{DD} = 6 V, R_1 = 0.6 \Omega$		11	22	-	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 10 \text{ A}, \text{ V}_{\text{GEN}} = 8 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$		100	170		
Fall Time	t <sub>f</sub>	Ŭ		11	22		
Drain-Source Body Diode Characteristic	-			1			
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			7	- A	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				70		
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 3 A		0.54	1.1	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>	-		84	140	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			93	150	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	I <sub>F</sub> = 10 A, dl/dt = 100 A/μs, T <sub>J</sub> = 25 °C		28			
Reverse Recovery Rise Time	t <sub>b</sub>			56		ns	

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

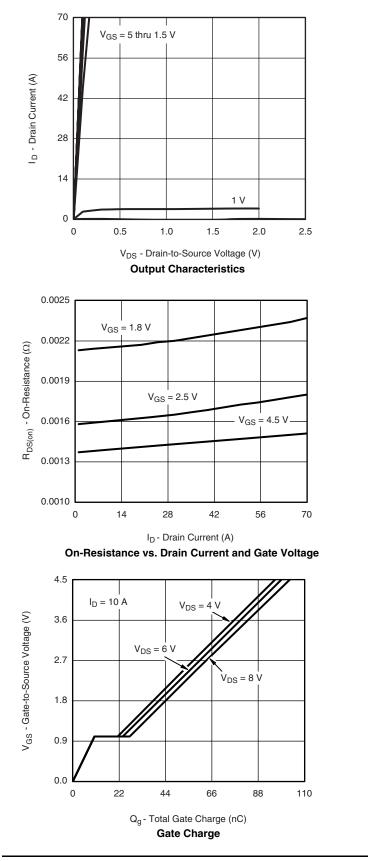
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

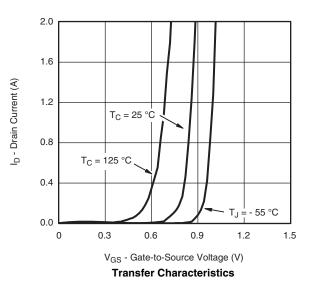


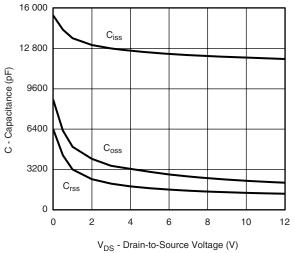
# Si4448DY

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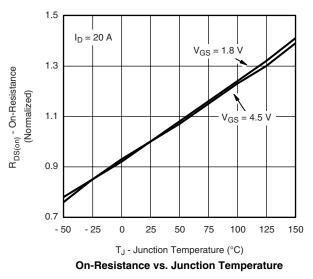
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







Capacitance

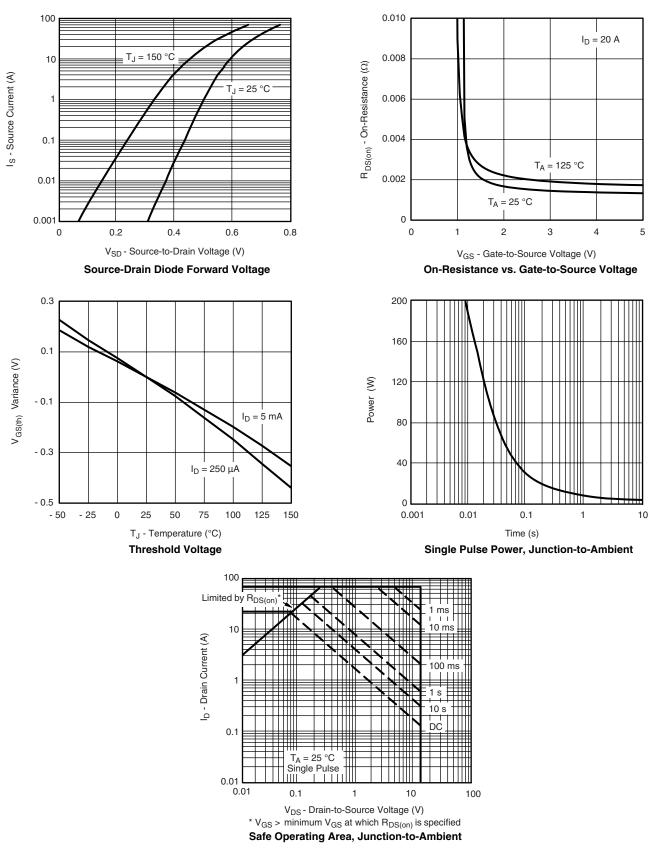


Document Number: 69653 S09-0138-Rev. B, 02-Feb-09

### Si4448DY

#### Vishay Siliconix

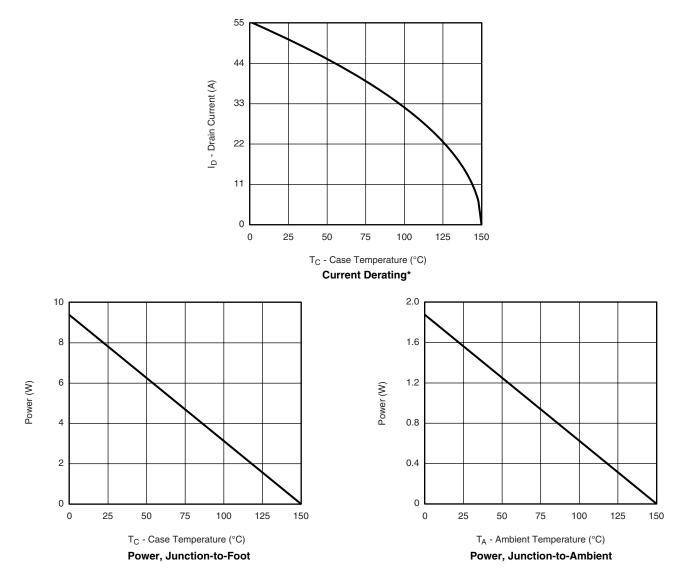




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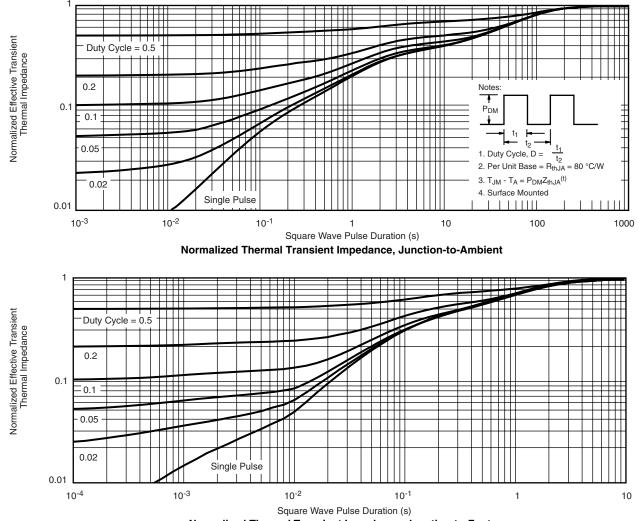
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

#### **Vishay Siliconix**

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?69653">www.vishay.com/ppg?69653</a>.



## Package Information

Vishay Siliconix

# SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012





	MILLIMETERS		INCHES		
DIM	Min	Мах	Min	Max	
A	1.35	1.75	0.053	0.069	
A <sub>1</sub>	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
E	3.80	4.00	0.150	0.157	
е	1.27 BSC		0.050 BSC		
н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498					

### **Application Note 826**

Vishay Siliconix



**RECOMMENDED MINIMUM PADS FOR SO-8** 



Recommended Minimum Pads Dimensions in Inches/(mm)

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