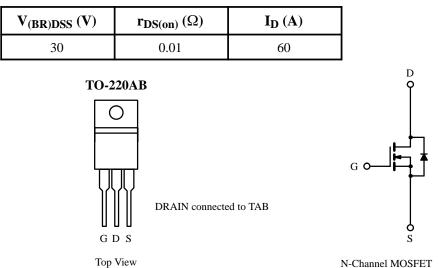


Siliconix

## N-Channel 30-V (D-S), 150°C MOSFET, Logic Level

#### **Product Summary**



## Absolute Maximum Ratings ( $T_C = 25^{\circ}C$ Unless Otherwise Noted)

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30	v	
Gate-Source Voltage		V <sub>GS</sub>	$\pm 20$	Ť	
Continuous Drain Current	$T_C = 25^{\circ}C$	т	60		
	$T_C = 100^{\circ}C$	ID	51	A	
Pulsed Drain Current		I <sub>DM</sub>	240	A	
Avalanche Current		I <sub>AR</sub>	60	7	
Avalanche Energy	L = 0.1  mH	E <sub>AS</sub>	180	mJ	
Repetitive Avalanche Energy <sup>a</sup>	L = 0.05 mH	E <sub>AR</sub>	90		
Power Dissipation	$T_C = 25^{\circ}C$	р	105	w	
	$T_C = 100^{\circ}C$	P <sub>D</sub>	42	vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C	
Lead Temperature $(^{1}/_{16})$ " from case for 10 sec.)		TL	300		

### **Thermal Resistance Ratings**

Parameter	Symbol	Typical	Maximum	Unit
Junction-to-Ambient	R <sub>thJA</sub>		80	
Junction-to-Case	R <sub>thJC</sub>		1.2	°C/W
Case-to-Sink	R <sub>thCS</sub>	1.0		

Notes:

Updates to this data sheet may be obtained via facsimile by calling Siliconix FaxBack, 1-408-970-5600. Please request FaxBack document #70280. A SPICE Model data sheet is available for this product (FaxBack document #70525).

a. Duty cycle  $\leq 1\%$ 

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## Specifications ( $T_J = 25^{\circ}C$ Unless Otherwise Noted)

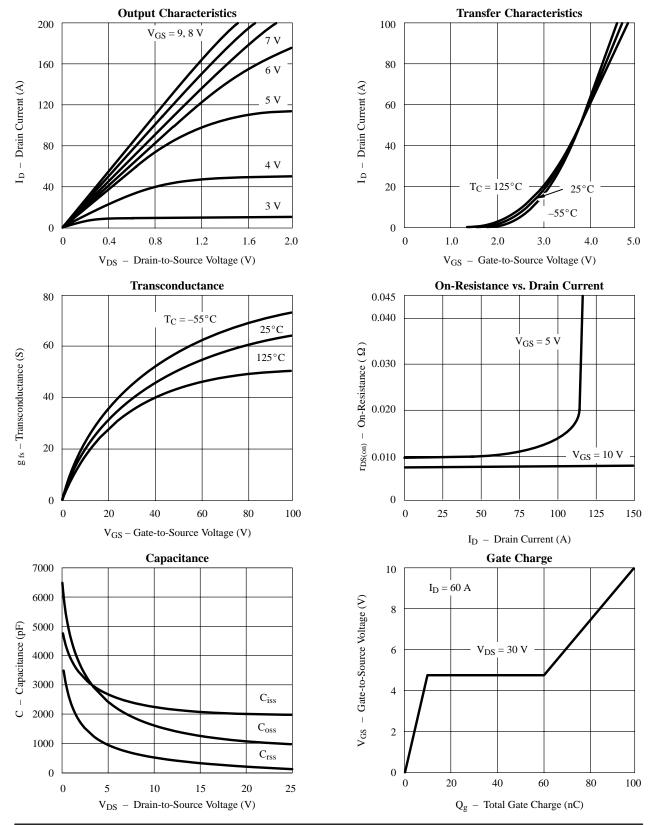
Parameter	Symbol	Test Condition	Min	Typa	Max	Unit	
Static	<b>I</b>		•	•	•		
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS}$ = 0 V, $I_D$ = 250 $\mu A$	30				
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 1 \text{ mA}$	0.8		3.0	v	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0$ V, $V_{GS} = \pm 20$ V			±500	nA	
Zero Gate Voltage Drain Current	т	$V_{DS} = 24$ V, $V_{GS} = 0$ V			25		
	I <sub>DSS</sub>	$V_{DS}$ = 24 V, $V_{GS}$ = 0 V, $T_J$ = 125 $^\circ C$	250			μΑ	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 10 \text{ V}$	60			Α	
Drain-Source On-State Resistance <sup>b</sup>		$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$		0.007	0.010	Ω	
	r <sub>DS(on)</sub>	$V_{GS} = 5 \text{ V}, I_D = 30 \text{ A}$		0.010	0.015		
		$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}, T_J = 125^{\circ}\text{C}$		0.009	0.014	1	
Forward Transconductance <sup>b</sup>	g <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_D = 30 \text{ A}$		45		S	
Dynamic	•						
Input Capacitance	C <sub>iss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = 25 V, f = 1 MHz		2600		pF	
Output Capacitance	C <sub>oss</sub>			1500			
Reverse Transfer Capacitance	C <sub>rss</sub>			750			
Total Gate Charge <sup>c</sup>	Qg			100	120	nC	
Gate-Source Charge <sup>c</sup>	Qgs	$V_{DS} = 15 V_{,} V_{GS} = 10 V, I_{D} = 60 A$		10	15		
Gate-Drain Charge <sup>c</sup>	Qgd			45	75		
Turn-On Delay Timec	t <sub>d(on)</sub>			14	30	ns	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 30 \text{ V}, R_L = 1 \Omega$ $I_D \simeq 30 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 2.5 \Omega$		25	50		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			65	100		
Fall Time <sup>c</sup>	tf			45	80		
Source-Drain Diode Ratings and	Characteristi	$\operatorname{cs}\left(\mathrm{T}_{\mathrm{C}}=25^{\circ}\mathrm{C}\right)$					
Continuous Current	I <sub>S</sub>				60		
Pulsed Current	I <sub>SM</sub>				240	A	
Forward Voltage <sup>b</sup>	V <sub>SD</sub>	$I_F = 60 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$			1.6	v	
Reverse Recovery Time	t <sub>rr</sub>			160		ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	$I_F=60~A,~dl_F/dt=100~A/\mu s$		13		Α	
Reverse Recovery Charge	Qrr			1.0		μC	

Notes:

a. For design aid only; not subject to production testing. b. Pulse test; pulse width  $\leq 300 \ \mu$ s, duty cycle  $\leq 2\%$ .

Independent of operating temperature. c.

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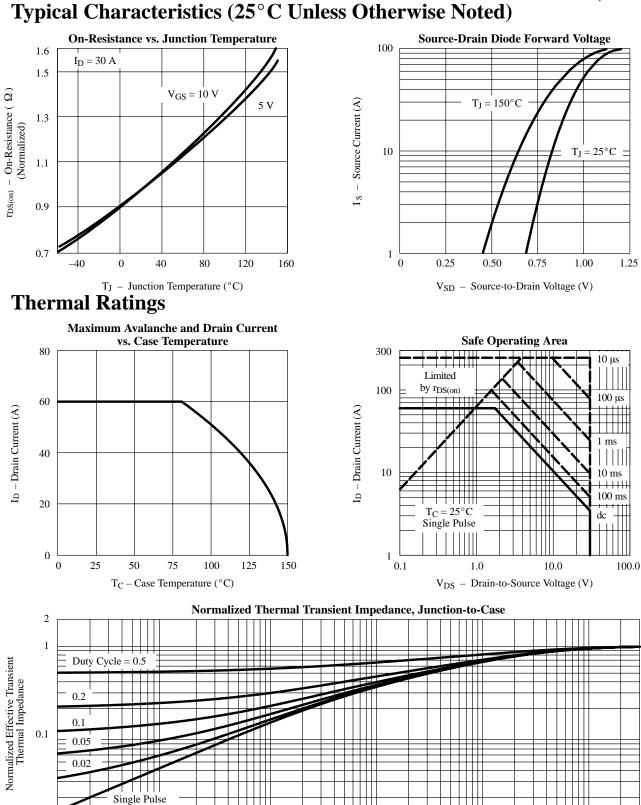
## Typical Characteristics (25°C Unless Otherwise Noted)

VISHAY

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Square Wave Pulse Duration (sec)

 $10^{-2}$ 

 $10^{-1}$ 

3

1

 $10^{-3}$ 

0.01

 $10^{-5}$ 

10-4



Vishay

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