



N-Channel 20 V (D-S) MOSFET with Schottky Diode

MOSFET PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$ $I_{D}\left(\Omega\right)$				
20	0.125 at V _{GS} = 4.5 V	2.4			
20	0.200 at V _{GS} = 2.5 V	1.8			

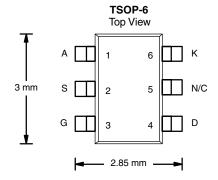
SCHOTTKY PRODUCT SUMMARY					
V _{KA} (V)	V _F (V) Diode Forward Voltage	I _F (A)			
20	0.48 V at 0.5 A	0.5			

FEATURES

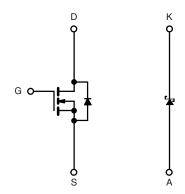
- Halogen-free According to IEC 61249-2-21 Definition
- LITTLE FOOT® Plus
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC



COMPLIANT HALOGEN FREE



Ordering Information: Si3812DV-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (TA)	= 25 °C, unl	ess otherwi	se noted)		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage (MOSFET)		V _{DS}	20		V
Reverse Voltage (Schottky)		V_{KA}	20		
Gate-Source Voltage (MOSFET)		V _{GS}	± 12		V
0 11 0 17 17 17 17 17 17 17 17 17 17 17 17 17	T _A = 25 °C	-	2.4	2.0	
Continuous Drain Current (T _J = 150 °C) (MOSFET) ^a	T _A = 85 °C	I _D	1.7	1.4	
Pulsed Drain Current (MOSFET)		I _{DM}	8		
Continuous Source Current (MOSFET Diode Conduction) ^a		I _S	1.05	0.75	Α
Average Foward Current (Schottky)		I _F	0.5	0.5	
Pulsed Foward Current (Schottky)		I _{FM}	8	8	
	T _A = 25 °C		1.15	0.83	
Maximum Power Dissipation (MOSFET) ^a	T _A = 85 °C	P _D	0.59	0.53	14/
	T _A = 25 °C		1.0	0.76	W
Maximum Power Dissipation (Schottky) ^a	T _A = 85 °C		0.52	0.48	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 t	°C		

Note:

a. Surface mounted on 1" x 1" FR4 board.

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THERMAL RESISTANCE RATINGS								
Parameter	Device	Symbol	Typical	Maximum	Unit			
	t ≤ 5 s	MOSFET	- R _{thJA}	93	110	°C/W		
Lucation to Ambient		Schottky		103	125			
Junction-to-Ambient ^a	Steady State	MOSFET		130	150			
		Schottky		140	165			
Junction to Foot (MOSFET Drain, Schottky Cathode)	Steady State	MOSFET	- R _{thJF}	75	90			
Junction to Foot (MOSFET Drain, Schottky Cathode)	Steady State	Schottky		80	95			

Note:

a. Surface mounted on 1" x 1" FR4 board.

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	0.6			V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA
Zero Gate Voltage Drain Current	lane	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ
(MOSFET and Schottky)	I _{DSS}	V _{DS} = 16 V, V _{GS} = 0 V, T _J = 85 °C			10	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	5			Α
	В	V _{GS} = 4.5 V, I _D = 2.4 A		0.100	0.125	Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 1.0 \text{ A}$		0.160	0.200	\$2
Forward Transconductance ^a	9 _{fs}	V _{DS} = 5 V, I _D = 2.4 A		5		S
Schottky Diode Forward Voltage ^a	V_{SD}	I _S = 1.5 A, V _{GS} = 0 V		0.79	1.1	V
Dynamic ^b			•	•		
Total Gate Charge	Q_g			2.1	4.0	
Gate-Source Charge	Q _{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 2.4 \text{ A}$		0.3		nC
Gate-Drain Charge	Q_{gd}			0.4		1
Gate Resistance	R_g		1		3.7	Ω
Turn-On Delay Time	t _{d(on)}			10	17	
Rise Time	t _r	V_{DD} = 10 V, R_L = 10 Ω		30	50	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 1 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 6 \Omega$		14	25	ns
Fall Time	t _f			6	12]
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 3.0 A, dl/dt = 100 A/μs		30	50	

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

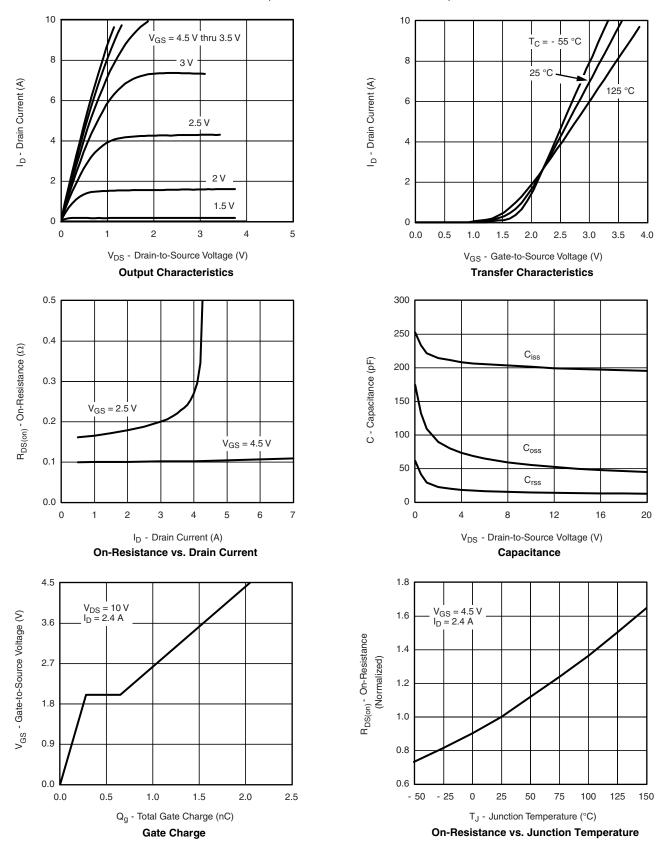
SCHOTTKY SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Forward Voltage Drop	V	I _F = 0.5 A		0.42	0.48	V		
	V _F	I _F = 0.5 A, T _J = 125 °C		0.33	0.4	V		
	I _{rm}	V _R = 20		0.002	0.100			
Maximum Reverse Leakage Current		V _R = 20 V, T _J = 75 °C		0.06	1	mA		
		V _R = 20 V, T _J = 125 °C		1.5	10			
Junction Capacitance	C _T	V _R = 10 V		31		pF		

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





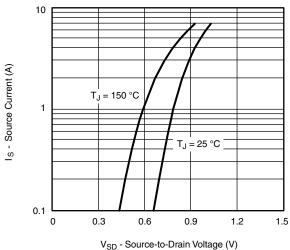
MOSFET TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



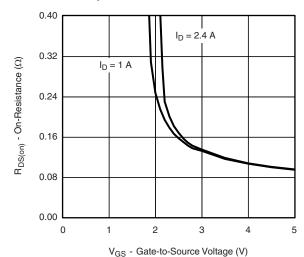
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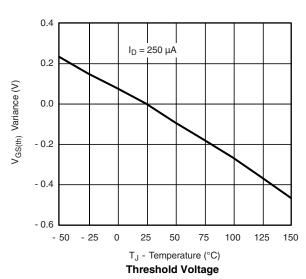
MOSFET TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

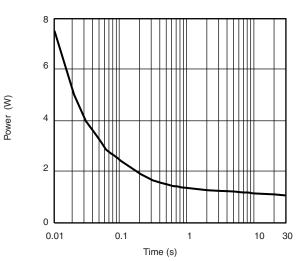


Source-Drain Diode Forward Voltage

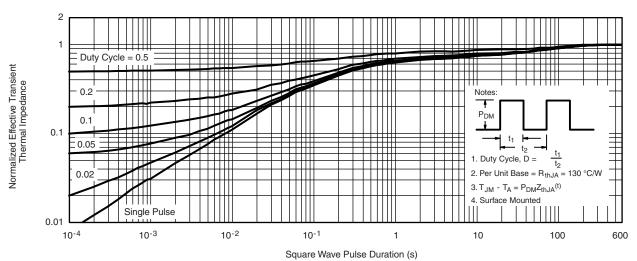


On-Resistance vs. Gate-to-Source Voltage





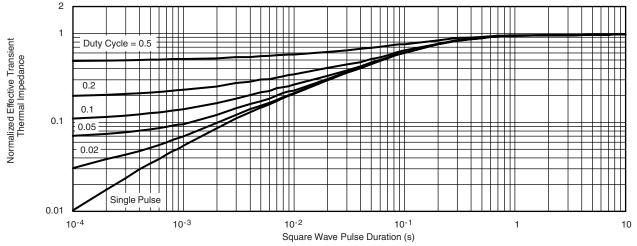
Single Pulse Power, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient

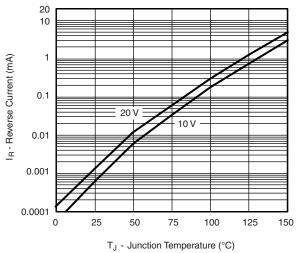


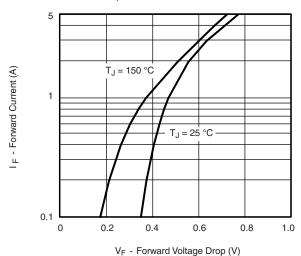
MOSFET TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

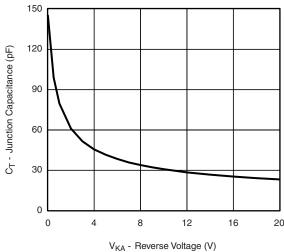
SCHOTTKY TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





Reverse Current vs. Junction Temperature

Forward Voltage Drop

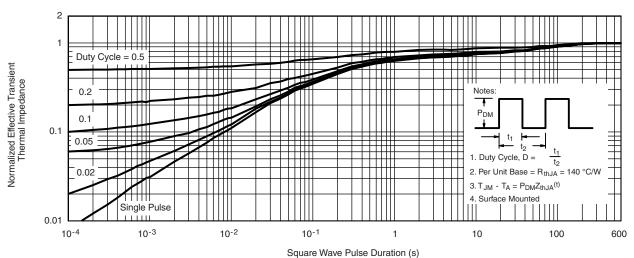


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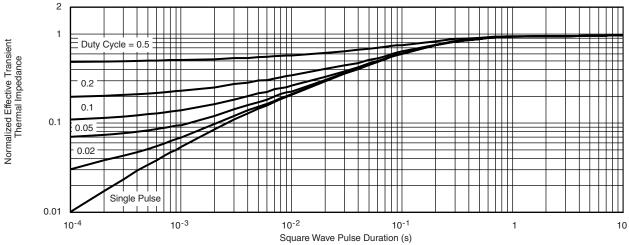
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SCHOTTKY TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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