

RoHS

COMPLIANT HALOGEN

FREE

Available

**Vishay Siliconix** 

# P-Channel 20-V (D-S), 1.5-V (G-S) MOSFET

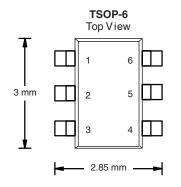
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)		
- 20	0.024 at V <sub>GS</sub> = - 4.5 V	- 7		
	0.030 at V <sub>GS</sub> = - 2.5 V	- 6.2		
	0.038 at V <sub>GS</sub> = - 1.8 V	- 5.2		
	0.048 at V <sub>GS</sub> = - 1.5 V	- 5.0		

#### FEATURES

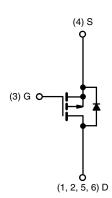
- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchFET<sup>®</sup> Power MOSFET: 1.5 V Rated
- Ultra-Low On-Resistance
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC

#### **APPLICATIONS**

• Load Switch and PA Switch for Portable Devices



Ordering Information: Si3495DV-T1-E3 (Lead (Pb)-free) Si3495DV-T1-GE3 (Lead (Pb)-free and Halogen-free) Marking Code: 95xxx



P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	$T_A = 25$ °C, unles	ss otherwise r	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 20		V
Gate-Source Voltage		V <sub>GS</sub>	± 5		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	– I <sub>D</sub>	- 7	- 5.3	
	T <sub>A</sub> = 85 °C		- 3.6	- 3.9	
Pulsed Drain Current		I <sub>DM</sub>	- 20		A
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	- 1.7	- 0.9	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	2.0	1.1	W
	T <sub>A</sub> = 85 °C		1.0	0.6	~~~
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 5 s	- R <sub>thJA</sub> R <sub>thJF</sub>	45	62.5	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		90	110	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		25	30	

Note:

a. Surface Mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions Min. Typ.		Тур.	Max.	Unit	
Static				•			
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.35		- 0.75	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 5 V$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$			- 1		
		$V_{DS}$ = - 20 V, $V_{GS}$ = 0 V, $T_{J}$ = 85 °C			- 10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 20			А	
Drain-Source On-State Resistance <sup>a</sup>		$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -7 \text{ A}$	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 7 A 0.02		0.024		
	R <sub>DS(on)</sub>	$V_{GS}$ = - 2.5 V, $I_D$ = - 6.2 A		0.024	0.030	Ω	
		$V_{GS}$ = - 1.8 V, $I_D$ = - 5.2 A		0.030	0.038		
		$V_{GS} = -1.5 \text{ V}, \text{ I}_{D} = -3 \text{ A}$		0.036	0.048		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = -5 V, I_{D} = -7 A$		25		S	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{\rm S}$ = - 1.7 A, $V_{\rm GS}$ = 0 V		- 0.62	- 1.1	V	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg			25	38		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 10 V, $V_{GS}$ = - 4.5 V, $I_D$ = - 7 A		2.5		nC	
Gate-Drain Charge	Q <sub>gd</sub>			7		1	
Gate Resistance	R <sub>g</sub>		4	8.5	13	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			19	30		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 10 $\Omega$		36	55	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_{\text{D}}\cong$ - 1 A, $\text{V}_{\text{GEN}}$ = - 4.5 V, $\text{R}_{\text{g}}$ = 6 $\Omega$		200	300		
Fall Time	t <sub>f</sub>			106	160		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.7 A, dl/dt = 100 A/μs		35	60		

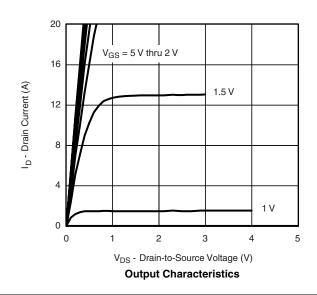
Notes:

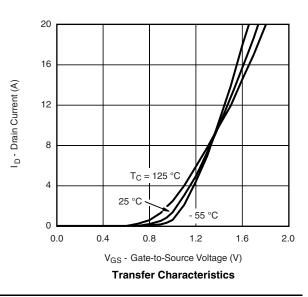
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

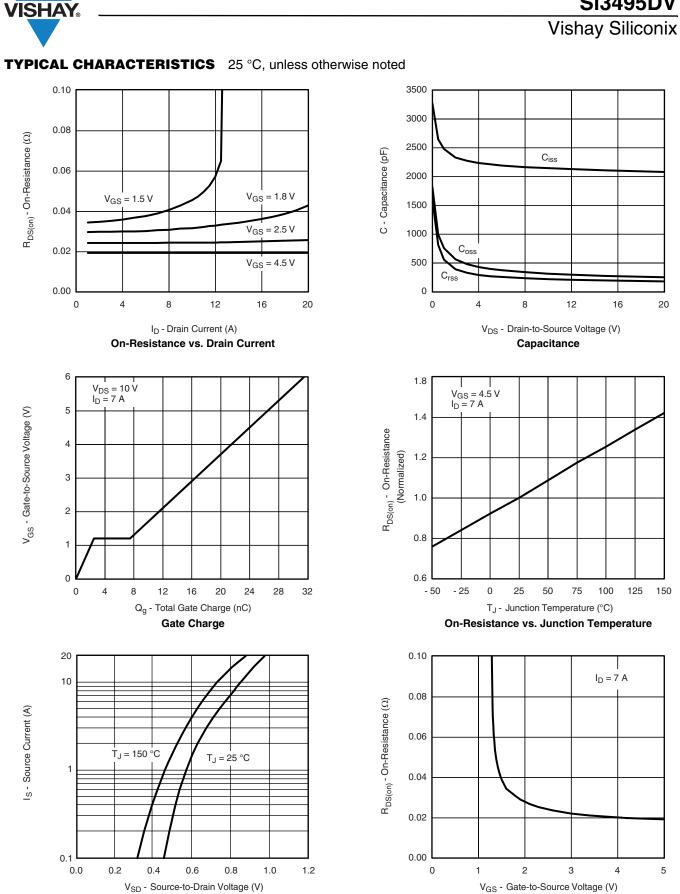
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







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On-Resistance vs. Gate-to-Source Voltage

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Si3495DV

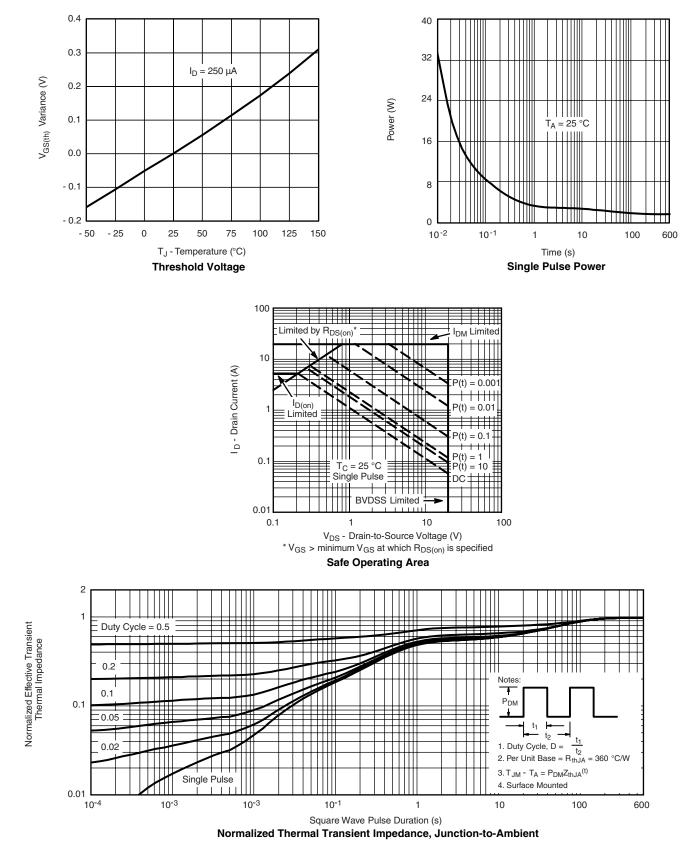
20

150

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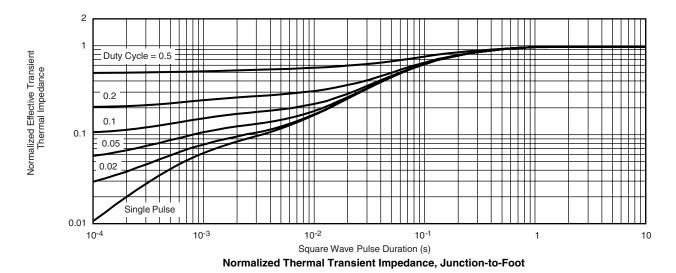




Si3495DV

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Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg273135">www.vishay.com/ppg273135</a>.



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