Vishay Siliconix

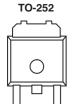
N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)}\left(\Omega\right)$	I _D (A) ^{a, e}	Q _g (Typ)	
30	0.0057 at V _{GS} = 10 V	90	30	
30	0.0078 at V _{GS} = 4.5 V	77	30	

FEATURES

- TrenchFET® Power MOSFET
- Optimized for Low-Side Synchronous **Rectifier Operation**
- 100 % R_g Tested

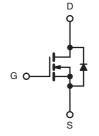




D Top View Drain Connected to Tab

APPLICATIONS

- DC/DC Converters
- · Synchronous Rectifiers



N-Channel MOSFET

Ordering Information: SUD50N03-06AP-E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATINGS	S T _A = 25 °C, unle	ess otherwise r	noted	
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	7 v
	T _C = 25 °C		90 ^{a, e}	
Continuous Drain Current /T 175 °C)	T _C = 70 °C		75 ^{a, e}	
Continuous Drain Current (T _J = 175 °C)	T _A = 25 °C	I _D	30 ^{b, c}	
	T _A = 70 °C	1	25 ^{b, c}	1
Pulsed Drain Current		I _{DM}	100	Α
Continuous Source-Drain Diode Current	T _C = 25 °C	,	55 ^{a, e}	
	T _A = 25 °C	l _S	6.7 ^{b, c}	
Avalanche Current Pulse	L = 0.1 mH	I _{AS}	45	
Single Pulse Avalanche Energy	L = 0.1 min	E _{AS}	101	mJ
Maximum Power Dissipation	T _C = 25 °C		83	
	T _C = 70 °C		58	١٨,
	T _A = 25 °C	P _D	10 ^{b, c}	W
	T _A = 70 °C		7 ^{b, c}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d} t ≤ 10 sec		R _{thJA}	12	15	°C/W
Maximum Junction-to-Case	Steady State	R _{thJC}	1.5	1.8	C/VV

- a. Based on T_C = 25 °C. b. Surface Mounted on 1" x 1" FR4 board.
- d. Maximum under Steady State conditions is 50 °C/W.
- e. Calculated based on maximum junction temperature. Package limitation current is 50 A.
- * Pb containing terminations are not RoHS compliant, exemptions may apply.

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New Product



Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μΑ		25		m\//°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	i _D = 250 μA		- 6.3		mV/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.2		2.4	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zoro Coto Voltogo Droin Current	l	V _{DS} = 30 V, V _{GS} = 0 V			1	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			10	μΑ
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α
D : 0		V _{GS} = 10 V, I _D = 20 A		0.0046	0.0057	0
Drain-Source On-State Resistance ^a	^r DS(on)	$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		0.0062	0.0078	Ω
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 30 A		70		S
Dynamic ^b						
Input Capacitance	C _{iss}			3800		
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		615		pF
Reverse Transfer Capacitance	C _{rss}			305		
Total Cata Charge	0	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 30 A		62	95	nC
Total Gate Charge	Qg			30	45	
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 25 \text{ A}$		11		
Gate-Drain Charge	Q_{gd}			9		
Gate Resistance	R _g	f = 1 MHz		0.9	1.4	Ω
Turn-On Delay Time	t _{d(on)}			12	18	
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.5 Ω		10	15]
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 30 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		30	45	
Fall Time	t _f			8	12	ne
Turn-On Delay Time	t _{d(on)}			26	40	ns
Rise Time	t _r	V_{DD} = 15 V, R_L = 0.6 Ω		230	345	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 25 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		25	40	
Fall Time	t _f			9	14	
Drain-Source Body Diode Characteristic	s					
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			55 ^c	Α
Pulse Diode Forward Current ^a	I _{SM}		100			
Body Diode Voltage	V_{SD}	I _S = 6.7 A		0.9	1.5	V
Body Diode Reverse Recovery Time	t _{rr}			65	100	ns
Body Diode Reverse Recovery Charge	Q _{rr}	$I_F = 6.7 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_{.1} = 25 \text{ °C}$		38	60	nC
Reverse Recovery Fall Time	t _a	1F = 0.7 A, αι/αι = 100 A/μS, 1J = 25 °C		50		
Reverse Recovery Rise Time	t _b			15		ns

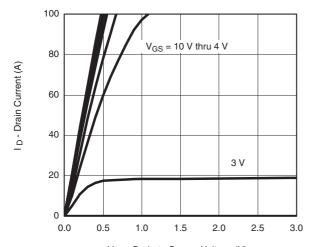
Notes:

- a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Calculated based on maximum junction temperature. Package limitation current is 50 A.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

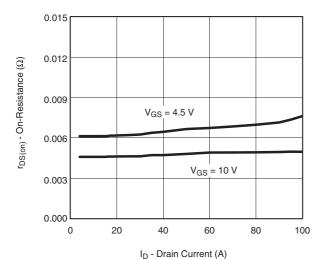


TYPICAL CHARACTERISTICS 25 °C unless noted

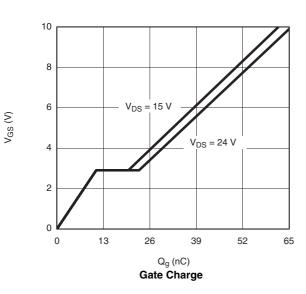


V_{DS} - Drain-to-Source Voltage (V)

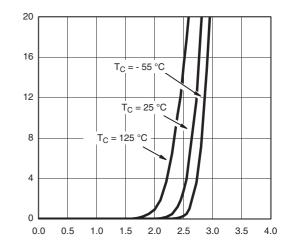
Output Characteristics



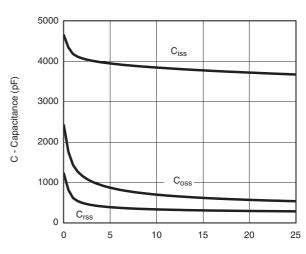
On-Resistance vs. Drain Current



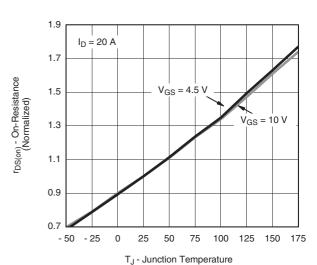
I_D - Drain Current (A)



V_{GS} - Gate-to-Source Voltage (V) **Transfer Characteristics**



V_{DS} - Drain-to-Source Voltage (V) **Capacitance**



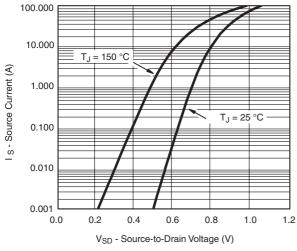
On-Resistance vs. Junction Temperature

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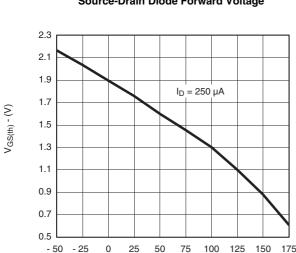
New Product



TYPICAL CHARACTERISTICS 25 °C unless noted

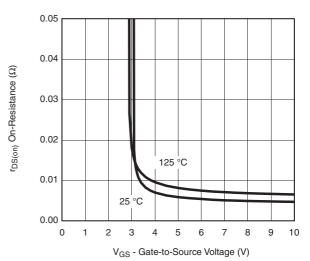


Source-Drain Diode Forward Voltage

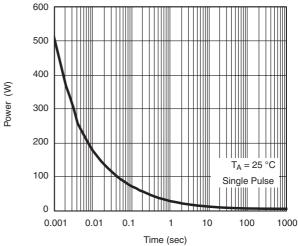


T_J - Temperature (°C) **Threshold Voltage**

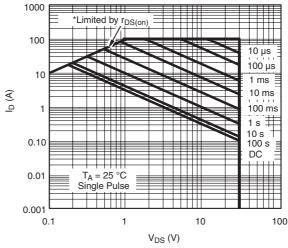
150



r_{DS(on)} vs V_{GS} vs. Temperature



Single Pulse Power, Junction-to-Ambient



 $^*V_{GS}$ > minimum V_{GS} at which $r_{DS(on)}$ is specified Safe Operating Area

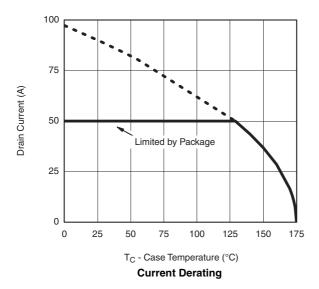
- 50 - 25

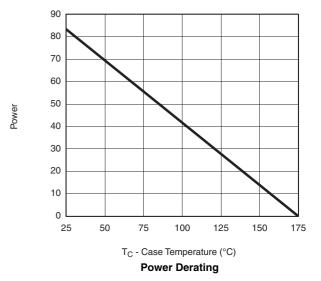




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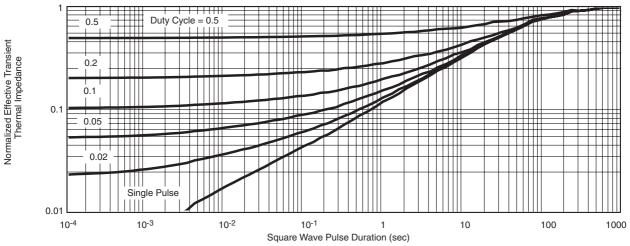
TYPICAL CHARACTERISTICS 25 °C unless noted



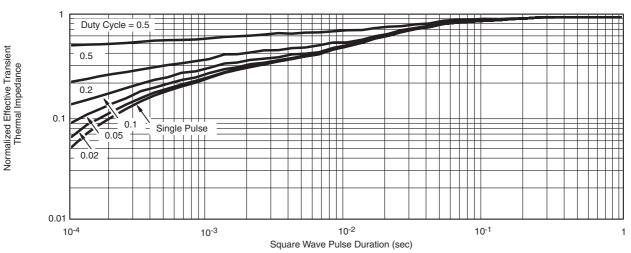




TYPICAL CHARACTERISTICS 25 °C unless noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



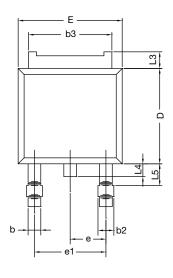
Normalized Thermal Transient Impedance, Junction-to-Case

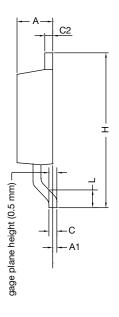
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?73540.



TO-252AA Case Outline

VERSION 1: FACILITY CODE = Y







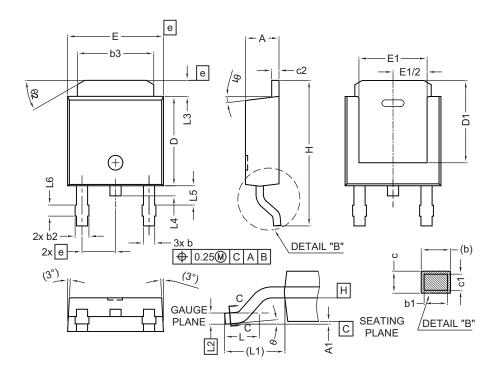
	MILLIMETERS		
DIM.	MIN.	MAX.	
А	2.18	2.38	
A1	-	0.127	
b	0.64	0.88	
b2	0.76	1.14	
b3	4.95	5.46	
С	0.46	0.61	
C2	0.46	0.89	
D	5.97	6.22	
D1	4.10	-	
Е	6.35	6.73	
E1	4.32	=	
Н	9.40	10.41	
е	2.28 BSC		
e1	4.56 BSC		
L	1.40	1.78	
L3	0.89	1.27	
L4	-	1.02	
L5	1.01	1.52	

Note

• Dimension L3 is for reference only



VERSION 2: FACILITY CODE = N



	MILLIMETERS		
DIM.	MIN.	MAX.	
А	2.18	2.39	
A1	-	0.13	
b	0.65	0.89	
b1	0.64	0.79	
b2	0.76	1.13	
b3	4.95	5.46	
С	0.46	0.61	
c1	0.41	0.56	
c2	0.46	0.60	
D	5.97	6.22	
D1	5.21	-	
Е	6.35	6.73	
E1	4.32	-	
е	2.29 BSC		
Н	9.94	10.34	

	MILLIMETERS		
DIM.	MIN.	MAX.	
L	1.50	1.78	
L1	2.74	ref.	
L2	0.51	BSC	
L3	0.89	1.27	
L4	-	1.02	
L5	1.14	1.49	
L6	0.65	0.85	
θ	0°	10°	
θ1	0°	15°	
θ2	25°	35°	

Notes

- Dimensioning and tolerance confirm to ASME Y14.5M-1994
- All dimensions are in millimeters. Angles are in degrees
- Heat sink side flash is max. 0.8 mm
- Radius on terminal is optional

ECN: E22-0399-Rev. R, 03-Oct-2022

DWG: 5347



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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