RoHS

HALOGEN FREE

GREEN

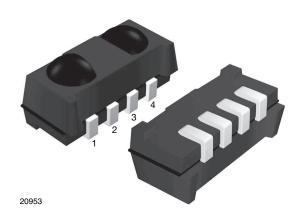
(5-2008)



www.vishay.com

## Vishay Semiconductors

# **IR Receiver Modules for Remote Control Systems**



### **LINKS TO ADDITIONAL RESOURCES**









## **DESCRIPTION**

This product is a SMD IR receiver module for infrared remote control systems. Two PIN diodes and a preamplifier are assembled on a leadframe, the epoxy package contains an IR filter.

The demodulated output signal can be directly connected to a microprocessor for decoding.

The TSOP75G36 is optimized for the usage with RCMM code with low pulse jitter. It can suppress almost all spurious pulses from energy saving fluorescent lamps, LCD backlighting, and plasma TVs.

This component has not been qualified according to automotive specifications.

#### **FEATURES**

- Low output pulse jitter, optimized for RCMM code
- Very low supply current
- · Photo detector and preamplifier in one package
- Internal filter for PCM frequency
- Supply voltage: 2.5 V to 5.5 V
- · Improved immunity against ambient light
- Capable of side or top view
- Two lenses for high sensitivity and wide receiving angle
- Insensitive to supply voltage ripple and noise
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **MECHANICAL DATA**

#### **Pinning:**

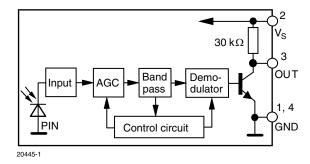
1, 4 = GND,  $2 = V_S$ , 3 = OUT

### **ORDERING CODE**

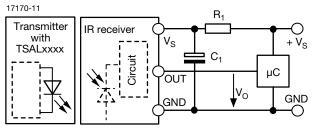
#### Taping:

TSOP75...TT - top view taped, 2200 pcs/reel TSOP75...TR - side view taped, 2300 pcs/reel

#### **BLOCK DIAGRAM**



#### APPLICATION CIRCUIT



 $R_1$  and  $C_1$  recommended to reduce supply ripple for  $V_s < 2.8 \text{ V}$ 



PARTS TABLE				
Carrier frequency	36 kHz	TSOP75G36 (1)		
Package		Heimdall		
Pinning		1, 4 = GND, 2 = V <sub>S</sub> , 3 = OUT		
Dimensions (mm)		6.8 W x 3.0 H x 3.2 D		
Mounting		SMD		
Application		Remote control		
		Extended temperature range: <u>www.vishay.com/doc?82738</u>		
Special options		<ul> <li>Narrow optical filter: <a href="https://www.vishay.com/doc?81590">www.vishay.com/doc?81590</a></li> </ul>		
		Wide optical fiter: <u>www.vishay.com/doc?82726</u>		

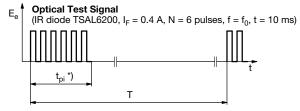
ABSOLUTE MAXIMUM RATINGS					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Supply voltage		V <sub>S</sub>	-0.3 to +6.0	V	
Supply current		I <sub>S</sub>	3	mA	
Output voltage		V <sub>O</sub>	-0.3 to (V <sub>S</sub> + 0.3)	V	
Output current		I <sub>O</sub>	5	mA	
Junction temperature		Tj	100	°C	
Storage temperature range		T <sub>stg</sub>	-25 to +85	°C	
Operating temperature range		T <sub>amb</sub>	-25 to +85	°C	
Power consumption	T <sub>amb</sub> ≤ 85 °C	P <sub>tot</sub>	10	mW	

#### Note

• Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability

<b>ELECTRICAL AND OPTICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage		Vs	2.5	-	5.5	V
Supply current	$E_{V} = 0, V_{S} = 3.3 \text{ V}$	I <sub>SD</sub>	0.27	0.35	0.45	mA
	E <sub>v</sub> = 40 klx, sunlight	I <sub>SH</sub>	-	0.45	-	mA
Transmission distance	$E_{v} = 0$ , test signal see Fig. 1, IR diode TSAL6200, $I_{F} = 50$ mA	d	-	24	-	m
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2$ , test signal see Fig. 1	V <sub>OSL</sub>	-	-	100	mV
Minimum irradiance	Pulse width tolerance: $t_{pi}$ - 1/ $f_0$ < $t_{po}$ < $t_{pi}$ + 3.5/ $f_0$ , test signal see Fig. 1	E <sub>e min.</sub>	-	0.2	0.4	mW/m <sup>2</sup>
Maximum irradiance	$t_{pi}$ - 1/ $f_0$ < $t_{po}$ < $t_{pi}$ + 3.5/ $f_0$ , test signal see Fig. 1	E <sub>e max.</sub>	30	-	-	W/m <sup>2</sup>
Directivity	Angle of half transmission distance	Φ1/2	=	± 50	=	0

## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)



\*) t<sub>pi</sub> ≥ 6/f<sub>0</sub> is recommended for optimal function

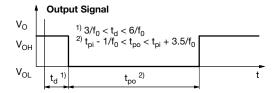


Fig. 1 - Output Active Low

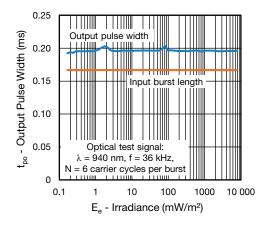


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient

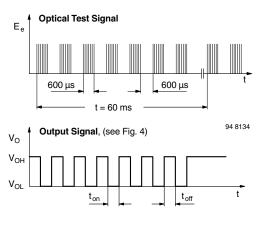


Fig. 3 - Output Function

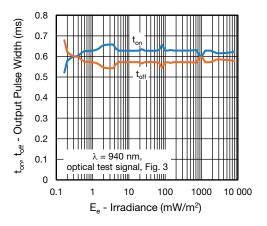


Fig. 4 - Output Pulse Diagram

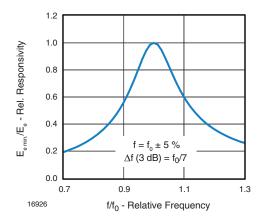


Fig. 5 - Frequency Dependence of Responsivity

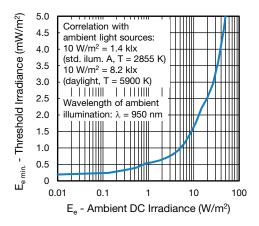


Fig. 6 - Sensitivity in Bright Ambient



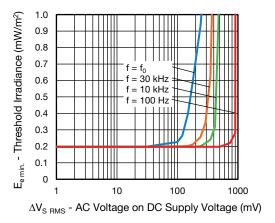


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

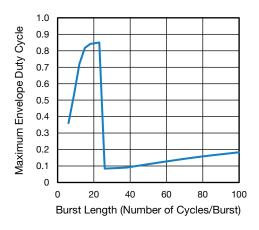


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length

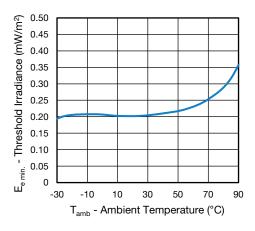


Fig. 9 - Sensitivity vs. Ambient Temperature

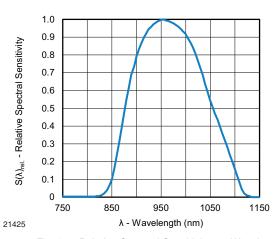


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength

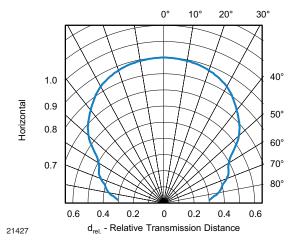


Fig. 11 - Horizontal Directivity

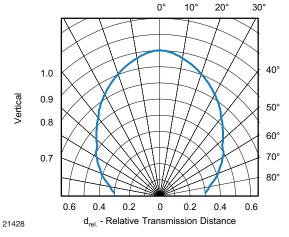


Fig. 12 - Vertical Directivity

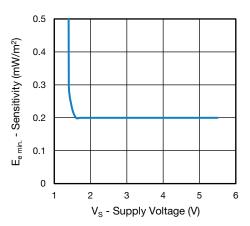


Fig. 13 - Sensitivity vs. Supply Voltage



## **SUITABLE DATA FORMAT**

This series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 36 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the product in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output.

Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- · Continuous signals at any frequency
- Strongly or weakly modulated patterns from fluorescent lamps with electronic ballasts (see Fig. 14 or Fig. 15)

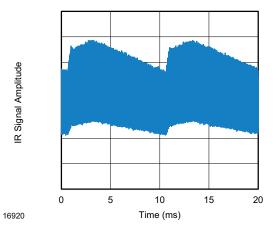


Fig. 14 - IR Disturbance from Fluorescent Lamp With Low Modulation

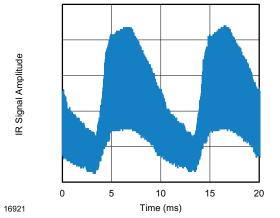
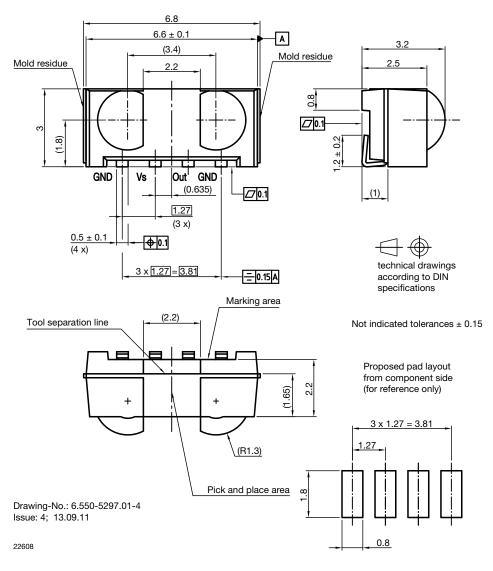


Fig. 15 - IR Disturbance from Fluorescent Lamp With High Modulation



## **PACKAGE DIMENSIONS** in millimeters



## **ASSEMBLY INSTRUCTIONS**

#### **Reflow Soldering**

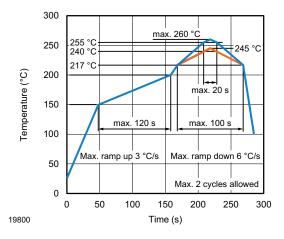
- Reflow soldering must be done within 72 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

## **Manual Soldering**

- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- · Handle products only after the temperature has cooled off

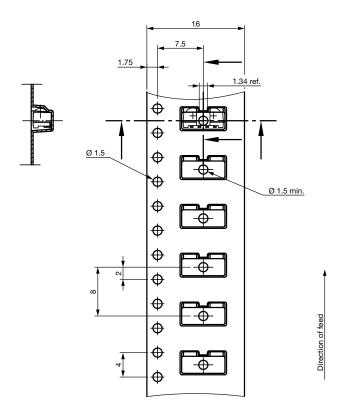


## **VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE**



## TAPING VERSION TSOP..TR DIMENSIONS in millimeters





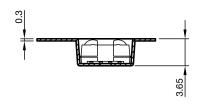
Drawing-No.: 9.700-5337.01-4

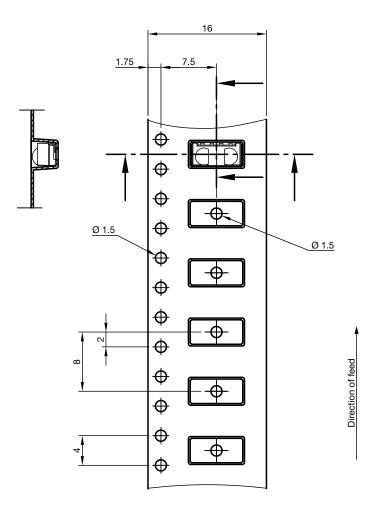
Issue: 2; 06.10.15





## TAPING VERSION TSOP..TT DIMENSIONS in millimeters



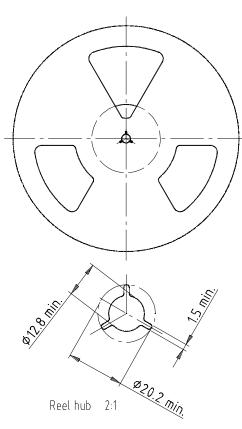


Drawing-No.: 9.700-5338.01-4

Issue: 4; 12.06.13

technical drawings according to DIN specifications

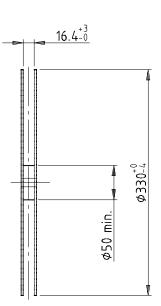
## **REEL DIMENSIONS** in millimeters



Drawing-No.: 9.800-5052.V2-4

Issue: 1; 07.05.02

16734



Form of the leave open of the wheel is supplier specific.

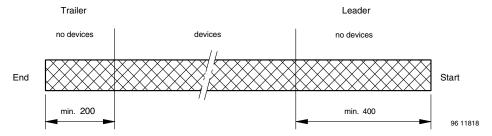
Dimension acc. to IEC EN 60 286-3

Tape width 16



technical drawings according to DIN specifications

## **LEADER AND TRAILER DIMENSIONS** in millimeters



## **COVER TAPE PEEL STRENGTH**

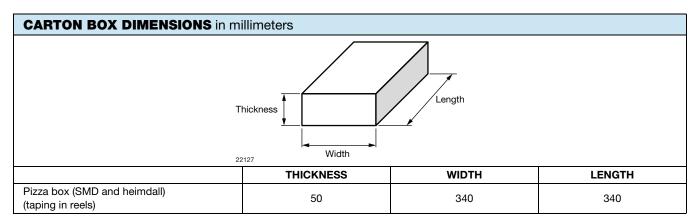
According to DIN EN 60286-3 0.1 N to 1.3 N  $300 \pm 10$  mm/min.  $165^{\circ}$  to  $180^{\circ}$  peel angle





### **OUTER PACKAGING**

The sealed reel is packed into a pizza box.



#### **LABEL**

## Standard bar code labels for finished goods

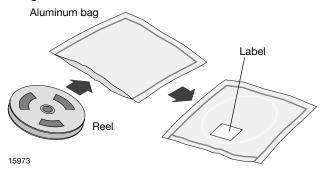
The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

VISHAY SEMICONDUCTOR GmbH STANDARD BAR CODE PRODUCT LABEL (finished goods)				
PLAIN WRITING	ABBREVIATION	LENGTH		
Item-description	-	18		
Item-number	INO	8		
Selection-code	SEL	3		
LOT-/serial-number	BATCH	10		
Data-code	COD	3 (YWW)		
Plant-code	PTC	2		
Quantity	QTY	8		
Accepted by	ACC	-		
Packed by	PCK	-		
Mixed code indicator	MIXED CODE	-		
Origin	XXXXXXX+	Company logo		
LONG BAR CODE TOP	TYPE	LENGTH		
Item-number	N	8		
Plant-code	N	2		
Sequence-number	X	3		
Quantity	N	8		
Total length	-	21		
SHORT BAR CODE BOTTOM	TYPE	LENGTH		
Selection-code	Х	3		
Data-code	N	3		
Batch-number	X	10		
Filter	-	1		
Total length	-	17		



## **DRY PACKING**

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



#### **FINAL PACKING**

The sealed reel is packed into a cardboard box.

#### RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 72 h under these conditions moisture content will be too high for reflow soldering.

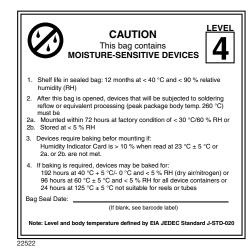
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC® standard J-STD-020 level 4 label is included on all dry bags.



EIA JEDEC standard J-STD-020 level 4 label is included on all dry bags

#### **ESD PRECAUTION**

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

# VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



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Vishay

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