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Vishay Semiconductors

RoHS

COMPLIANT

HALOGEN FREE

GREEN

High Speed Infrared Emitting Diodes, 850 nm, Surface Emitter Technology



LINKS TO ADDITIONAL RESOURCES



DESCRIPTION

As part of the <u>SurfLight</u>TM portfolio, the VSMY2850 series are infrared, 850 nm emitting diodes based on GaAlAs surface emitter chip technology with extreme high radiant intensities, high optical power and high speed, m

olded in clear, untinted plastic packages (with lens) for surface mounting (SMD).

FEATURES

Package type: surface-mountPackage form: GW, RGW



• Peak wavelength: λ_p = 850 nm

· High reliability

· High radiant power

· Very high radiant intensity

• Angle of half intensity: $\varphi = \pm 10^{\circ}$

· Suitable for high pulse current operation

· Terminal configurations: gullwing or reverse gullwing

Package matches with detector VEMD2500X01 series

Floor life: 4 weeks, MSL 2a, according to J-STD-020

 Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

APPLICATIONS

- · Miniature light barrier
- Photointerrupters
- · Optical switch
- Emitter source for proximity sensors
- IR illumination
- Smart metering

PRODUCT SUMMARY				
COMPONENT	I _e (mW/sr)	φ (°)	λ _P (nm)	t _r (ns)
VSMY2850RG	125	± 10	850	10
VSMY2850G	125	± 10	850	10

Note

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VSMY2850RG	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Reverse gullwing		
VSMY2850G	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Gullwing		

Note

· MOQ: minimum order quantity



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V _R	5	V
Forward current		I _F	100	mA
Peak forward current	$t_p/T = 0.5, t_p = 100 \mu s$	I _{FM}	200	mA
Surge forward current	t _p = 100 μs	I _{FSM}	1	Α
Power dissipation		P _V	190	mW
Junction temperature		Tj	100	°C
Operating temperature range		T _{amb}	-40 to +85	°C
Storage temperature range		T _{stg}	-40 to +100	°C
Soldering temperature	According to Fig. 10, J-STD-020	T _{sd}	260	°C
Thermal resistance junction to ambient	EIA / JESD51	R _{thJA}	250	K/W

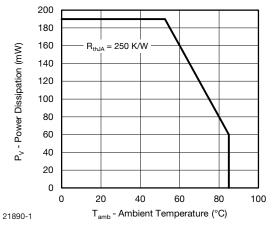


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

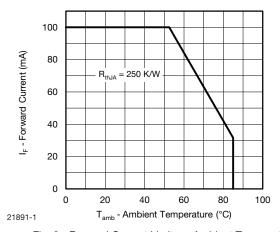


Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	V_{F}	-	1.6	1.9	V
	$I_F = 1 \text{ A}, t_p = 100 \mu \text{s}$	V _F	-	2.8	-	V
Temperature coefficient of V _F	I _F = 100 mA	TK _{VF}	-	-1.5	-	mV/K
Reverse current		I _R	Not designed for reverse operation μA		μΑ	
Junction capacitance	$V_R = 0 \text{ V}, f = 1 \text{ MHz}, E = 0 \text{ mW/cm}^2$	CJ	-	50	-	pF
Radiant intensity	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	l _e	70	125	210	mW/sr
	$I_F = 1 \text{ A}, t_p = 100 \mu \text{s}$	l _e	-	1000	-	mW/sr
Radiant power	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	фе	-	55	-	mW
Temperature coefficient of radiant power	I _F = 100 mA	TKφ _e	-	-0.12	-	%/K
Angle of half intensity		φ	-	± 10	-	0
Peak wavelength	I _F = 100 mA	λ_{p}	840	850	870	nm
Spectral bandwidth	I _F = 30 mA	Δλ	-	30	-	nm
Temperature coefficient of λ_p	I _F = 30 mA	TKλ _p	-	0.25	-	nm/K
Rise time	I _F = 100 mA, 10 % to 90 %	t _r	-	10	-	ns
Fall time	I _F = 100 mA, 10 % to 90 %	t _f	-	10	-	ns

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

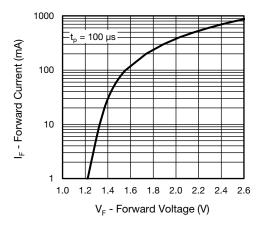


Fig. 3 - Forward Current vs. Forward Voltage

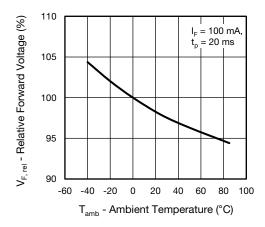


Fig. 4 - Relative Forward Voltage vs. Ambient Temperature

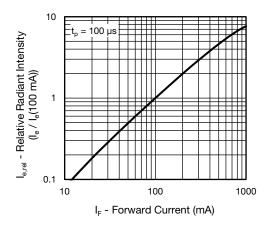


Fig. 5 - Relative Radiant Intensity vs. Forward Current

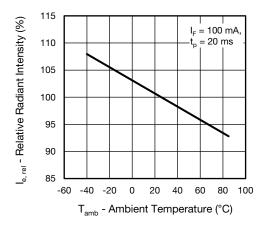


Fig. 6 - Relative Radiant Intensity vs. Ambient Temperature

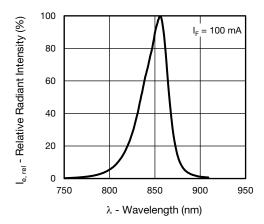


Fig. 7 - Relative Radiant Intensity vs. Wavelength

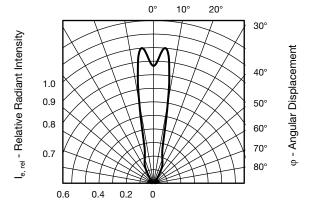


Fig. 8 - Relative Radiant Intensity vs. Angular Displacement

VSMY2850RG, VSMY2850G

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SOLDER PROFILE

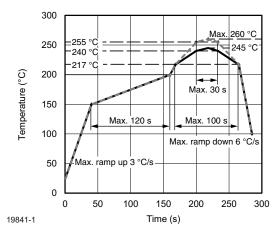


Fig. 9 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 4 weeks

Conditions: T_{amb} < 30 °C, RH < 60 %

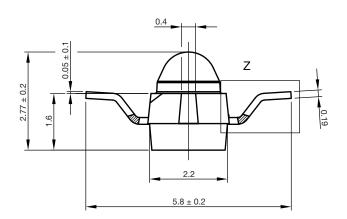
Moisture sensitivity level 2a, according to J-STD-020.

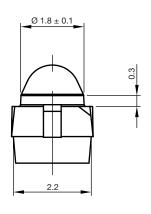
DRYING

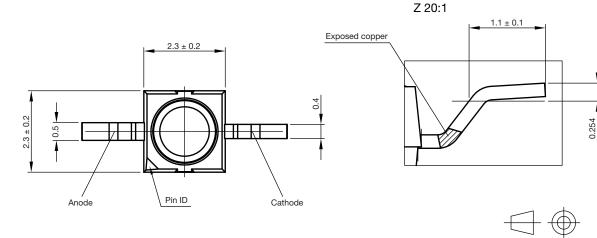
In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 $^{\circ}$ M.

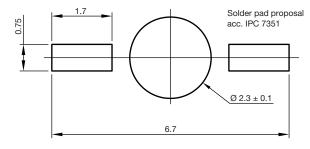


PACKAGE DIMENISONS in millimeters: VSMY2850RG





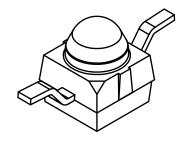




Not indicated tolerances ± 0.1

technical drawings

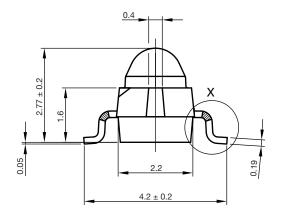
according to DIN specifications

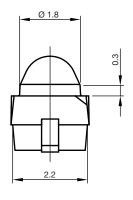


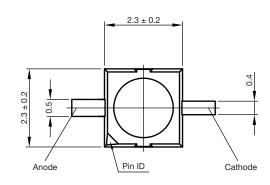
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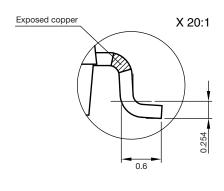
Issue: 1; 18.03.10

PACKAGE DIMENSIONS in millimeters: VSMY2850G

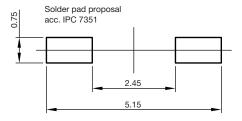




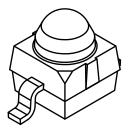








Not indicated tolerances \pm 0.1

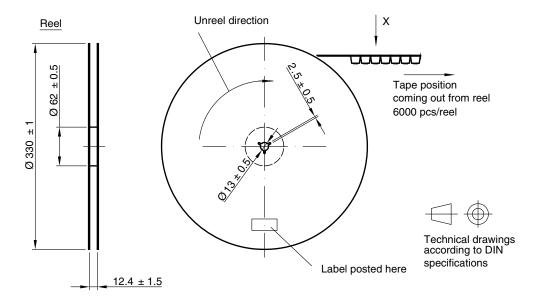


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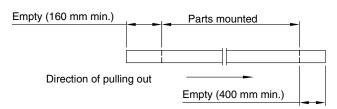
Issue: 1; 18.03.10



TAPING AND REEL DIMENSIONS in millimeters: VSMY2850RG

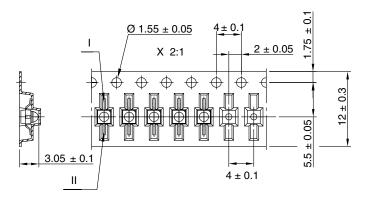


Leader and trailer tape:



Terminal position in tape

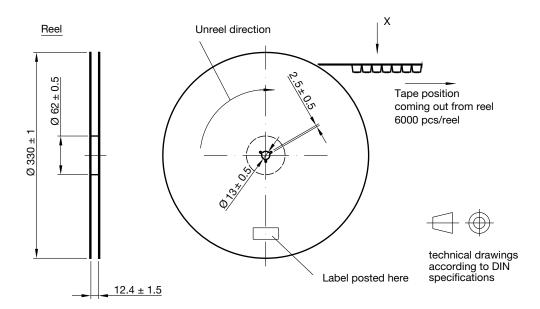
Device	Lead I	Lead II
VEMT2000		
VEMT2500	Collector	Emitter
VEMD2000		
VEMD2500	0-4	Al -
VSMB2000	Cathode	Anode
VSMG2000		
VSMY2850RG	Anode	Cathode



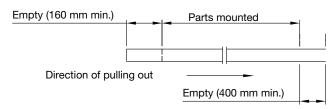
Drawing-No.: 9.800-5100.01-4

Issue: 2; 18.03.10

TAPING AND REEL DIMENSIONS in millimeters: VSMY2850G

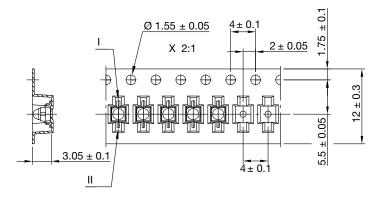


Leader and trailer tape:



Terminal position in tape

Device	Lead I	Lead II	
VEMT2020	Collector	tor Emitter	
VEMT2520	Collector		
VSMB2020		Anode	
VSMG2020	Cathode		
VEMD2020	Calliode		
VEMD2520			
VSMY2850G	Anode	Cathode	
VSMB294008GC	Allode	Califode	



Drawing-No.: 9.800-5091.01-4

Issue: 3; 18.03.10



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