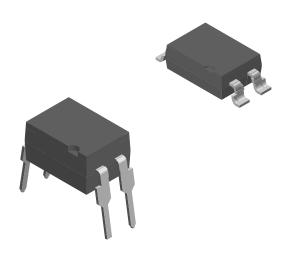
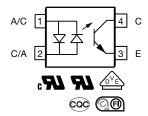


# Optocoupler, Phototransistor Output, AC Input, 5300 V<sub>RMS</sub>





#### LINKS TO ADDITIONAL RESOURCES









#### **DESCRIPTION**

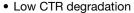
The SFH620AA, SFH620AGB features a high current transfer ratio, low coupling capacitance and high isolation voltage. These couplers have a GaAs infrared emitting diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a plastic DIP-4 package.

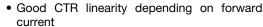
The coupling devices are designed for signal transmission between two electrically separated circuits.

The couplers are end-stackable with 2.54 mm lead spacing. This version complies with IEC 60950 (DIN VDE 0805) for reinforced insulation up to an operation voltage of 400  $V_{RMS}$  or DC.

#### **FEATURES**

- High current transfer ratios
  - at 5 mA: 50 to 600 %
  - at 1.0 mA: 45 % typical (> 13)







- Isolation test voltage, 5300 V<sub>RMS</sub>
- High collector emitter voltage, V<sub>CEO</sub> = 70 V
- Low saturation voltage
- Fast switching times
- Temperature stable
- Low coupling capacitance
- End stackable, 0.100" (2.54 mm) spacing
- High common mode interference immunity (unconnected base)
- SMD option, see SFH620A, SFH6206 datasheet
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

### **AGENCY APPROVALS**

- <u>UL</u>
- cUL
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- BSI
- CQC
- FIMKO

## SFH620AA, SFH620AGB

## Vishay Semiconductors

ORDERING INFORMATION						
S F H 6 2 0 PART NUMBER	A X X - X 0 PACKAGE O	0 1 SMD-4 SMD-4 PTION -> 8 mm				
AGENCY CERTIFIED / PACKAGE	CTR (%)					
AGENCY CERTIFIED / FACRAGE	± 5 mA					
UL, cUL, BSI, FIMKO, CQC	50 to 600	100 to 600				
DIP-4	SFH620AA	SFH620AGB				
UL, cUL, BSI, FIMKO, CQC, VDE (option 1)	50 to 600	100 to 600				
DIP-4	-	SFH620AGB-X001				
SMD-4, option 9	-	SFH620AGB-X019T				

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
DC forward current		I <sub>F</sub>	± 60	mA		
Surge forward current	t <sub>p</sub> ≤ 1.0 μs	I <sub>FSM</sub>	± 2.5	Α		
Total power dissipation		P <sub>diss</sub>	100	mW		
ОUТРUТ						
Collector emitter voltage		$V_{CE}$	70	V		
Emitter collector voltage		V <sub>EC</sub>	7	V		
Collector current		I <sub>C</sub>	50	mA		
Collector current	t <sub>p</sub> ≤ 1.0 ms	I <sub>C</sub>	100	mA		
Power dissipation	·	P <sub>diss</sub>	150	mW		
COUPLER						
Isolation test voltage between emitter and detector		$V_{ISO}$	5300	V <sub>RMS</sub>		
Creepage distance			≥7	mm		
Clearance distance			≥7	mm		
Insulation thickness between emitter and detector			≥ 0.4	mm		
Comparative tracking index per DIN IEC 112/VDE 0303 part 1		СТІ	≥ 175			
In della constate and	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω		
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω		
Storage temperature		T <sub>stg</sub>	-55 to +150	°C		
Ambient temperature		T <sub>amb</sub>	-55 to +100	°C		
Junction temperature		T <sub>i</sub>	100	°C		
Soldering temperature (1)	Max. 10 s, dip soldering distance to seating plane ≥ 1.5 mm	T <sub>sld</sub>	260	°C		

### Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT								
Forward voltage	$I_F = \pm 60 \text{ mA}$		$V_{F}$	-	1.25	1.65	V	
Capacitance	V <sub>R</sub> = 0 V, f = 1 MHz		Co	-	50	-	pF	
Thermal resistance			R <sub>thja</sub>	-	750	-	K/W	
OUTPUT	OUTPUT							
Collector emitter capacitance	V <sub>CE</sub> = 5 V, f = 1 MHz		C <sub>CE</sub>	-	6.8	-	pF	
Thermal resistance			R <sub>thja</sub>	-	500	-	K/W	
COUPLER								
Collector emitter saturation voltage	$I_F = \pm 10 \text{ mA}, I_C = 2.5 \text{ mA}$		V <sub>CEsat</sub>	-	0.25	0.4	V	
Coupling capacitance			C <sub>C</sub>	-	0.2	-	pF	
Collector emitter leakage current	V 10.V	SFH620AA	I <sub>CEO</sub>	-	10	100	nA	
Collector emitter leakage current	V <sub>CE</sub> = 10 V	SFH620AGB	I <sub>CEO</sub>	-	10	100	nA	

#### Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
1.4	1 - 1 5 m/s V/ - 5 V/	SFH620AA	CTR	50	-	600	%
I <sub>C</sub> /I <sub>F</sub>	$I_F = \pm 5 \text{ mA}, V_{CE} = 5 \text{ V}$	SFH620AGB	CIR	100	-	600	%

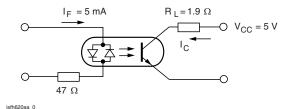


Fig. 1 - Switching Times (Typical Values) Linear Operation (Saturated)

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION SYMBOL MIN. TYP. MAX. UNIT					UNIT
Turn-on time	$I_F = \pm 5$ mA, $R_L = 1.9$ k $\Omega$ , $V_{CC} = 5$ V	t <sub>on</sub>	-	2	ı	μs
Turn-off time	$I_F = \pm 5$ mA, $R_L = 1.9$ k $\Omega$ , $V_{CC} = 5$ V	t <sub>off</sub>	-	25	-	μs

SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Climatic classification (according to IEC 68 part 1)			-	55 / 100 / 21	-		
Comparative tracking index		CTI	175	-	399		
V <sub>IOTM</sub>			10000	-	-	V	
V <sub>IORM</sub>			890	-	-	V	
P <sub>SO</sub>			-	-	400	mW	
I <sub>SI</sub>			-	-	275	mA	
T <sub>SI</sub>			-	-	175	°C	
Creepage distance	standard DIP-4		7	-	-	mm	
Clearance distance	standard DIP-4		7	-	-	mm	
Insulation thickness, reinforced rated	per IEC 60950 2.10.5.1		0.4	-	-	mm	

#### Note

## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

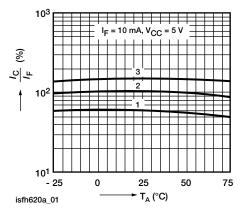


Fig. 2 - Current Transfer Ratio (CTR) vs. Temperature

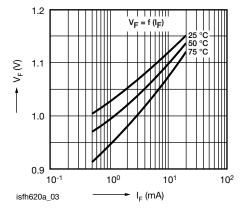


Fig. 4 - Diode Forward Voltage (Typ.) vs. Forward Current

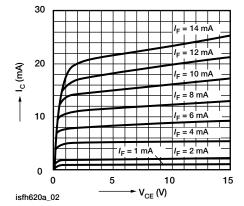


Fig. 3 - Output Characteristics (Typ.) Collector Current vs. Collector Emitter Voltage

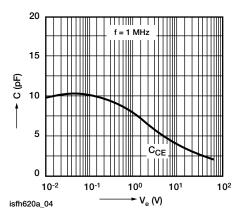


Fig. 5 - Transistor Capacitance (Typ.) vs. Collector Emitter Voltage

As per IEC 60747-5-5, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

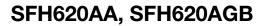




Fig. 7 - Permissible Power Dissipation vs. Ambient Temperature

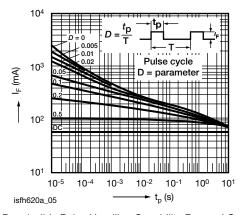
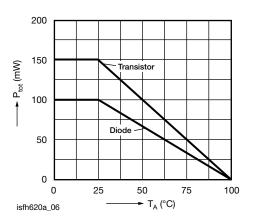


Fig. 6 - Permissible Pulse Handling Capability Forward Current vs. Pulse Width



**PACKAGE DIMENSIONS** in millimeters

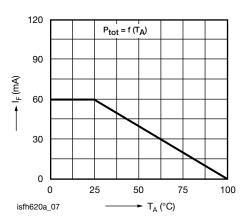
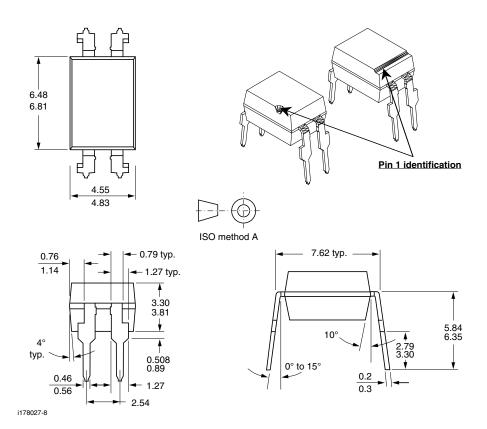


Fig. 8 - Permissible Diode Forward Current vs. **Ambient Temperature** 

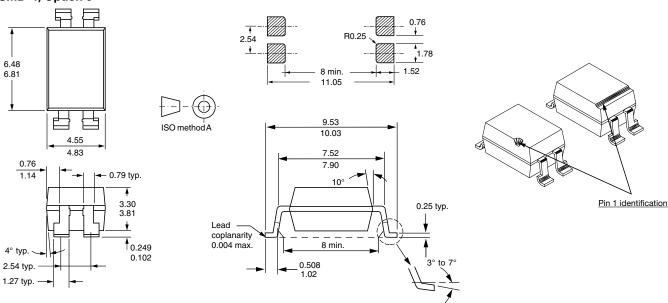
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# Vishay Semiconductors

DIP-4



## SMD-4, Option 9

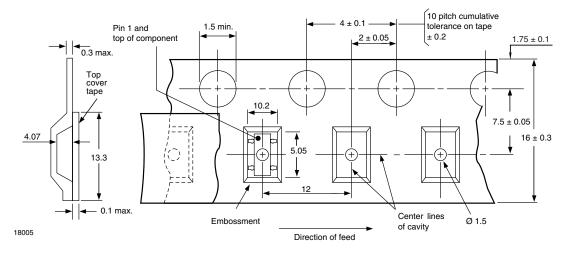




## TAPE AND REEL PACKAGING (in millimeters)

The tape is 16 mm and is wound on a 33 cm reel. There are 1000 parts per reel. Taped and reeled 4 pin optocouplers conform to EIA-481-2 and IEC60286-3.

#### SMD-4 ("T")



## PACKAGE MARKING (example of SFH620AGB-X001)



#### Notes

- XXXX = LMC (lot marking code)
- The VDE logo is only marked on option1 parts.



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Vishay

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