





<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
<b>INPUT</b>				
Reverse voltage		$V_R$	4.5	V
Forward continuous current		$I_F$	25	mA
Peak forward current	$t = 1\text{ ms}$ , duty cycle 50 %	$I_{FM}$	50	mA
Maximum surge forward current	$t \leq 1\text{ }\mu\text{s}$ , 300 pulses/s	$I_{FSM}$	1	A
Derate linearly from 25 °C			0.6	mW/°C
Power dissipation	$T_{amb} \leq 70\text{ }^{\circ}\text{C}$	$P_{diss}$	50	mW
<b>OUTPUT</b>				
Supply voltage		$V_S$	-0.5 to 30	V
Output voltage		$V_O$	-0.5 to 25	V
Collector output current		$I_{CO}$	8	mA
Derate linearly from 25 °C			1.33	mW/°C
Power dissipation	$T_{amb} \leq 70\text{ }^{\circ}\text{C}$	$P_{diss}$	50	mW
<b>COUPLER</b>				
Isolation test voltage	$t = 1\text{ min}$	$V_{ISO}$	5300	$V_{RMS}$
Pollution degree (DIN VDE0109)			2	
Creepage distance			$\geq 8$	mm
Clearance distance			$\geq 8$	mm
Derate linearly from 25 °C			1.93	mW/°C
Total package dissipation		$P_{tot}$	145	mW
Comparative tracking index per DIN IEC112/VDE0303 part 1, group IIIa per DIN VDE6110			175	
Isolation resistance	$V_{IO} = 500\text{ V}$ , $T_{amb} = 25\text{ }^{\circ}\text{C}$	$R_{IO}$	$\geq 10^{12}$	$\Omega$
	$V_{IO} = 500\text{ V}$ , $T_{amb} = 100\text{ }^{\circ}\text{C}$	$R_{IO}$	$\geq 10^{11}$	$\Omega$
Storage temperature range		$T_{stg}$	-55 to +150	°C
Ambient temperature range		$T_{amb}$	-55 to +100	°C
Soldering temperature <sup>(1)</sup>	max. 10 s, dip soldering distance to seating plane $\geq 1.5\text{ mm}$	$T_{sld}$	260	°C

**Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
- <sup>(1)</sup> Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).



ELECTRICAL CHARACTERISTICS							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
<b>INPUT</b>							
Forward voltage	$I_F = 16 \text{ mA}$		$V_F$	-	1.33	1.9	V
Breakdown voltage	$I_R = 10 \text{ }\mu\text{A}$		$V_{BR}$	4.5	-	-	V
Reverse current	$V_R = 4.5 \text{ V}$		$I_R$	-	0.5	10	$\mu\text{A}$
Capacitance	$V_R = 0 \text{ V}, f = 1 \text{ MHz}$		$C_O$	-	30	-	pF
Temperature coefficient of forward voltage	$I_F = 16 \text{ mA}$		$\Delta V_F / \Delta T_{amb}$	-	-1.7	-	mV/°C
<b>OUTPUT</b>							
Logic low supply current	$I_F = 16 \text{ mA}, V_O = \text{open}, V_{CC} = 4.5 \text{ V}$		$I_{CCL}$	-	100	200	$\mu\text{A}$
Supply current, logic high	$I_F = 0 \text{ mA}, V_O = \text{open}, V_{CC} = 15 \text{ V}$		$I_{CCH}$	-	0.01	4	$\mu\text{A}$
Logic low output voltage	$I_F = 16 \text{ mA}, V_{CC} = 4.5 \text{ V}, I_O = 1.1 \text{ mA}$	SFH6325	$V_{OL}$	-	0.1	0.5	V
	$I_F = 16 \text{ mA}, V_{CC} = 4.5 \text{ V}, I_O = 3 \text{ mA}$	SFH6326	$V_{OL}$	-	0.1	0.5	V
Logic high output current	$I_F = 0 \text{ mA}, V_O = V_{CC} = 5.5 \text{ V}$		$I_{OH}$	-	3	500	nA
	$I_F = 0 \text{ mA}, V_O = V_{CC} = 15 \text{ V}$		$I_{OH}$	-	-	50	$\mu\text{A}$
Channel to channel <sup>(1)</sup> crosstalk	$I_F = 16 \text{ mA}, V_O = V_{CC} = 5.5 \text{ V}$		$I_{OH-XT}$	-	-	500	nA
<b>COUPLER</b>							
Capacitance (input to output)	$f = 1 \text{ MHz}$		$C_{IO}$	-	0.6	-	pF

**Notes**

- $T_{amb} = 0 \text{ }^\circ\text{C}$  to  $70 \text{ }^\circ\text{C}$ , unless otherwise specified, typical values  $T_{amb} = 25 \text{ }^\circ\text{C}$
  - Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements
- <sup>(1)</sup> To measure crosstalk, turn on the LED for channel 1 and the output current for channel 2 in logic high. Repeat for channel 2

CURRENT TRANSFER RATIO ( $T_{amb} = 25 \text{ }^\circ\text{C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio	$I_F = 16 \text{ mA}, V_{CC} = 4.5 \text{ V}, V_O = 0.4 \text{ V}, T_{amb} = 25 \text{ }^\circ\text{C}$	SFH6325	CTR	7	16	-	%
		SFH6326	CTR	19	35	-	%
	$I_F = 16 \text{ mA}, V_{CC} = 4.5 \text{ V}, V_O = 0.5 \text{ V}, T_{amb} = 0 \text{ }^\circ\text{C}$ to $70 \text{ }^\circ\text{C}$	SFH6325	CTR	5	-	-	%
		SFH6326	CTR	15	-	-	%

SWITCHING CHARACTERISTICS ( $T_{amb} = 25 \text{ }^\circ\text{C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
High to low	$I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 4.1 \text{ k}\Omega$	SFH6325	$t_{PHL}$	-	0.3	1.5	$\mu\text{s}$
	$I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1.9 \text{ k}\Omega$	SFH6326	$t_{PHL}$	-	0.2	0.8	$\mu\text{s}$
Low to high	$I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 4.1 \text{ k}\Omega$	SFH6325	$t_{PLH}$	-	0.6	1.5	$\mu\text{s}$
	$I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1.9 \text{ k}\Omega$	SFH6326	$t_{PLH}$	-	0.5	0.8	$\mu\text{s}$

COMMON MODE TRANSIENT IMMUNITY ( $T_{amb} = 25 \text{ }^\circ\text{C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
CMTI at logic high level output	$I_F = 0 \text{ mA}, C_{CM} = 10 \text{ V}_{P-P}, V_{CC} = 5 \text{ V}, R_L = 4.1 \text{ k}\Omega$	SFH6325	$CM_H$	-	1000	-	V/ $\mu\text{s}$
	$I_F = 0 \text{ mA}, C_{CM} = 10 \text{ V}_{P-P}, V_{CC} = 5 \text{ V}, R_L = 1.9 \text{ k}\Omega$	SFH6326	$CM_H$	-	1000	-	V/ $\mu\text{s}$
CMTI at logic low level output	$I_F = 16 \text{ mA}, C_{CM} = 10 \text{ V}_{P-P}, V_{CC} = 5 \text{ V}, R_L = 4.1 \text{ k}\Omega$	SFH6325	$CM_L$	-	1000	-	V/ $\mu\text{s}$
	$I_F = 16 \text{ mA}, C_{CM} = 10 \text{ V}_{P-P}, V_{CC} = 5 \text{ V}, R_L = 1.9 \text{ k}\Omega$	SFH6326	$CM_L$	-	1000	-	V/ $\mu\text{s}$

**TYPICAL CHARACTERISTICS** ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)

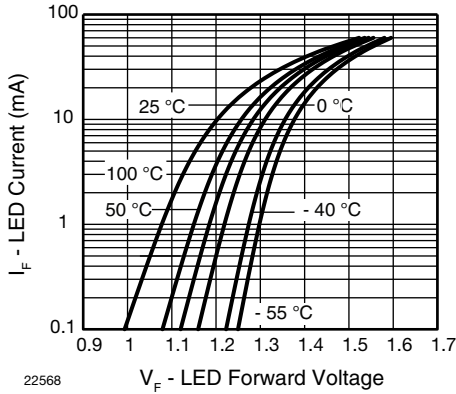


Fig. 1 - LED Forward Current vs. Forward Voltage

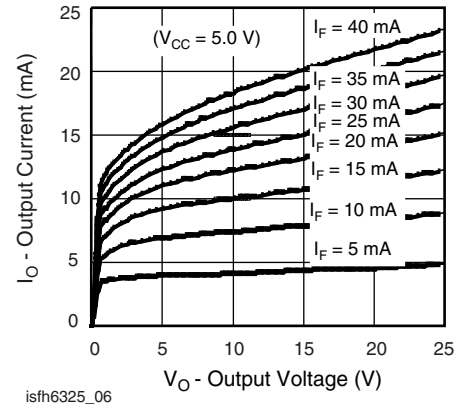


Fig. 4 - Output Current vs. Output Voltage

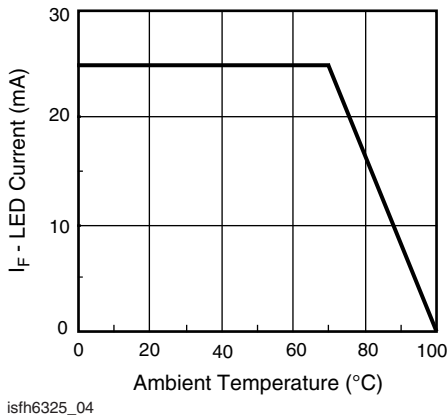


Fig. 2 - Permissible Forward LED Current vs. Temperature

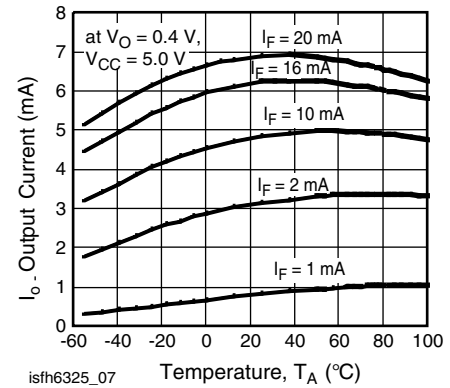


Fig. 5 - Output Current vs. Temperature

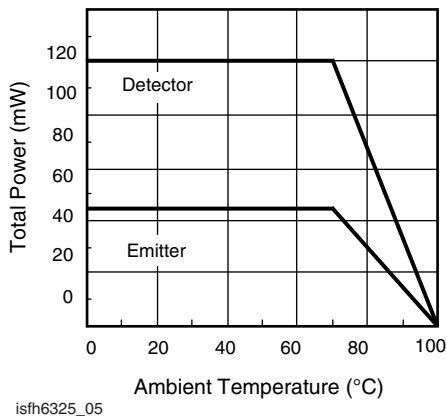


Fig. 3 - Permissible Power Dissipation vs. Temperature

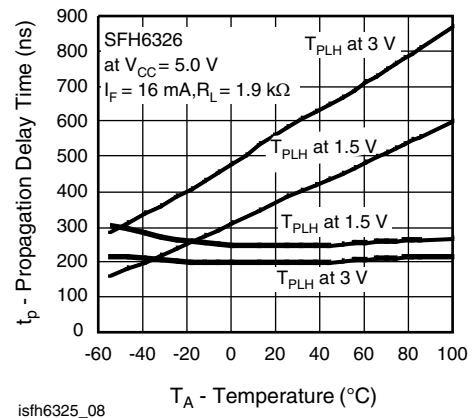


Fig. 6 - Propagation Delay vs. Ambient Temperature

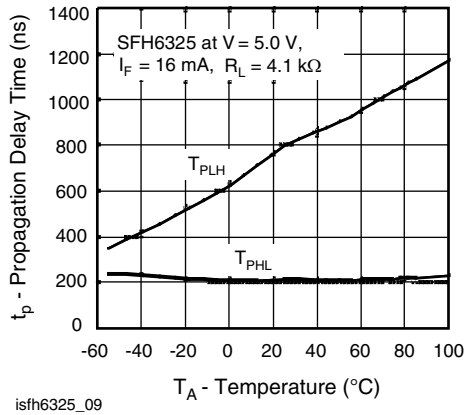


Fig. 7 - Propagation Delay vs. Ambient Temperature

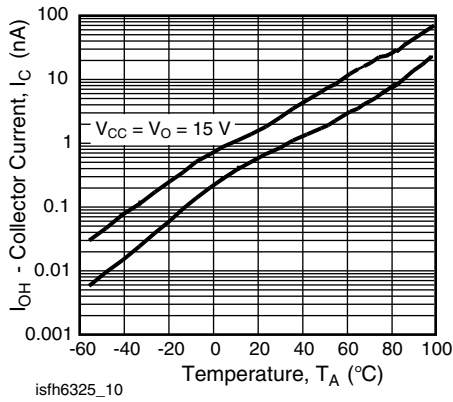


Fig. 8 - Logic High Output Current vs. Temperature

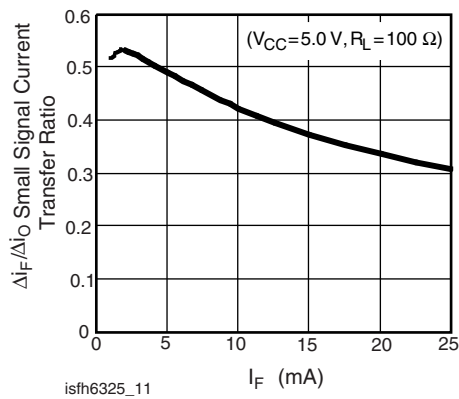


Fig. 9 - Small Signal Current Transfer Ratio vs. Input Current

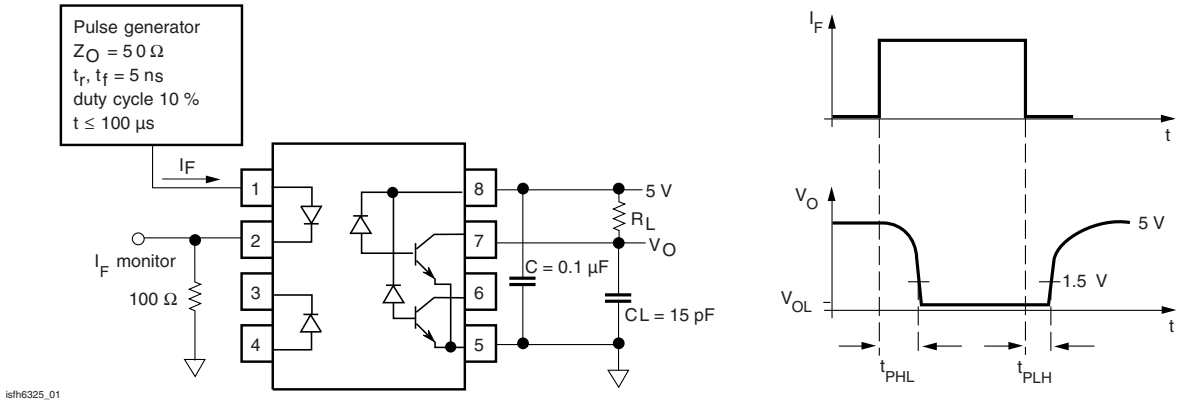


Fig. 10 - Switching Time and Test Circuit

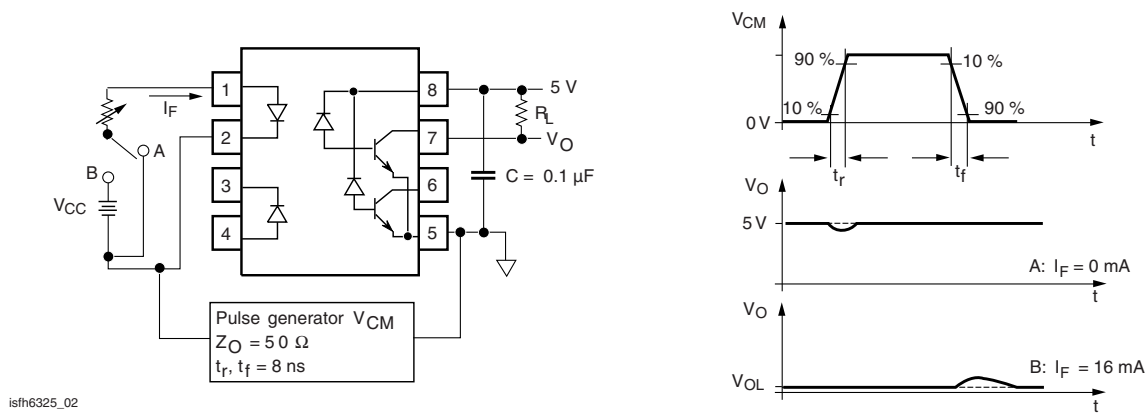
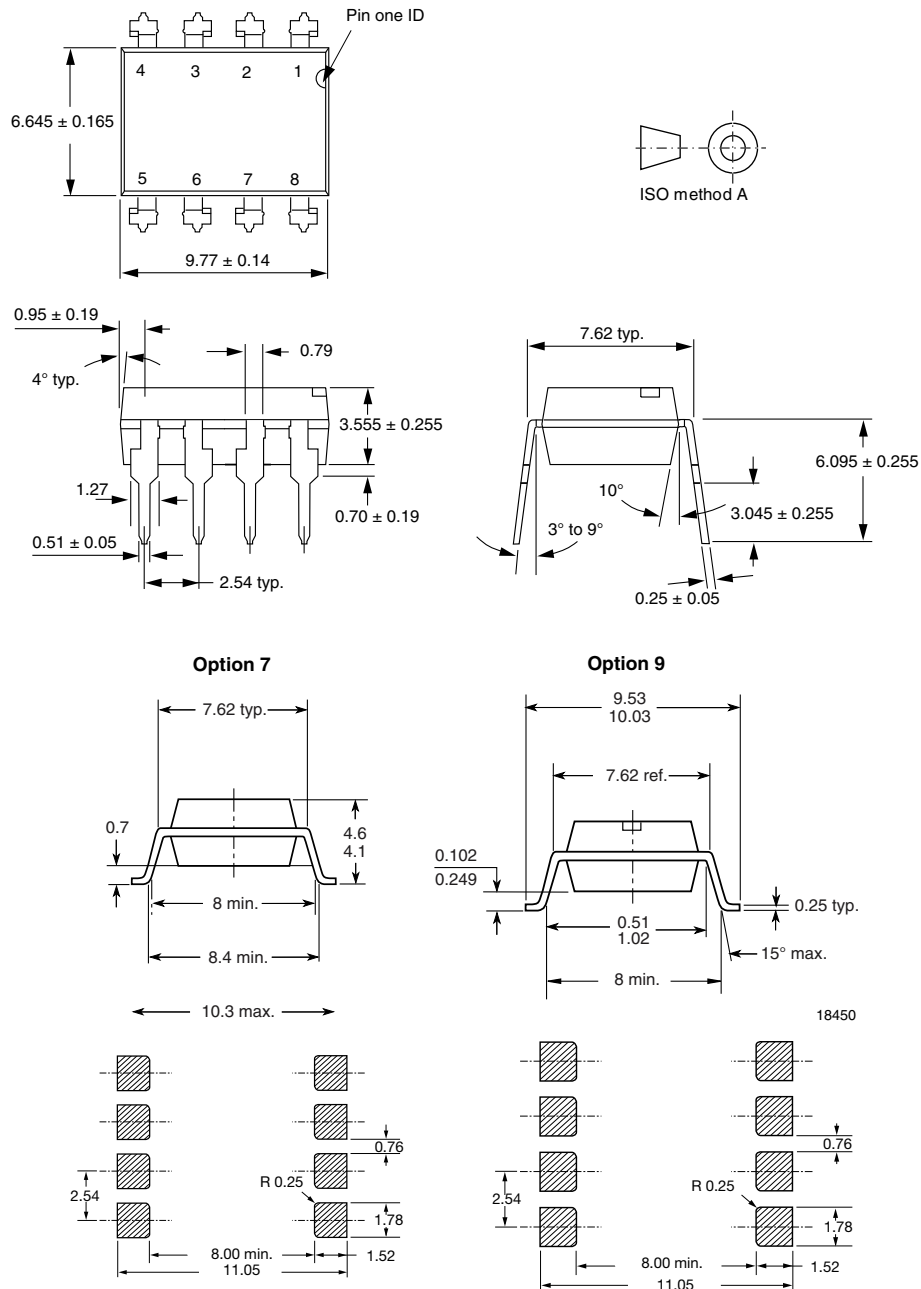
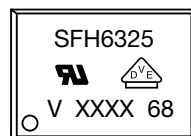


Fig. 11 - Waveform and Test Circuit for Common Mode Transient Immunity

## PACKAGE DIMENSIONS in millimeters



## PACKAGE MARKING (example)



### Notes

- XXXX = LMC (lot marking code)
- The VDE Logo is only marked on option1 parts
- Tape and reel suffix (T) is not part of the package marking



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