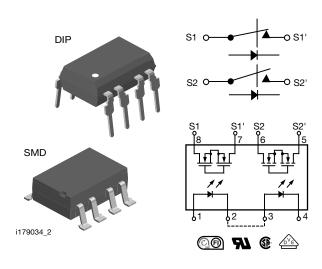


Vishay Semiconductors

Dual 1 Form A/B, C Solid-State Relay



LINKS TO ADDITIONAL RESOURCES

	SPICE	30			
Design Tools	Models	3D Models			

DESCRIPTION

The LH1502 relays contain normally open and normally closed switches that can be used independently as a 1 form A and 1 form B relay, or when used together, as a 1 form C relay. The relays are constructed as a multi-chip hybrid device. Actuation control is via an infrared LED. The output switch is a combination of a photodiode array with MOSFET switches and control circuity.

FEATURES

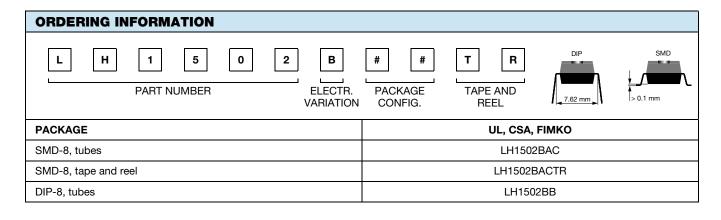
- Current limit protection
- Isolation test voltage 3750 V_{RMS}
- Typical R_{ON} 20 Ω
- Load voltage 350 V
- Load current 150 mA
- High surge capability
- Clean bounce free switching
- Low power consumption
- · SMD lead available on tape and reel
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- General telecom switching
 - On / off hook control
- Ring delay
- Dial pulse
- Ground start
- Ground fault protection
- Instrumentation
- Industrial controls

AGENCY APPROVALS

- <u>UL</u>
- <u>VDE</u>
- <u>CQC</u>
- FIMKO





RoHS

COMPLIAN



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ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
LED continuous forward current		I _F	50	mA
LED reverse voltage	I _R ≤ 10 μA	V _R	8	V
OUTPUT				
DC or peak AC load voltage	$I_L \le 50 \ \mu A$	VL	350	V
Continuous DC load current (form C operation)		ΙL	150	mA
Peak load current, form A	t = 100 ms	IР	(3)	
Peak load current (single shot), form B		l _P	350	mA
SSR				
Ambient operating temperature range		T _{amb}	-40 to +85	°C
Storage temperature range		T _{stg}	-40 to +125	°C
Pin soldering temperature ⁽¹⁾	t = 10 s max.	T _{sld}	260	°C
Input to output isolation test voltage	t = 1 s, I _{ISO} = 10 μA max.	V _{ISO}	3750	V _{RMS}
Pole-to-pole isolation voltage (S1 to S2) ⁽²⁾ , (dry air, dust free, at sea level)			1600	V
Output power dissipation (continuous)		P _{diss}	600	mW

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

⁽¹⁾ Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP)

⁽²⁾ Breakdown occurs between the output pins external to the package

⁽³⁾ Refer to current limit performance application note for a discussion on relay operation during transient currents

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						•
LED forward current, switch turn-on (NO)	I _L = 100 mA, t = 10 ms	I _{Fon}	-	0.6	2	mA
LED forward current, switch turn-off (NO)	$V_L = \pm 300 V$	I _{Foff}	0.4	0.5	-	mA
LED forward current, switch turn-on (NC)	I _L = 300 mA, t = 10 ms	I _{Fon}	0.2	0.9	-	mA
LED forward current, switch turn-off (NC)	$V_L = \pm 300 V$	I _{Foff}	-	1	2	mA
LED forward voltage	I _F = 10 mA	V _F	1.15	1.26	1.45	V
OUTPUT						•
On-resistance (NO, NC)	$I_{\rm F}=5~{\rm mA}~({\rm NO}),~I_{\rm F}=0~{\rm mA}~({\rm NC}),\\ I_{\rm L}=50~{\rm mA}~({\rm NC})$	R _{ON}	12	20	25	Ω
Off-resistance (NO)	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	R _{OFF}	0.35	5000	-	GΩ
Off-resistance (NC)	$I_{\rm F} = 5 \text{ mA}, V_{\rm L} = \pm 100 \text{ V}$	R _{OFF}	0.1	1.4	-	GΩ
Current limit (NO)	$I_F = 5 \text{ mA}, \text{ t} = 5 \text{ ms}, \text{ V}_L = \pm 5 \text{ V}$	I _{LMT}	270	290	380	mA
Off-state leakage current (NO)	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	Ι _Ο	-	0.02	1000	nA
Off-state leakage current (NC)	$I_F = 5 \text{ mA}, V_L = \pm 100 \text{ V}$	Ι _Ο	-	0.07	1	μA
Off-state leakage current (NO, NC)	$I_{\rm F} = 0 \text{ mA (NO), } I_{\rm F} = 5 \text{ mA (NC),} \\ V_{\rm L} = \pm 200 \text{ V}$	Ι _Ο	-	-	1	μA
Output capacitance (NO)	$I_F = 0 \text{ mA}, V_L = 50 \text{ V}$	Co	-	50	-	pF
Output capacitance (NC)	$I_{F} = 5 \text{ mA}, V_{L} = 50 \text{ V}$	Co	-	50	-	pF
TRANSFER				•	•	
Capacitance (input to output)	V _{ISO} = 1 V	C _{IO}	-	3	-	pF

Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements



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SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
TRANSFER						
Turn-on time (NO)	$I_F = 10 \text{ mA}, I_L = 37.5 \text{ mA}, V_L = 150 \text{ V}$	t _{on}	0.2	3.2	6	ms
Turn-on time (NC)	$I_F = 10 \text{ mA}, I_L = 37.5 \text{ mA}, V_L = 150 \text{ V}$	t _{on}	0.2	3.8	6	ms
Turn-off time (NO)	$I_F = 10 \text{ mA}, I_L = 37.5 \text{ mA}, V_L = 150 \text{ V}$	t _{off}	-	1.6	3	ms
Turn-off time (NC)	$I_F = 10 \text{ mA}, I_L = 37.5 \text{ mA}, V_L = 150 \text{ V}$	t _{off}	-	0.8	3	ms

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

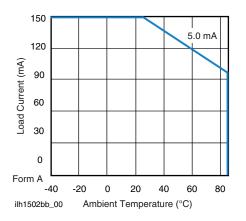


Fig. 1 - Maximum Load Current vs. Ambient Temperature

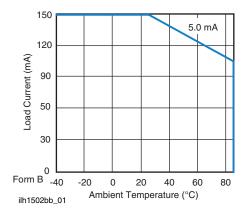


Fig. 2 - Maximum Load Current vs. Ambient Temperature

3

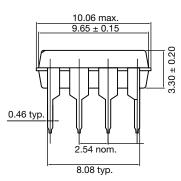


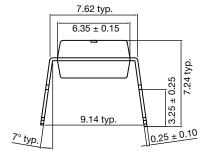
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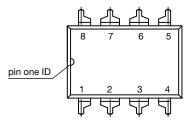
PACKAGE DIMENSIONS (in millimeters)

DIP-8

SMD-8

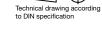


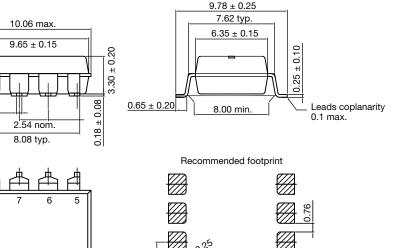




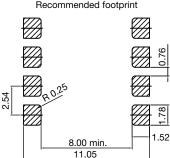


10.06 max 9.65 ± 0.15 0.46 <u>typ</u>



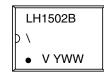


pin one ID





PACKAGE MARKING (example)



Note

Tape and reel suffix (TR) is not part of the package marking ٠

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SOLDER PROFILES

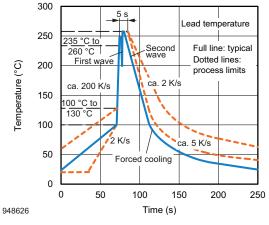


Fig. 3 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP Devices

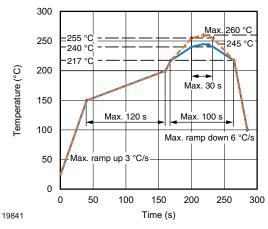
HANDLING AND STORAGE CONDITIONS

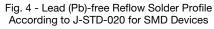
ESD level: HBM class 2

Floor life: unlimited

Conditions: T_{amb} < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020





Rev. 1.7, 13-Oct-2020

5



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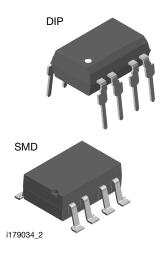
Footprint and Schematic Information for LH1502BAC, LH1502BACTR, LH1502BB

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

Note that the 3D models for these parts can be found on the Vishay product page.

PART NUMBER	FOOTPRINT / SCHEMATIC		
LH1502BAC	www.snapeda.com/parts/LH1502BAC/Vishay/view-part		
LH1502BACTR	www.snapeda.com/parts/LH1502BACTR/Vishay/view-part		
LH1502BB	www.snapeda.com/parts/LH1502BB/Vishay/view-part		

For technical issues and product support, please contact optocoupleranswers@vishay.com.





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