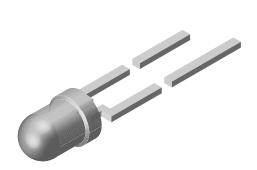
HALOGEN FREE

**GREEN** 

## Vishay Semiconductors

# High Speed Infrared Emitting Diode, 940 nm, Surface Emitter Technology



#### **DESCRIPTION**

As part of the SurfLight<sup>TM</sup> portfolio, the VSLY3943 is a high speed infrared emitting diode based on surface emitter technology, molded in a blue-gray plastic package.

### **FEATURES**

Package type: leaded

• Package form: T-1, clear epoxy

• Dimensions: Ø 3 mm

• Peak wavelength:  $\lambda_p = 940 \text{ nm}$ 

• High speed

· High radiant power

• High radiant intensity

• Angle of half intensity:  $\phi = \pm 17^{\circ}$ 

· Low forward voltage

Good spectral matching to Si photodetectors

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>



· Infrared remote control units

• Free air transmission systems

· Infrared source for optical counters and card readers

PRODUCT SUMMARY					
COMPONENT	I <sub>e</sub> (mW/sr)	φ <b>(°)</b>	$\lambda_{\mathbf{p}}$ (nm)	t <sub>r</sub> (ns)	
VSLY3943	70	± 17	940	5	

#### Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VSLY3943	Bulk	MOQ: 5000 pcs, 5000 pcs/bulk	T-1		
VSLY3943-MSZ	Ammopack	MOQ: 10 000 pcs, 2000 pcs/box	T-1		

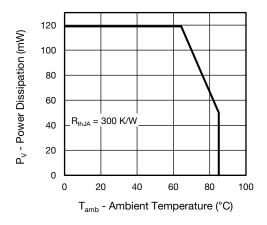
#### Note

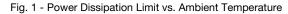
MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V <sub>R</sub>	5	V
Forward current		I <sub>F</sub>	70	mA
Peak forward current	$t_p/T = 0.1, t_p = 100 \mu s$	I <sub>FM</sub>	140	mA
Surge forward current	t <sub>p</sub> = 100 μs	I <sub>FSM</sub>	500	mA
Power dissipation		P <sub>V</sub>	160	mW
Junction temperature		T <sub>j</sub>	100	°C
Operating temperature range		T <sub>amb</sub>	-40 to +85	°C
Storage temperature range		T <sub>stg</sub>	-40 to +100	°C
Soldering temperature	$t \le 5$ s, 2 mm from case	T <sub>sd</sub>	260	°C
Thermal resistance junction-to-ambient	J-STD-051, leads 7 mm, soldered on PCB	R <sub>thJA</sub>	300	K/W



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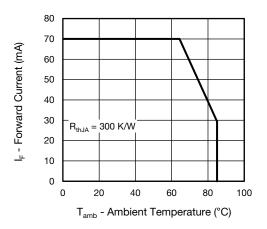


Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 70 \text{ mA}, t_p = 20 \text{ ms}$	V <sub>F</sub>	-	1.5	1.7	V
	$I_F = 500 \text{ mA}, t_p = 100 \mu \text{s}$	V <sub>F</sub>	-	2.6	-	V
Temperature coefficient of V <sub>F</sub>	I <sub>F</sub> = 50 mA	TK <sub>VF</sub>	-	-0.7	-	mV/K
Reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	Not designed for reverse operation			μΑ
Junction capacitance	$V_R = 0 \text{ V, f} = 1 \text{ MHz,}$ $E = 0 \text{ mW/cm}^2$	CJ	-	30	-	pF
Radiant intensity	$I_F = 70 \text{ mA}, t_p = 20 \text{ ms}$	l <sub>e</sub>	32	70	120	mW/sr
	$I_F = 500 \text{ mA}, t_p = 100 \mu \text{s}$	l <sub>e</sub>	-	650	-	mW/sr
Radiant power	$I_F = 70 \text{ mA}, t_p = 20 \text{ ms}$	фе	-	40	-	mW
Temperature coefficient of radiant power	I <sub>F</sub> = 50 mA	TK <sub>φe</sub>	-	-0.2	-	%/K
Angle of half intensity		φ	-	± 17	-	0
Peak wavelength	I <sub>F</sub> = 50 mA	$\lambda_{p}$	-	940	-	nm
Spectral bandwidth	I <sub>F</sub> = 70 mA	Δλ	-	55	-	nm
Temperature coefficient of Ip	I <sub>F</sub> = 70 mA	$TK_{\lambdap}$	-	0.28	-	nm
Rise time	I <sub>F</sub> = 70 mA, 10 % to 90 %	t <sub>r</sub>	-	5	-	ns
Fall time	I <sub>F</sub> = 70 mA, 10 % to 90 %	t <sub>f</sub>	-	6	-	ns



### BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

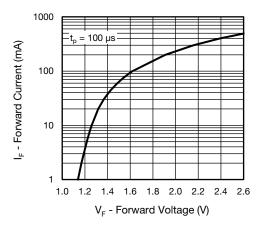


Fig. 3 - Forward Current vs. Forward Voltage

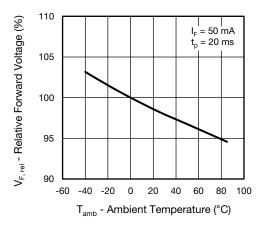


Fig. 4 - Relative Forward Voltage vs. Ambient Temperature

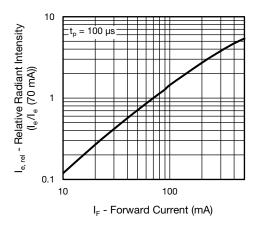


Fig. 5 - Relative Radiant Intensity vs. Forward Current

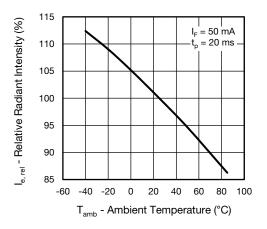


Fig. 6 - Relative Radiant Intensity vs. Ambient Temperature

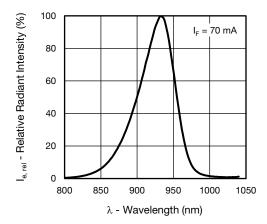


Fig. 7 - Relative Radiant Intensity vs. Wavelength

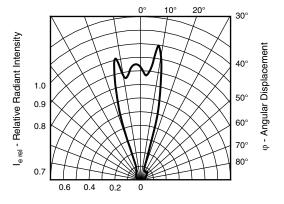
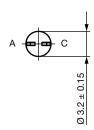
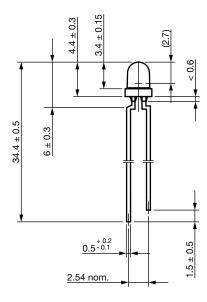


Fig. 8 - Relative Radiant Intensity vs. Angular Displacement

# Vishay Semiconductors

### **PACKAGE DIMENSIONS** in millimeters





Area not plane

Ø 2.9 ± 0.15

0.4-0.05

technical drawings according to DIN specifications

Drawing-No.: 6.541-5118.01-4

Issue: 1; 13.12.17



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Vishay

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