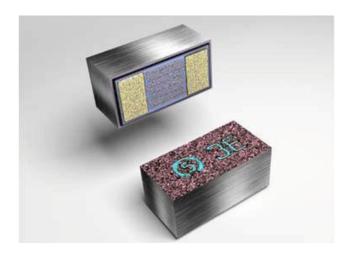
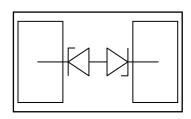


Vishay Semiconductors

# **Bidirectional Symmetrical ESD Protection Device in CLP0603**





#### PSpice model for subcircuit VCUT03E1-SD0:

- \* Vishay Semiconductor GmbH
- \* Claus Mähner, October 2017

### .SUBCKT VCUT03E1 1 2

Df1 c 1 D1

Dr1 1 c D2

Df2 c 2 D1

Dr2 2 c D2

.MODEL D1 D

+IS=1.02E-12

+N=1.41

+ISR=1.00E-16

+NR=1000

+RS=0.075

+IKF=100

+CJO=2.65E-11

+M=0.26

+VJ=0.53

+BV=7.5

+IBV=0.001

+NBV=1 +IBVL=0

+NBVL=1

+TT=0

.MODEL D2 D

+IS=6E-12

+N=32

+ISR=0

+NR=100

+RS=0

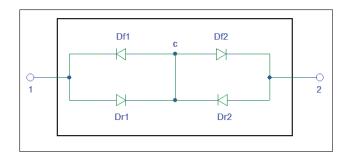
+IKF=1000000

#### .ENDS VCUT03E1

#### Remarks:

RS = 75 m $\Omega$  is the physical series resistance of the diode without heating

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