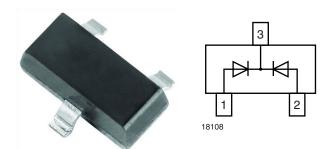
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Small Signal Switching Diode, Dual



LINKS TO ADDITIONAL RESOURCES



MECHANICAL DATA

Case: SOT-23 Weight: approx. 9.2 mg Packaging codes / options: 18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

FEATURES

- Silicon epitaxial planar diode
- Fast switching dual diode with common cathode
- AEC-Q101 qualified available (part number on request)
- Molding compound meets UL 94 V-0 flammability rating
- Moisture sensitivity level (MSL) 1
- Base P/N-G3 green, commercial grade
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

PARTS TABLE						
PART	ORDERING CODE	AEC-Q101 QUALIFIED	TYPE MARKING	CIRCUIT CONFIGURATION	TAPED UNITS PER REEL	MINIMUM ORDER QUANTITY
BAV70-G	BAV70-G3-08	no	no JJG Common cathode 3 000 (8 mm tape on 7" reel) 10 000 (8 mm tape on 13" reel)	Common cathode		15 000
BAVI0-G	BAV70-G3-18	no		10 000		

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Peak reverse voltage		V _{RRM}	70	V	
Reverse voltage		V _R	70	V	
Forward current (continuous) ⁽¹⁾		I _F	350	mA	
Non repetitive peak forward current ⁽¹⁾	t _p = 1 μs	I _{FSM}	2	А	
	t _p = 1 ms	I _{FSM}	1	А	
	t _p = 1 s	I _{FSM}	0.5	А	
Devuer disain ation	on FR-4 board with recommended soldering footprint	D	270	mW	
Power dissipation	Infinite heatsink	P _{tot}	390		

Note

⁽¹⁾ Infinite heatsink

THERMAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air	according to JEDEC [®] 51-3 on FR-4 board with recommended soldering footprint	R _{thJA}	460	K/W		
Thermal resistance junction to lead	Infinite heatsink	R _{thJL}	320	K/W		
Junction temperature		Tj	150	°C		
Storage temperature range		T _{stg}	-65 to +150	°C		
Operating temperature range		T _{op}	-55 to +150	°C		

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RoHS

COMPLIANT

HALOGEN

FREE

GREEN (5-2008)



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ELECTRICAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	MAX.	UNIT	
	I _F = 1 mA	V _F	0.715	V	
Forward voltage	I _F = 10 mA	V _F	0.855	V	
Forward voltage	I _F = 50 mA	V _F	1	V	
	I _F = 150 mA	V _F	1.25	V	
	V _R = 70 V	I _R	100	nA	
Reverse current	V _R = 70 V, T _j = 150 °C	I _R	50	μA	
	V _R = 25 V, T _j = 150 °C	I _R	30	μA	
Diode capacitance	$V_R = 0 V, f = 1 MHz$	CD	1.5	pF	
Reverse recovery time	I_F = 10 mA to i_R = 1 mA, V_R = 6 V, R_L = 100 Ω	t _{rr}	6	ns	

TYPICAL CHARACTERISICS (Tamb = 25 °C, unless otherwise specified)

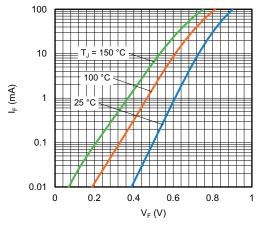


Fig. 1 - Forward Current vs. Forward Voltage

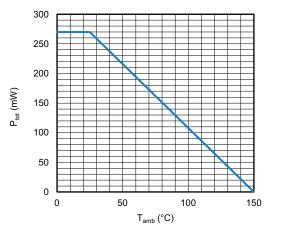
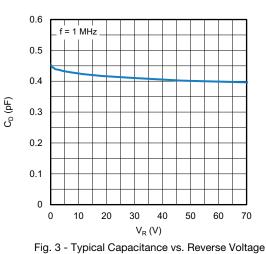


Fig. 2 - Admissible Power Dissipation vs. Ambient Temperature



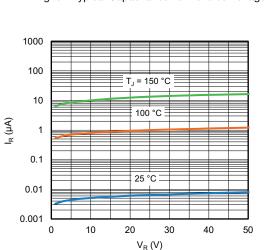


Fig. 4 - Typical Reverse Leakage Current vs. Reverse Voltage

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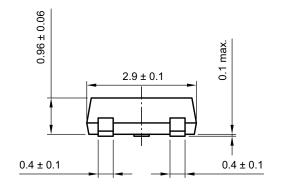
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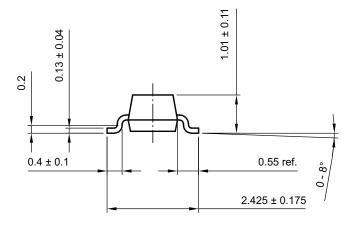
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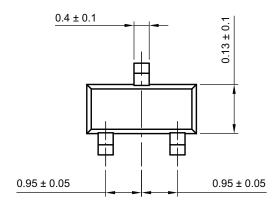
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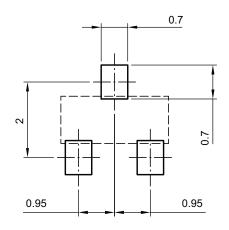
PACKAGE DIMENSIONS in millimeters: SOT-23







footprint recommendation:

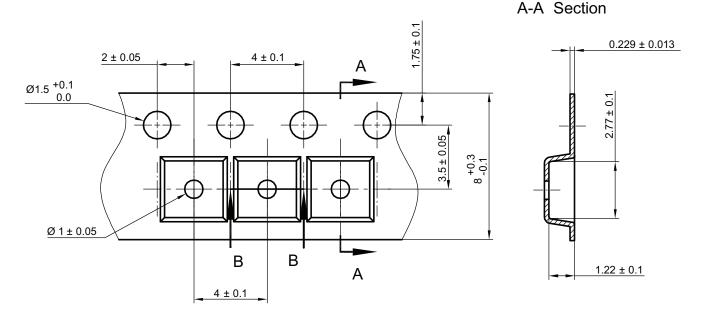


Created - Date: 18-Oct-2021 Rev. 01 - Date: 18-Jan-2022 S8-V-3929.01-009 (4)

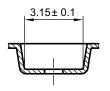


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CARRIER TAPE SOT-23

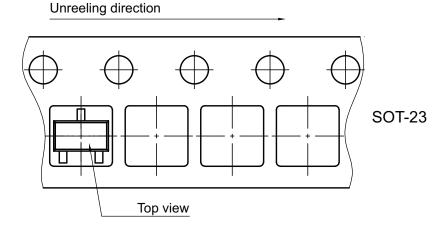


B-B Section



Created Date: 04-Feb-2010 Rev. Date: 07-Feb-2022 S8-V-3929.01-005 (4)

ORIENTATION IN CARRIER TAPE SOT-23



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