Vishay General Semiconductor

# **Dual High Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.49$  V at  $I_F = 5$  A



PIN 2 CASE

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 15 A			
V <sub>RRM</sub>	120 V			
I <sub>FSM</sub>	160 A			
V <sub>F</sub> at I <sub>F</sub> = 15 A (125 °C)	0.66 V			
T <sub>J</sub> max.	150 °C			
Package	TO-220AB			
Diode variation	Common cathode			

### **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- · High efficiency operation
- HALOGEN • Solder bath temperature 275 °C maximum, 10 s, FREE per JESD 22-B106
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **TYPICAL APPLICATIONS**

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

#### **MECHANICAL DATA**

Case: TO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V30120CI	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	120	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	30	A	
	per diode		15		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	160	A	
Operating junction temperature range		T <sub>J</sub> <sup>(1)</sup>	-40 to +150	- °C	
Storage temperature range		T <sub>STG</sub>	-55 to +150		

Note

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction to ambient:  $dP_D/dT_J < 1/R_{0,JA}$ 



RoHS COMPLIANT

V30120CI



V30120CI



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.55	-	V
	I <sub>F</sub> = 7.5 A			0.62	-	
	I <sub>F</sub> = 15 A			0.82	0.90	
	$I_F = 5 A$	T <sub>A</sub> = 125 °C		0.49	-	
	I <sub>F</sub> = 7.5 A			0.55	-	
	I <sub>F</sub> = 15 A			0.66	0.74	
Reverse current per diode	V <sub>B</sub> = 90 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	0.01	-	mA
	$v_{\rm R} = 90 v$	T <sub>A</sub> = 125 °C		5.0	-	
	V <sub>R</sub> = 120 V	T <sub>A</sub> = 25 °C		-	0.5	
		T <sub>A</sub> = 125 °C		11.0	25	
Junction capacitance	4 V, 1MHz		CJ	1300	_	pF

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

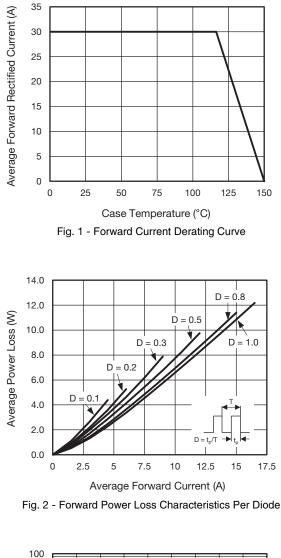
<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	V30120CI	UNIT		
Typical thermal resistance per device	$R_{\theta JC}$	1.8	°C/W		

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V30120CI-M3/P	1.88	Р	50/tube	Tube		



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### **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)



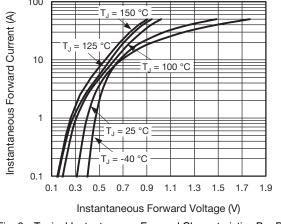


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

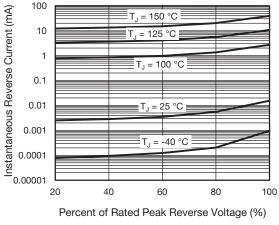
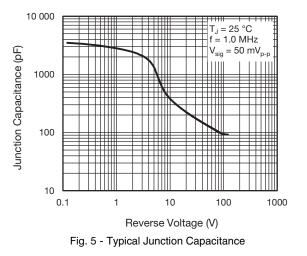


Fig. 4 - Typical Reverse Characteristics Per Diode



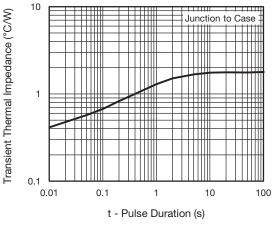


Fig. 6 - Typical Transient Thermal Impedance Per Device

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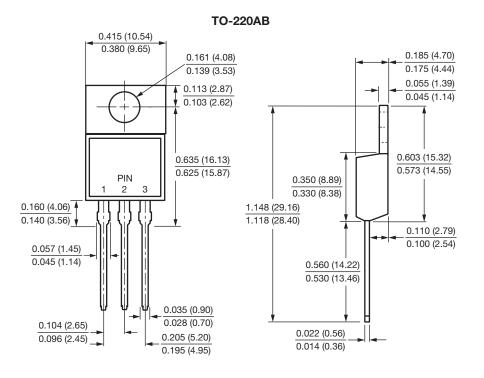
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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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