Vishay General Semiconductor

Dual High Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.36$ V at $I_F = 5$ A

TMBS[®] TO-220AB PIN 2

CASE

PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 20 A			
V _{RRM}	100 V			
I _{FSM}	250 A			
V _F at I _F = 20 A (125 °C)	0.59 V			
T _J max.	150 °C			
Package	TO-220AB			
Circuit configuration	Common cathode			

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- · High efficiency operation
- HALOGEN • Solder bath temperature 275 °C maximum, 10 s, FREE per JESD 22-B106
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V40100CI	UNIT	
Maximum repetitive peak reverse voltage Maximum DC reverse voltage		V _{RRM}	100	- v	
		V _{DC}	80		
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)}	40	A	
	per diode		20		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	250	А	
Operating junction temperature range		T _J ⁽¹⁾	-40 to +150	°C	
Storage temperature range		T _{STG}	-55 to +150		

Note

⁽¹⁾ The heat generated must be less than the thermal conductivity from junction to ambient: $dP_D/dT_J < 1/R_{0JA}$



RoHS COMPLIANT

V40100CI



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V40100CI



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ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	V _F ⁽¹⁾	0.45	-	V
	I _F = 10 A			0.52	-	
	I _F = 20 A			0.64	0.70	
	$I_F = 5 A$	T _A = 125 °C		0.36	-	
	I _F = 10 A			0.47	-	
	I _F = 20 A			0.59	0.65	
Reverse current per diode	V _R = 80 V	T _A = 25 °C	I _R ⁽²⁾	0.01	-	mA
	$v_{\rm R} = 00 v$	T _A = 125 °C		13.0	-	
	V _R = 100 V	T _A = 25 °C		-	1	
		T _A = 125 °C		21.0	60	
Junction capacitance	4 V, 1MHz		CJ	2450	-	pF

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	V40100CI	UNIT		
Typical thermal resistance per device	$R_{ ext{ heta}JC}$	1.7	°C/W		

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V40100CI-M3/P	1.88	Р	50/tube	Tube		



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RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

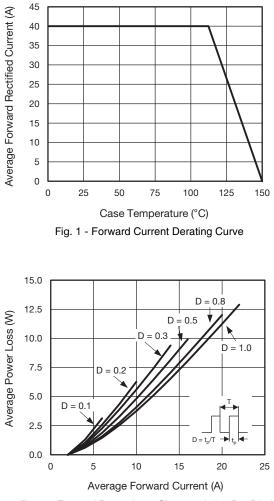


Fig. 2 - Forward Power Loss Characteristics Per Diode

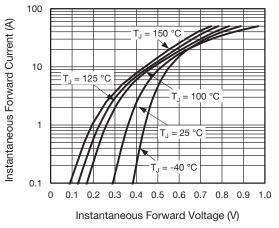


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

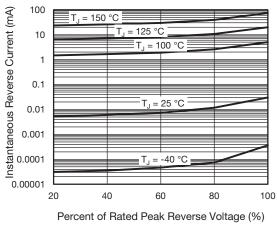
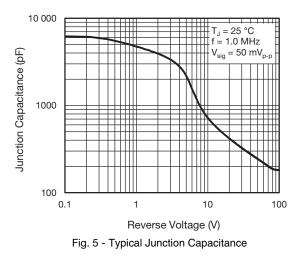


Fig. 4 - Typical Reverse Characteristics Per Diode



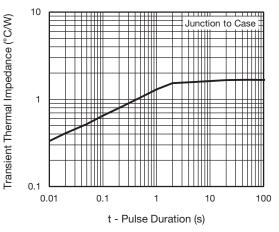


Fig. 6 - Typical Transient Thermal Impedance Per Device

Revision: 28-Sep-17

3

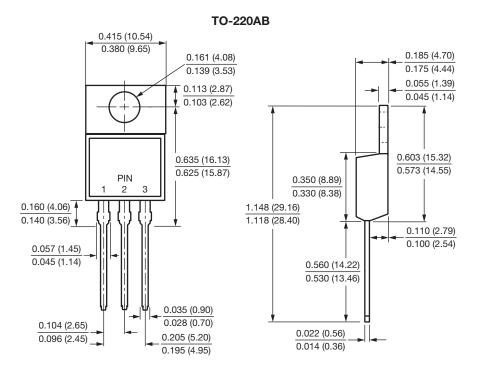
Document Number: 87545

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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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Revision: 01-Jan-2024