AUTOMOTIVE

RoHS

COMPLIANT

HALOGEN

FREE



Vishay General Semiconductor

Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifiers

eSMP® Series



SMF (DO-219AB)

Cathode O Anode

Bottom view

LINKS TO ADDITIONAL RESOURCES

Top view



PRIMARY CHARACTERISTICS			
I _{F(AV)}	3.0 A		
V_{RRM}	100 V		
I _{FSM}	55 A		
V _F at I _F = 3 A (T _A = 125 °C)	0.62 V		
T _J max.	175 °C		
Package	SMF (DO-219AB)		
Circuit configuration	Single		

FEATURES

- Trench MOS Schottky technology
- Low profile package
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Wave and reflow solderable
- AEC-Q101 qualified available
 - Automotive ordering code: base P/NHM3
- Compatible to SOD-123W package case outline
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency inverters, freewheeling, DC/DC converters, and polarity protection in commercial, industrial, and automotive applications.

MECHANICAL DATA

Case: SMF (DO-219AB)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and

AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test

Polarity: color band denotes the cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V3FM10	UNIT	
Device marking code		3МВ		
Maximum repetitive peak reverse voltage	V_{RRM}	100	V	
Maximum average forward rectified current (fig.1)	I _{F(AV)} (1)	2.5	Α	
	I _{F(AV)} (2)	3.0	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	55	А	
Operating junction temperature range	T _J (3)	-40 to +175	°C	
Storage temperature range	T _{STG}	-55 to +175	\neg	

Notes

- (1) Free air, mounted on FR4 PCB, 2 oz. standard footprint
- (2) Mounted on FR4 PCB, 2 oz. 10 mm x 10 mm copper pad areas
- $^{(3)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST C	CONDITIONS	SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 1.5 A	T _A = 25 °C		0.59	-	V
	I _F = 3.0 A), (1)	0.74	0.83	
	I _F = 1.5 A	T _A = 125 °C	V _F ⁽¹⁾	0.52	-	
	I _F = 3.0 A			0.62	0.70	
Reverse current	V 70.V	T _A = 25 °C		0.7	-	μΑ
	V _R = 70 V	T _A = 125 °C	I _R ⁽²⁾	500	-	
	V _R = 100 V	T _A = 25 °C	IR (-)	-	85	
		T _A = 125 °C		900	3000	
Typical junction capacitance	4.0 V, 1 MHz	•	CJ	240	-	pF

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

 $^{(2)}$ Pulse test: Pulse width $\leq 5 \text{ ms}$

THERMAL CHARACTERISTICS (T _A = 25 °c unless otherwise noted)				
PARAMETER	SYMBOL	V3FM10	UNIT	
Typical thermal resistance	R ₀ JA (1)(2)	125	°C/W	
	R _{0JM} (3)	22		

Notes

- $^{(1)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$
- $^{(2)}$ Device mounted on FR4 PCB, 2 oz. standard footprint, thermal resistance $R_{\theta JA}$ junction-to-ambient
- $^{(3)}$ Device mounted on 10 mm x 10 mm pad size area footprint; thermal resistance $R_{\theta JM}$ junction-to-mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V3FM10-M3/H	0.015	Н	3000	7" diameter plastic tape and reel	
V3FM10-M3/I	0.015	I	10 000	13" diameter plastic tape and reel	
V3FM10HM3/H (1)	0.015	Н	3000	7" diameter plastic tape and reel	
V3FM10HM3/I (1)	0.015	I	10 000	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified

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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

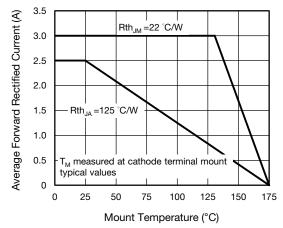


Fig. 1 - Maximum Forward Current Derating Curve

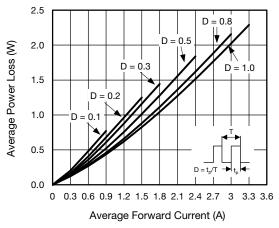


Fig. 2 - Average Power Loss Characteristics

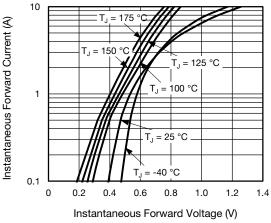


Fig. 3 - Typical Instantaneous Forward Characteristics

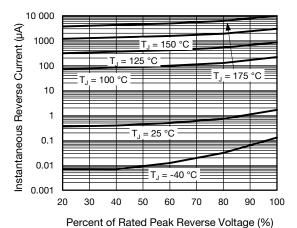


Fig. 4 - Typical Reverse Leakage Characteristics

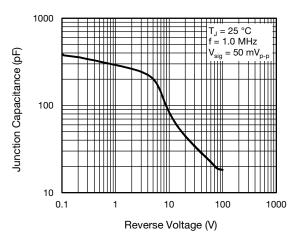


Fig. 5 - Typical Junction Capacitance

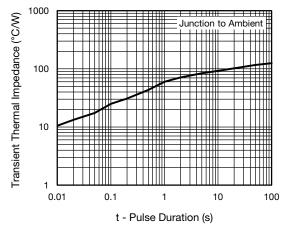
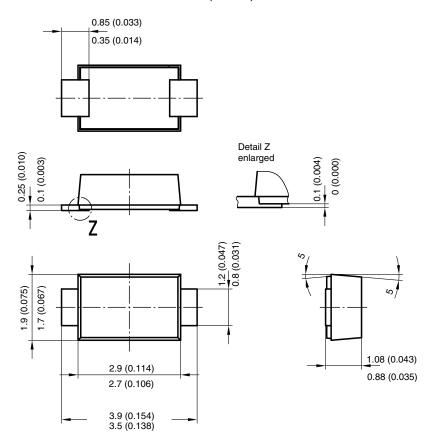


Fig. 6 - Typical Transient Thermal Impedance

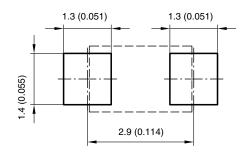


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PACKAGE OUTLINE DIMENSIONS in millimeters (inches)



Foot print recommendation:



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