V40PW12C

Vishay General Semiconductor

## **High Current Density Surface-Mount** TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.49$  V at  $I_F = 5$  A



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PIN 1 O-

PIN 2 O-HEATSINK

#### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 20 A			
V <sub>RRM</sub>	120 V			
I <sub>FSM</sub>	240 A			
$V_F$ at $I_F$ = 20 A ( $T_A$ = 125 °C)	0.73 V			
T <sub>J</sub> max.	150 °C			
Package	SlimDPAK (TO-252AE)			
Circuit configuration	Common cathode			

### **FEATURES**

- Very low profile typical height of 1.3 mm
- Trench MOS Schottky technology
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- · High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

## **TYPICAL APPLICATIONS**

For use in low voltage high frequency DC/DC converters, freewheeling diodes, and polarity protection applications.

## **MECHANICAL DATA**

Case: SlimDPAK (TO-252AE) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 gualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102 M3 and HM3 suffix meets JESD 201 class 2 whisker test

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V40PW12C	UNIT	
Device marking code		V40PW12C			
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	120	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub> <sup>(1)</sup>	40	А	
	per diode		20	А	
Peak forward surge current 8.3 ms single half sine-w superimposed on rated load per diode	I <sub>FSM</sub>	240	A		
Operating junction temperature range	T <sub>J</sub> <sup>(2)</sup>	-40 to +150	°C		
Storage temperature range	T <sub>STG</sub>	-55 to +150	°C		

#### Notes

<sup>(1)</sup> With infinite heatsink

<sup>(2)</sup> The heat generated must be less than the thermal conductivity from junction to ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 





RoHS COMPLIANT HALOGEN FREE

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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I <sub>F</sub> = 5.0 A	<sub>F</sub> = 10 A T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.54	-	V
	I <sub>F</sub> = 10 A			0.69	-	
	I <sub>F</sub> = 20 A			0.95	1.03	
	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 125 °C		0.49	-	
	I <sub>F</sub> = 10 A			0.60	-	
	I <sub>F</sub> = 20 A			0.73	0.81	
Reverse current per diode	V <sub>B</sub> = 90 V	$T_{A} = 25 \text{ °C}$	I <sub>R</sub> (2)	0.05	-	- mA
	v <sub>R</sub> = 90 v	T <sub>A</sub> = 125 °C		7	-	
	V <sub>R</sub> = 120 V	T <sub>A</sub> = 25 °C		-	0.75	
		T <sub>A</sub> = 125 °C		14	35	
Typical junction capacitance per diode	4.0 V, 1 MHz		CJ	1250	-	pF

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	V40PW12C	UNIT		
Typical thermal resistance	R <sub>0JA</sub> (1)(2)	55	°C/W		
	R <sub>0JM</sub> <sup>(3)</sup>	1.5			

### Notes

 $^{(1)}$  The heat generated must be less than thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

 $^{(2)}$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  - junction to ambient

<sup>(3)</sup> Mounted on infinite heat sink; thermal resistance  $R_{\theta JM}$  - junction-to-mount

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V40PW12C-M3/I	0.20	I	4500	13" diameter plastic tape and reel		
V40PW12CHM3/I (1)	0.20	I	4500	13" diameter plastic tape and reel		

Note

(1) AEC-Q101 qualified



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## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

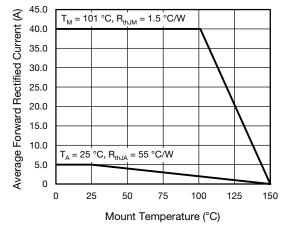


Fig. 1 - Maximum Forward Current Derating Curve

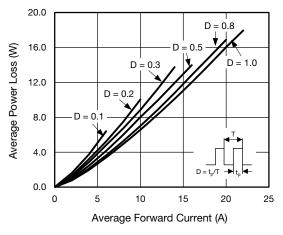


Fig. 2 - Forward Power Loss Characteristics

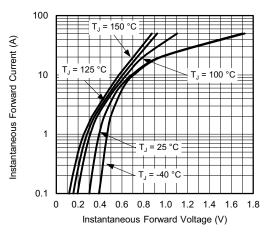


Fig. 3 - Typical Instantaneous Forward Characteristics

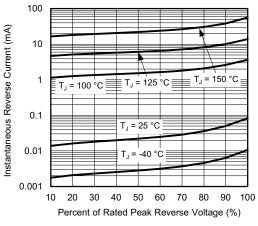


Fig. 4 - Typical Reverse Leakage Characteristics

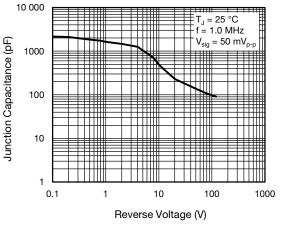


Fig. 5 - Typical Junction Capacitance

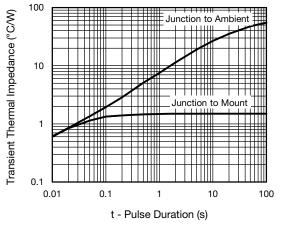


Fig. 6 - Typical Transient Thermal Impedance

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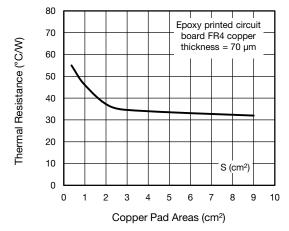
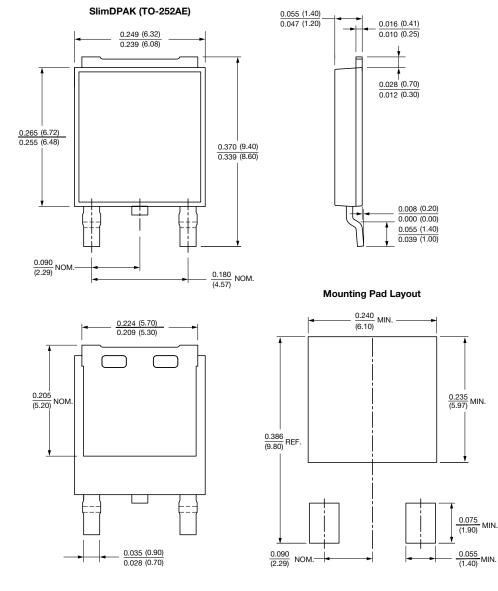


Fig. 7 - Typical Resistance Junction to Ambient vs. Copper Pad Areas



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