V40DL45BP

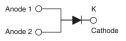
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TMBS[®] (Trench MOS Barrier Schottky) Rectifier for PV Solar Cell Bypass Protection

Ultra Low $V_F = 0.26$ V at $I_F = 5$ A





LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	40 A			
V _{RRM}	45 V			
I _{FSM}	240 A			
V_{F} at I_{F} = 40 A (T_{A} = 125 °C)	0.53 V			
T _{OP} max. (AC model)	150 °C			
T _J max. (DC forward current)	200 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Single			

FEATURES

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- Ideal for automated placement
- · Low forward voltage drop, low power losses
- High efficiency operation
- \bullet Meets MSL level 1, per J-STD-020, LF maximum peak of 260 $^{\circ}\mathrm{C}$
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

MECHANICAL DATA

Case: SMPD (TO-263AC) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102 M3 suffix meets JESD 201 class 2 whisker test

Polarity: As marked

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	V40DL45BP	UNIT		
Maximum repetitive peak reverse voltage	V _{RRM}	45	V		
Maximum DC forward current (fig. 1)	I _{F(DC)} ⁽¹⁾	40	А		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	240	А		
Operating junction temperature range (AC model)	T _{OP}	-40 to +150	°C		
Junction temperature in DC forward current without reverse bias, t = \leq 1 h	T _J ⁽²⁾	≤ 200	°C		

Note

(1) With heatsink

⁽²⁾ Meets the requirements of IEC 61215 ed.2 bypass diode thermal test

Revision: 18-Nov-2020

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Document Number: 87789

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ROHS COMPLIANT

V40DL45BP



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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 5 A	T _A = 25 °C	V _F ⁽¹⁾	0.38	-	V
	I _F = 20 A			0.47	-	
	I _F = 40 A			0.58	0.66	
	I _F = 5 A	T _A = 125 °C		0.26	-	
	I _F = 20 A			0.38	-	
	I _F = 40 A			0.53	0.61	
Reverse current	V _B = 45 V	T _A = 25 °C	I _R ⁽²⁾	-	5	mA
	v _R = 43 v	T _A = 125 °C		36	125	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER SYMBOL V40DL45B		V40DL45BP	UNIT	
Typical thermal resistance	R _{θJC}	0.9	°C/W	
	R _{0JA} (1)(2)	45		

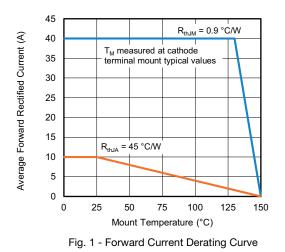
Notes

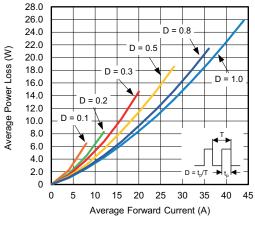
⁽¹⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

⁽²⁾ Free air, without heatsink

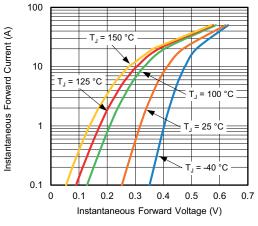
ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
SMPD (TO-263AC)	V40DL45BP-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)





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Fig. 3 - Typical Instantaneous Forward Characteristics

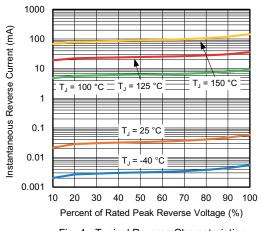


Fig. 4 - Typical Reverse Characteristics

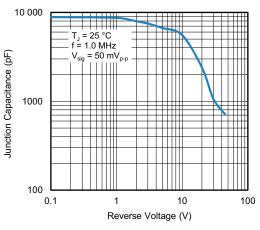


Fig. 5 - Typical Junction Capacitance

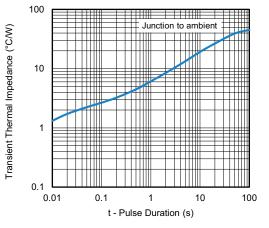


Fig. 6 - Typical Transient Thermal Impedance

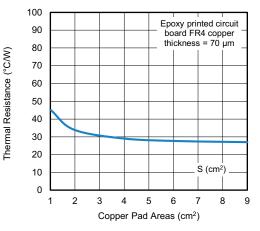


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas

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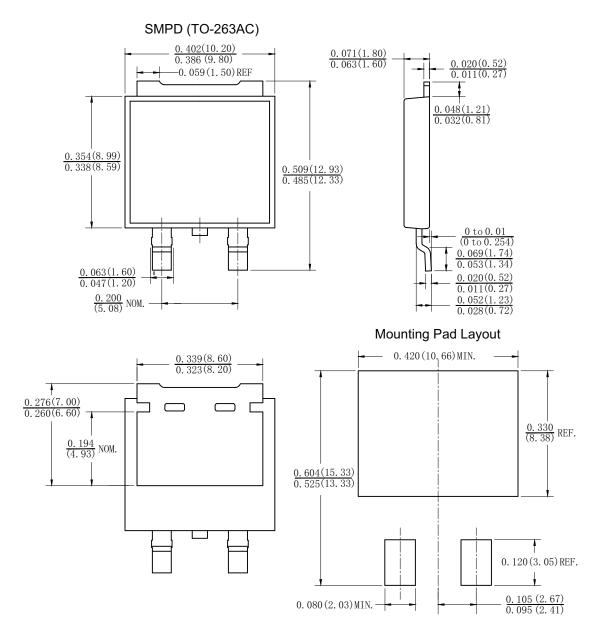
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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Revision: 01-Jan-2025

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