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Vishay General Semiconductor

# Dual High-Voltage TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.47$  V at  $I_F = 5.0$  A

### **FEATURES**

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- · High efficiency operation
- Solder bath temperature 275 °C maximum, 10 s per JESD 22-B106
- AEC-Q101 gualified available Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

### **MECHANICAL DATA**

Case: TO-247AD 3L Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3\_X - halogen-free, RoHS-compliant, and AEC-Q101 qualified ("\_X" denotes revision code e.g. A, B,....)

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102 M3 meets JESD 201 class 1A whisker test, HM3 suffix

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meets JESD 201 class 2 whisker test

Polarity: as marked

Mounting torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER		SYMBOL	VX60202PW	UNIT		
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	200	V		
Maximum average forward rectified current	per device	1	60	٨		
(fig. 1)	per diode	IF(AV)	30	— A		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	350	А		
Operating junction temperature range		T <sub>J</sub> <sup>(1)</sup>	-40 to +175			
Storage temperature range		T <sub>STG</sub>	-40 to +175			

#### Note

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub> 2 x 30 A					
V <sub>RRM</sub>	200 V				
I <sub>FSM</sub>	350 A				
$V_{F}$ at $I_{F}$ = 30 A ( $T_{J}$ = 125 °C)	0.65 V				
T <sub>J</sub> max.	175 °C				
Package	TO-247AD 3L				
Circuit configuration	Common cathode				



CASE

PIN 3 O-



FREE





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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_J = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
	I <sub>F</sub> = 5 A	T <sub>J</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.61	-	V
	I <sub>F</sub> = 15 A			0.72	-	
Instantaneous forward valtage per diada	I <sub>F</sub> = 30 A			0.79	0.84	
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A			0.47	-	
	I <sub>F</sub> = 15 A	T <sub>J</sub> = 125 °C		0.57	-	
	I <sub>F</sub> = 30 A			0.65	0.71	
	V <sub>B</sub> = 160 V	T <sub>J</sub> = 25 °C		0.002	-	
Reverse current at rated V <sub>B</sub> per diode	v <sub>R</sub> = 100 v	T <sub>J</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	3.7	-	mA
neverse current at lated v <sub>R</sub> per diode	V 000.V	T <sub>J</sub> = 25 °C		-	0.2	
	V <sub>R</sub> = 200 V	T <sub>J</sub> = 125 °C		8.5	40	
Typical junction capacitance	4.0 V, 1 MHz		CJ	2800	_	pF

Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25$ °C unless otherwise noted)					
PARAMETER SYMBOL VX60202PW U					
Typical thermal resistance per device	$R_{\theta JC}$	0.6	°C/W		

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g) PACKAGE CODE BASE QUANTITY DELIVERY M					
VX60202PW-M3/P	5.64	Р	25/tube	Tube		
VX60202PWHM3_A/P <sup>(1)</sup>	5.64	Р	25/tube	Tube		

Note

<sup>(1)</sup> AEC-Q101 qualified



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### **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

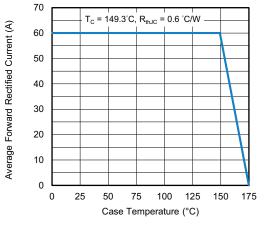


Fig. 1 - Maximum Forward Current Derating Curve

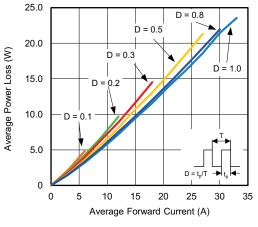


Fig. 2 - Average Power Loss Characteristics

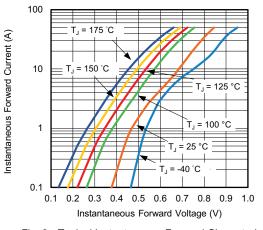


Fig. 3 - Typical Instantaneous Forward Characteristics

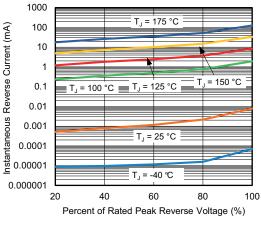
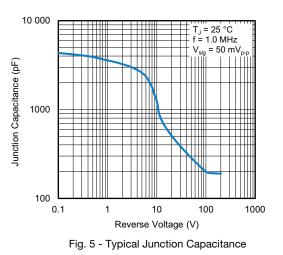
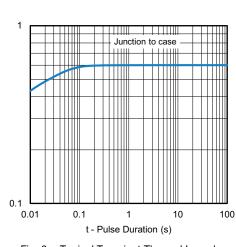


Fig. 4 - Typical Reverse Leakage Characteristics







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Transient Thermal Impedance (°C/W)

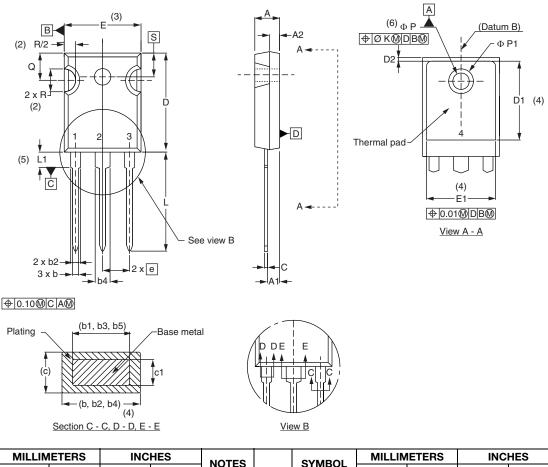
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### PACKAGE OUTLINE DIMENSIONS in millimeters (inches) TO-247AD 3L



CVMDOI	-		-	NOTES	
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
с	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	MILLIMETERS		INCHES		
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES	
D2	0.51	1.30	0.020	0.051		
E	15.29	15.87	0.602	0.625	3	
E1	13.46	-	0.53	-		
е	5.46	BSC	0.215 BSC			
ØК	0.254		0.0	0.010		
L	19.81	20.32	0.780	0.800		
L1	3.71	4.29	0.146	0.169		
ØΡ	3.56	3.66	0.14	0.144		
Ø P1	-	6.98	-	0.275		
Q	5.31	5.69	0.209	0.224		
R	4.52	5.49	0.178	0.216		
S	5.51 BSC		0.217 BSC			

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

<sup>(3)</sup> Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

<sup>(4)</sup> Thermal pad contour optional with dimensions D1 and E1

<sup>(5)</sup> Lead finish uncontrolled in L1

<sup>(6)</sup> Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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