# V20120C, VI20120C

Vishay General Semiconductor

# Dual High Voltage TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.54$  V at  $I_F = 5$  A

### **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- · High efficiency operation
- HALOGEN Solder dip 275 °C max. 10 s, per JESD 22-B106 FREE
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

## **MECHANICAL DATA**

Case: TO-220AB and TO-262AA Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS compliant, and commercial grade

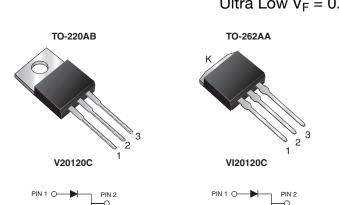
Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

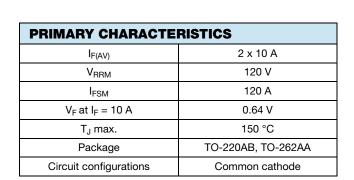
Polarity: as marked

Mounting Torque: 10 in-lbs max.

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER		SYMBOL	V20120C	VI20120C	UNIT		
Max. repetitive peak reverse voltage		V <sub>RRM</sub>	120		V		
Max. average forward rectified current (fig. 1)	per device		20		A		
	per diode	I <sub>F(AV)</sub>	10				
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	120		А		
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000		V/µs		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	- 40 to	+ 150	°C		



PIN 3 O







PIN 3 O

CASE



RoHS COMPLIANT



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25$ °C unless otherwise noted)								
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT		
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	– T <sub>A</sub> = 25 °C	- V <sub>F</sub> <sup>(1)</sup>	0.62	-	V		
	$I_F = 10 \text{ A}$			0.81	0.90			
	$I_F = 5 A$	– T <sub>A</sub> = 125 °C		0.54	-			
	I <sub>F</sub> = 10 A			0.64	0.72			
Reverse current per diode	V <sub>R</sub> = 90 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	8	-	μA		
		T <sub>A</sub> = 125 °C		6	-	mA		
	V <sub>R</sub> = 120 V	T <sub>A</sub> = 25 °C		-	700	μA		
		T <sub>A</sub> = 125 °C		14	45	mA		

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1  $\,\%$  duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	V20120C	VI20120C	UNIT		
Typical thermal resistance per diode	$R_{ ext{ heta}JC}$	2.8		°C/W		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-220AB	V20120C-M3/4W	1.88	4W	50/tube	Tube	
TO-262AA	VI20120C-M3/4W	1.45	4W	50/tube	Tube	



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## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

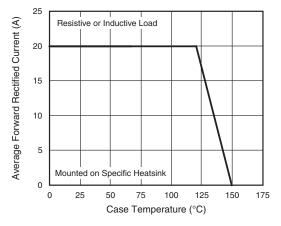


Fig. 1 - Maximum Forward Current Derating Curve

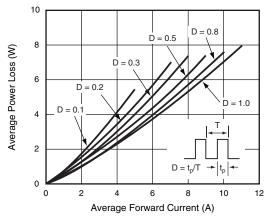


Fig. 2 - Forward Power Loss Characteristics Per Diode

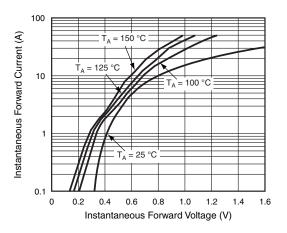


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

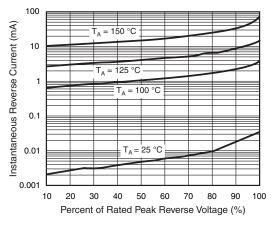


Fig. 4 - Typical Reverse Characteristics Per Diode

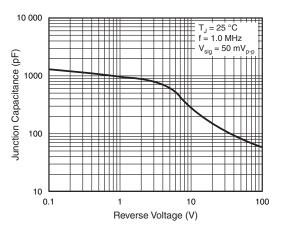


Fig. 5 - Typical Junction Capacitance Per Diode

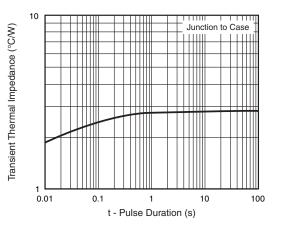


Fig. 6 - Typical Transient Thermal Impedance Per Diode

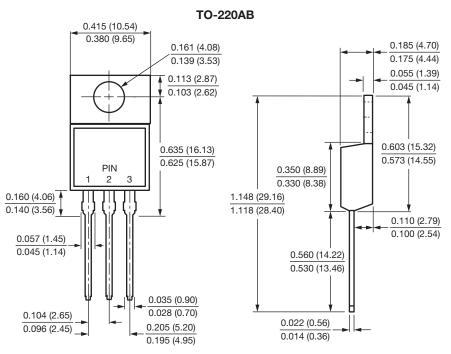
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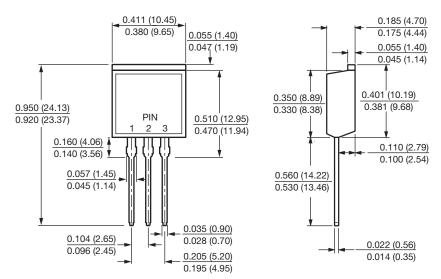
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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



**TO-262AA** 





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