

## Vishay General Semiconductor

# Trench MOS Barrier Schottky Rectifier for PV Solar Cell Bypass Protection

Ultra Low  $V_F = 0.30 \text{ V}$  at  $I_F = 5.0 \text{ A}$ 







#### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 15 A				
V <sub>RRM</sub>	45 V				
I <sub>FSM</sub>	200 A				
V <sub>F</sub> at I <sub>F</sub> = 15 A	0.39 V				
T <sub>OP</sub> max. (AC mode)	150 °C				
T <sub>J</sub> max. (DC forward current)	200 °C				
Package	D <sup>2</sup> PAK (TO-263AB)				
Circuit configuration	Common cathode				

#### **FEATURES**

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- · High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C



- T<sub>J</sub> 200 °C max. in solar bypass mode application
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **TYPICAL APPLICATIONS**

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

#### **MECHANICAL DATA**

Case: D<sup>2</sup>PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VBT3045CBP	UNIT	
Maximum repetitive peak reverse voltage		$V_{RRM}$	45	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub> <sup>(1)</sup>	30	А	
	per diode		15		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	200	А	
Operating junction and storage temperature range (AC mode)		T <sub>OP</sub> , T <sub>STG</sub>	-40 to +150	°C	
Junction temperature in DC forward current without reverse bias, $t \le 1$ h		T <sub>J</sub> <sup>(2)</sup>	≤ 200	°C	

#### Notes

<sup>(1)</sup> With heatsink

<sup>(2)</sup> Meets the requirements of IEC 61215 ed. 2 bypass diode thermal test



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	- V <sub>F</sub> <sup>(1)</sup>	0.42	-	V
	I <sub>F</sub> = 7.5 A			0.44	-	
	I <sub>F</sub> = 15 A			0.49	0.57	
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.30	-	
	I <sub>F</sub> = 7.5 A			0.33	-	
	I <sub>F</sub> = 15 A			0.39	0.48	
Reverse current per diode	V <sub>R</sub> = 45 V	T <sub>A</sub> = 25 °C T <sub>A</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	-	2000	μΑ
	v <sub>R</sub> = 45 v			17	50	mA

#### **Notes**

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	VBT3045CBP	UNIT		
Typical thermal resistance	per diode	$R_{ heta JC}$	1.6	°C/W	
	per device		0.85		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
D <sup>2</sup> PAK (TO-263AB)	VBT3045CBP-E3/4W	1.38	4W	50/tube	Tube	
D <sup>2</sup> PAK (TO-263AB)	VBT3045CBP-E3/8W	1.38	8W	800/reel	Tape and reel	

## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

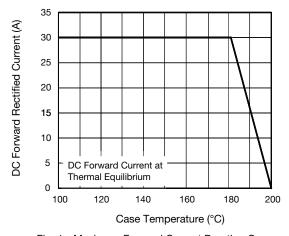


Fig. 1 - Maximum Forward Current Derating Curve

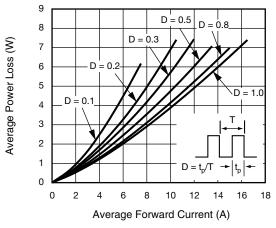


Fig. 2 - Forward Power Loss Characteristics Per Diode



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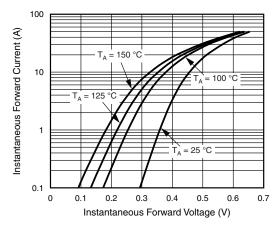


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

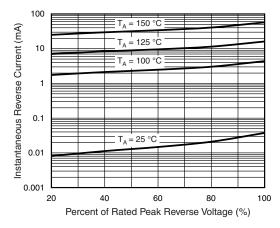


Fig. 4 - Typical Reverse Characteristics Per Diode

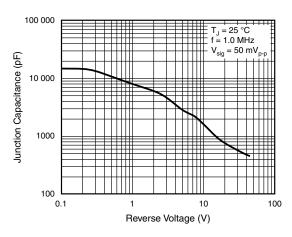


Fig. 5 - Typical Junction Capacitance Per Diode

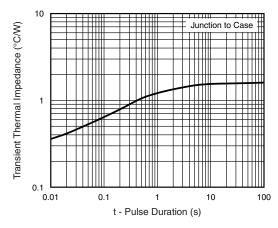
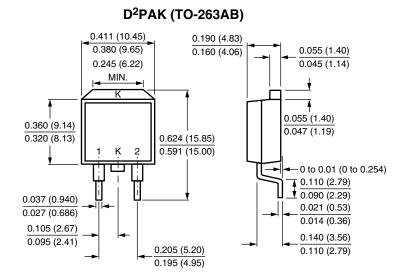
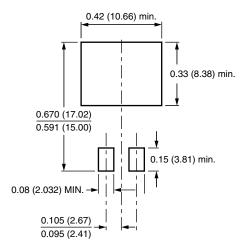


Fig. 6 - Typical Transient Thermal Impedance Per Diode

### PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



#### **Mounting Pad Layout**





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