Vishay General Semiconductor

Dual Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.32$ V at $I_F = 5.0$ A

FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- · High efficiency operation
- Solder dip 275 °C max. 10 s, per JESD 22-B106 RoHS
- Material categorization: for definitions of COMPLIANT compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

MECHANICAL DATA

Case: ITO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VFT4060C	UNIT	
Maximum repetitive peak reverse voltage	V _{RRM}	60	V	
Maximum average forward rectified current per device	I=	40	^	
(fig. 1) per diode	IF(AV)	20	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	240	A	
Voltage rate of change (rated V _R)	dV/dt	10 000	V/µs	
Isolation voltage from terminal to heatsink t = 1 min	V _{AC}	1500	V	
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +150	°C	

ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5.0 A	T _A = 25 °C	V _F (1)	0.43	-	V	
	I _F = 10 A			0.48	-		
	I _F = 20 A			0.53	0.62		
	I _F = 5.0 A	T _A = 125 °C		0.32	-		
	I _F = 10 A			0.39	-		
	I _F = 20 A			0.48	0.57		
Reverse current per diode	V - 60 V	T _A = 25 °C	I _R ⁽²⁾	-	6.0	- mA	
	V _R = 60 V	T _A = 125 °C		34	190		

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

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PIN 2

2 x 20 A

60 V

240 A

0.48 V

150 °C

ITO-220AB

Common cathode



PIN 1 O

PIN 3 O

PRIMARY CHARACTERISTICS

I_{F(AV)}

V_{RRM}

 I_{FSM}

 V_F at $I_F = 20 A$

T_J max.

Package

Circuit configuration



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THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	VFT4060C	UNIT	
Typical thermal resistance	per diode	$R_{ extsf{ heta}JC}$	5.0	°C/W	
	per device		3.0		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
ITO-220AB	VFT4060C-E3/4W	1.75	4W	50/tube	Tube	

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

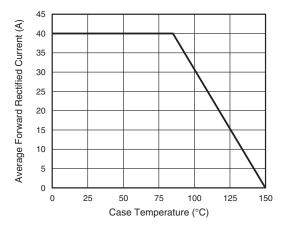


Fig. 1 - Maximum Forward Current Derating Curve

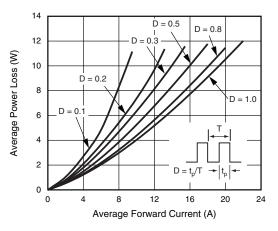


Fig. 2 - Forward Power Dissipation Characteristics Per Diode

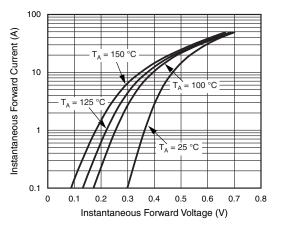


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

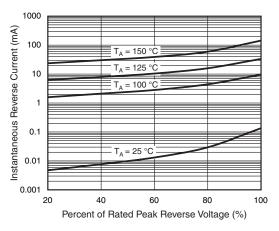


Fig. 4 - Typical Reverse Characteristics Per Diode

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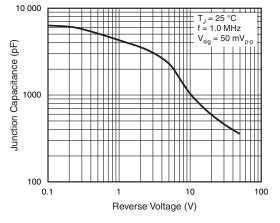


Fig. 5 - Typical Transient Thermal Impedance Per Diode

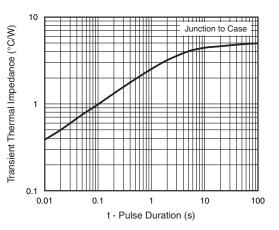
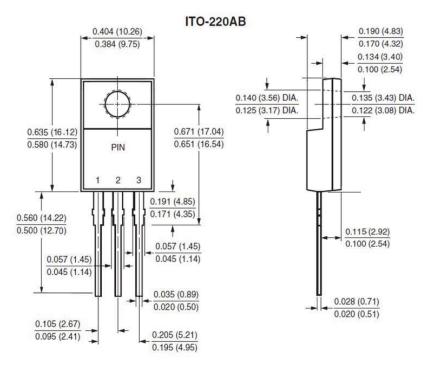


Fig. 6 - Typical Junction Capacitance Per Diode







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