ROHS COMPLIANT

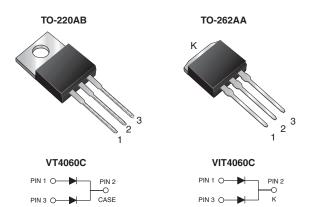
HALOGEN

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Vishay General Semiconductor

# Dual TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.32$  V at  $I_F = 5.0$  A



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 20 A			
V <sub>RRM</sub>	60 V			
I <sub>FSM</sub>	240 A			
$V_F$ at $I_F = 20$ A	0.48 V			
T <sub>J</sub> max.	150 °C			
Package	TO-220AB, TO-262AA			
Circuit configuration	Common cathode			

### FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

#### **MECHANICAL DATA**

**Case:** TO-220AB and TO-262AA Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER		SYMBOL	VT4060C	VIT4060C	UNIT		
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	60		V		
Maximum average forward rectified current (fig. 1)	per device		40		A		
	per diode	I <sub>F(AV)</sub>	20				
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	240		А		
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000		V/µs		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +150		°C		

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25$ °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 25 °C	- V <sub>F</sub> (1)	0.43	-	- V	
	I <sub>F</sub> = 10 A			0.48	-		
	I <sub>F</sub> = 20 A			0.53	0.62		
	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 125 °C		0.32	-		
	I <sub>F</sub> = 10 A			0.39	-		
	I <sub>F</sub> = 20 A			0.48	0.57		
Reverse current per diode	V 60.V	T <sub>A</sub> = 25 °C T <sub>A</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	-	6.0	mA	
	$V_R = 60 V T_A$	T <sub>A</sub> = 125 °C		34	190		

#### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

 $^{(2)}$  Pulse test: Pulse width  $\leq 40\ ms$ 

 Revision: 12-Aug-2019
 1
 Document Number: 89439

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<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER		SYMBOL	VT4060C	VIT4060C	UNIT	
Typical thermal resistance	per diode	Б	1.5		°C/W	
	per device	R <sub>0JC</sub>	0.8			

ORDERING INFORMATION (Example)							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
TO-220AB	VT4060C-M3/4W	1.89	4W	50/tube	Tube		
TO-262AA	VIT4060C-M3/4W	1.46	4W	50/tube	Tube		

### **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

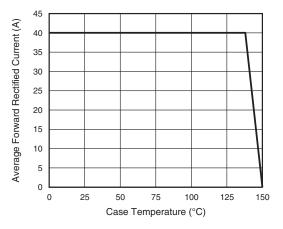


Fig. 1 - Maximum Forward Current Derating Curve

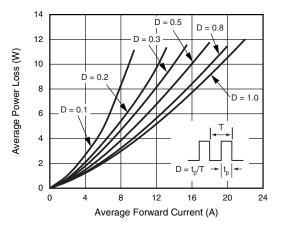


Fig. 2 - Forward Power Dissipation Characteristics Per Diode

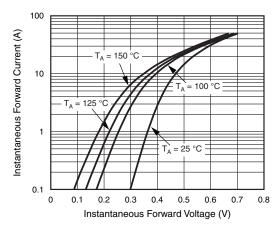


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

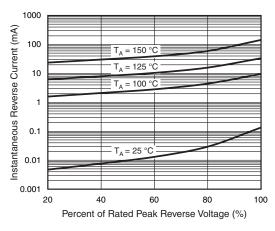
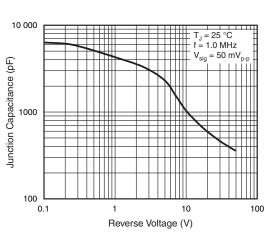


Fig. 4 - Typical Reverse Characteristics Per Diode



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Fig. 5 - Typical Junction Capacitance Per Diode

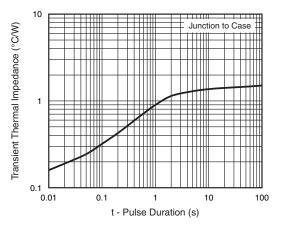
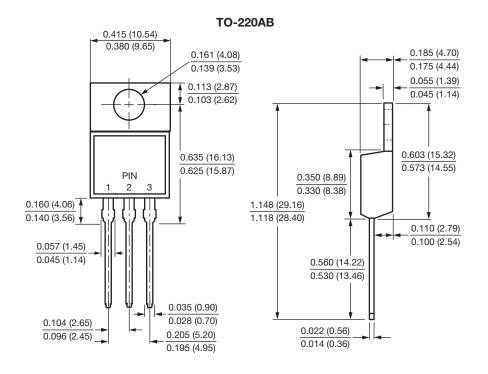


Fig. 6 - Typical Transient Thermal Impedance Per Diode

#### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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0.411 (10.45) 0.185 (4.70) 0.380 (9.65) 0.175 (4.44) 0.055 (1.40) 0.047 (1.19) 0.055 (1.40) 0.045 (1.14) 0.401 (10.19) 0.350 (8.89) 0.950 (24.13) 0.381 (9.68) 0.330 (8.38) PIN 0.510 (12.95) 0.920 (23.37) 2 3 0.470 (11.94) 0.160 (4.06) Л 0.110 (2.79) 0.140 (3.56) 0.100 (2.54) 0.057 (1.45) 0.560 (14.22) 0.045 (1.14) 0.530 (13.46) 0.035 (0.90) 0.104 (2.65) 0.028 (0.70) 0.022 (0.56) 0.096 (2.45) 0.205 (5.20) 0.014 (0.35) 0.195 (4.95)

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TO-262AA



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Revision: 01-Jan-2025

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