VT4045BP-M3

Vishay General Semiconductor

TMBS[®] (Trench MOS Barrier Schottky) Rectifier for PV Solar Cell Bypass Protection

Ultra Low $V_F = 0.28$ V at $I_F = 5$ A



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PRIMARY CHARACTERISTICS					
I _{F(AV)}	40 A				
V _{RRM}	45 V				
I _{FSM}	240 A				
V_F at $I_F = 40$ A	0.51 V				
T _{OP} max. (AC mode)	150 °C				
T _J max. (DC forward current)	200 °C				
Package	TO-220AC				
Circuit configuration	Single				

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Solder dip 275 °C max. 10 s, per JESD 22-B106
 FREE
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

MECHANICAL DATA

Case: TO-220AC

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		VT4045BP	UNIT		
Maximum repetitive peak reverse voltage	V _{RRM}	45	V		
Maximum DC forward bypassing current (fig. 1)		40	А		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		240	А		
Operating junction temperature range (AC mode)	T _{OP}	-40 to +150	°C		
Junction temperature in DC forward current without reverse bias, $t \leq 1 \ h$	T _J ⁽²⁾	≤ 200	°C		

Notes

⁽¹⁾ With heatsink

⁽²⁾ Meets the requirements of IEC 61215 ed.2 bypass diode thermal test

ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT	
Instantaneous forward voltage	I _F = 5 A	T _A = 25 °C	V _F (1)	0.41	-	V	
	I _F = 20 A			0.50	-		
	$I_F = 40 A$			0.57	0.67		
	I _F = 5 A	T _A = 125 °C		0.28	-		
	I _F = 20 A			0.41	-		
	I _F = 40 A			0.51	0.63		
Reverse current	V _R = 45 V	T _A = 25 °C	I _R ⁽²⁾	-	3000	μA	
	v _R = 45 v	T _A = 125 °C		29	85	mA	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

Revision: 12-Aug-2019 1 Document Number: 89441 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>









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THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	VT4045BP	UNIT		
Typical thermal resistance $R_{\theta JC}$		0.8	°C/W		

ORDERING INFORMATION (Example)							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
TO-220AC	VT4045BP-M3/4W	1.87	4W	50/tube	Tube		

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

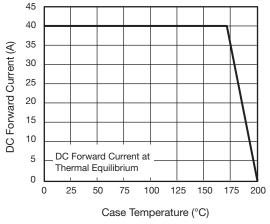


Fig. 1 - Maximum Forward Current Derating Curve

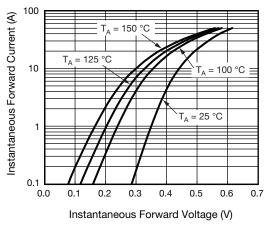


Fig. 2 - Typical Instantaneous Forward Characteristics

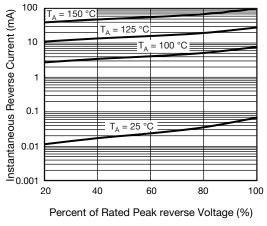
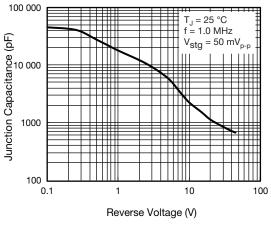
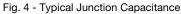


Fig. 3 - Typical Reverse Characteristics









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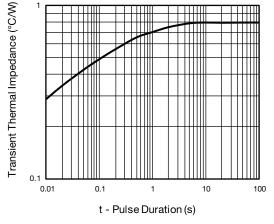
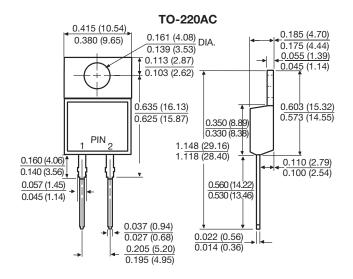


Fig. 5 - Typical Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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