V30M120C, VI30M120C

Vishay General Semiconductor

Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.52$ V at $I_F = 5$ A

FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- · High efficiency operation
- HALOGEN Solder dip 275 °C max. 10 s, per JESD 22-B106 FREE
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB and TO-262AA Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)							
PARAMETER		SYMBOL	V30M120C	VI30M120C	UNIT		
Maximum repetitive peak reverse voltage		V _{RRM}	/ _{RRM} 120		V		
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)}	30		А		
	per diode		15				
Peak forward surge current 8.3 ms single half sine-w superimposed on rated load per diode	-wave I _{FSM}		15	0			
Voltage rate of change (rated V_R)		dV/dt	/dt 10 000		V/µs		
Operating junction and storage temperature range		T _J , T _{STG}	-40 to	+175	°C		

TMBS[®] TO-220AB TO-262AA VI30M120C V30M120C PIN 1 O-PIN 2 PIN 2

PIN 3 O

к

PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 15 A			
V _{RRM}	120 V			
I _{FSM}	150 A			
V_F at $I_F = 15 A$	0.68 V			
T _J max.	175 °C			
Package	TO-220AB, TO-262AA			
Diode variations	Common cathode			



RoHS COMPLIANT





CASE

PIN 3 O



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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CO	NDITIONS	SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	- V _F ⁽¹⁾	0.60	-	V
	I _F = 7.5 A			0.67	-	
	I _F = 15 A			0.87	0.98	
	I _F = 5 A	T _A = 125 °C		0.52	-	
	I _F = 7.5 A			0.57	-	
	I _F = 15 A			0.68	0.76	
Reverse current per diode	V _R = 90 V	T _A = 25 °C	I _R (2)	3.5	-	μA
		T _A = 125 °C		2	-	mA
	$V_{P} = 120 V$	T _A = 25 °C		-	800	μA
		T _A = 125 °C		5	27	mA

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

 $^{(2)}$ Pulse test: Pulse width $\leq 5\mbox{ ms}$

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER		SYMBOL	V30M120C	VI30M120C	UNIT	
	per diode	P	2.2		°C/W	
Typical thermal resistance ⁽¹⁾	per device	R _{θJC}	1.3			
	per device	R _{0JA} ⁽²⁾	45	55		

Notes

 $^{(1)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient dP_D/dT_J < 1/R_{θ JA}

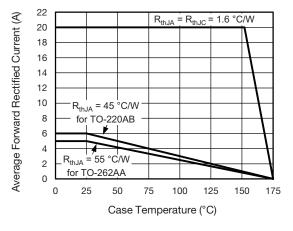
⁽²⁾ Free air, without heatsink

ORDERING INFORMATION (Example)							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
TO-220AB	V30M120C-M3/4W	1.89	4W	50/tube	Tube		
TO-262AA	VI30M120C-M3/4W	1.45	4W	50/tube	Tube		

V30M120C, VI30M120C



RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)



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Fig. 1 - Maximum Forward Current Derating Curve

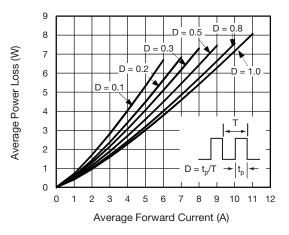


Fig. 2 - Forward Power Loss Characteristics Per Diode

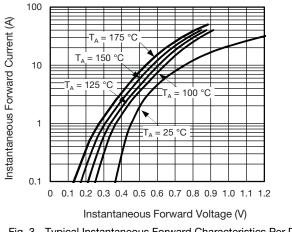
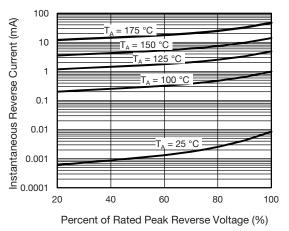
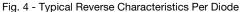


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode





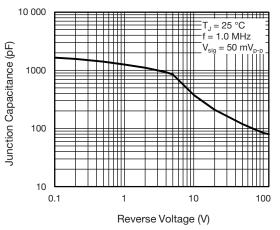


Fig. 5 - Typical Junction Capacitance Per Diode

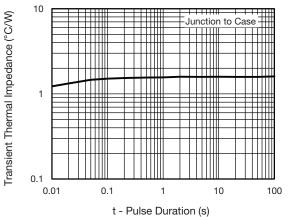


Fig. 6 - Typical Transient Thermal Impedance Per Diode

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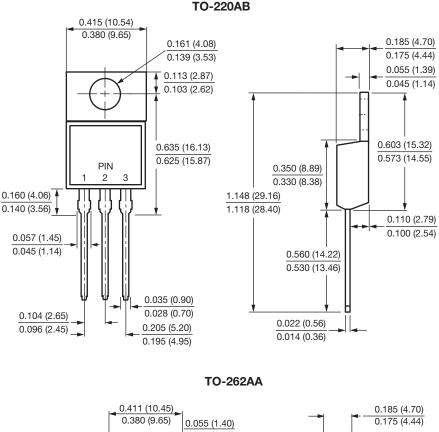
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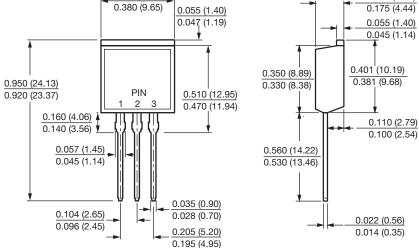
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)







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