V40170C-M3

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Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.52$ V at $I_F = 5$ A



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 20 A			
V _{RRM}	170 V			
I _{FSM}	200 A			
V_F at $I_F = 20$ A	0.68 V			
T _J max.	175 °C			
Package	TO-220AB			
Circuit configuration Common cathode				

FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Solder dip 275 °C max. 10 s, per JESD 22-B106
 FREE
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V40170C	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	170	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)}	40	А	
	per diode		20		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	200	А	
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +175	°C	

ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	V _F ⁽¹⁾	0.66	-	V	
	I _F = 10 A			0.75	-		
	I _F = 20 A			0.86	1.20		
	I _F = 5 A	T _A = 125 °C		0.52	-		
	I _F = 10 A			0.59	-		
	I _F = 20 A			0.68	0.76		
Reverse current per diode	V _R = 136 V	T _A = 25 °C	I _R (2)	1.3	-	μA	
		T _A = 125 °C		2.2	-	mA	
	V _R = 170 V	T _A = 25 °C		-	250	μA	
		T _A = 125 °C		4.2	50	mA	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 20 ms

Revision: 01-Dec-16 1 Document Number: 89941 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



RoHS

COMPLIANT



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THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V40170C	UNIT	
Typical thermal resistance	per diode	- R _{θJC}	1.2	°C/W	
	per device		0.85		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-220AB	V40170C-M3/4W	1.85	4W	50/tube	Tube	

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

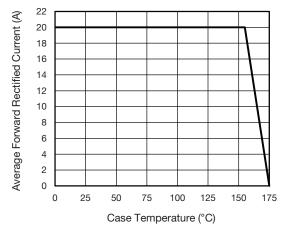


Fig. 1 - Maximum Forward Current Derating Curve

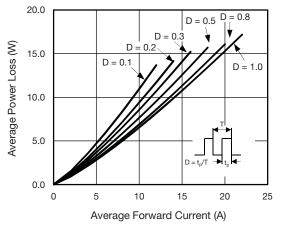
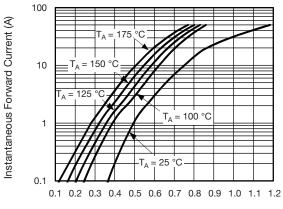


Fig. 2 - Forward Power Loss Characteristics Per Diode



Instantaneous Forward Voltage (V)



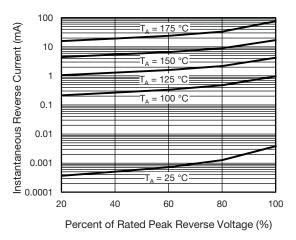


Fig. 4 - Typical Reverse Characteristics Per Diode

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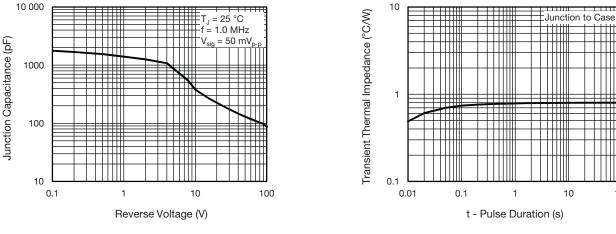
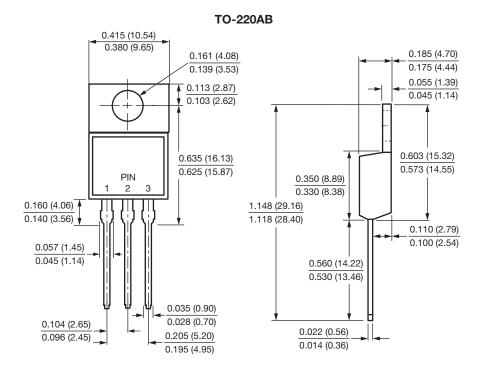


Fig. 5 - Typical Junction Capacitance Per Diode

Fig. 6 - Typical Transient Thermal Impedance Per Device

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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