COMPLIANT

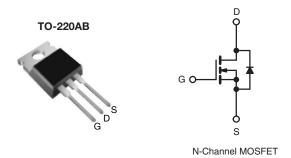
HALOGEN

FREE



D Series Power MOSFET

PRODUCT SUMMARY				
V _{DS} (V) at T _J max.	450			
R _{DS(on)} max. at 25 °C (Ω)	V _{GS} = 10 V	0.17		
Q _g max. (nC)	88			
Q _{gs} (nC)	12			
Q _{gd} (nC)	23			
Configuration	Single			



FEATURES

- Optimal Design
 - Low Area Specific On-Resistance
 - Low Input Capacitance (Ciss)
 - Reduced Capacitive Switching Losses
 - High Body Diode Ruggedness
 - Avalanche Energy Rated (UIS)
- · Optimal Efficiency and Operation
 - Low Cost
 - Simple Gate Drive Circuitry
 - Low Figure-of-Merit (FOM): Ron x Qg
 - Fast Switching
- Compliant to RoHS Directive 2011/65/EU

Note

* Pb containing terminations are not RoHS compliant, exemptions may apply

APPLICATIONS

- Consumer Electronics
 - Displays (LCD or Plasma TV)
- Lighting
- Industrial
 - Welding
 - Induction Heating
 - Motor Drives
 - Battery Chargers
- SMPS

ORDERING INFORMATION			
Package	TO-220AB		
Lead (Pb)-free	SiHP25N40D-E3		
Lead (Pb)-free and Halogen-free	SiHP25N40D-GE3		

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage	V _{DS}	400				
Gate-Source Voltage	V _{GS}	± 30	V			
Gate-Source Voltage AC (f > 1 Hz)		30				
Continuous Drain Current (T _J = 150 °C)	V_{GS} at 10 V $T_{C} = 25 ^{\circ}C$ $T_{C} = 100 ^{\circ}C$	I _D	25	А		
	$T_C = 100 ^{\circ}C$		16			
Pulsed Drain Current ^a	I _{DM}	78				
Linear Derating Factor		2.2	W/°C			
Single Pulse Avalanche Energy ^b	E _{AS}	556	mJ			
Maximum Power Dissipation	P_{D}	278	W			
Operating Junction and Storage Temperature Range		T_J, T_stg	- 55 to + 150	°C		
Drain-Source Voltage Slope	T _J = 125 °C	dV/dt	24	V/ns		
Reverse Diode dV/dt ^d		uv/ut	0.6	V/115		
Soldering Recommendations (Peak Temperature)	for 10 s		300°	°C		

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature.
- b. V_{DD} = 50 V, starting T_J = 25 °C, L = 2.3 mH, R_g = 25 Ω , I_{AS} = 17 A.
- c. 1.6 mm from case.
- d. $I_{SD} \le I_D$, starting $T_J = 25$ °C.



Vishay Siliconix

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	TYP.	MAX.	UNIT	
Maximum Junction-to-Ambient	R _{thJA}	-	62	°C/W	
Maximum Junction-to-Case (Drain)	R_{thJC}	-	0.45	C/ VV	

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static					•	•	
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		400	-	-	V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	Reference to 25 °C, I _D = 250 μA		0.5	-	V/°C
Gate-Source Threshold Voltage (N)	V _{GS(th)}	V _{DS} :	= V _{GS} , I _D = 250 μA	3	-	5	V
Gate-Source Leakage	I _{GSS}		V _{GS} = ± 30 V	-	-	± 100	nA
7 0		V _{DS} :	V _{DS} = 400 V, V _{GS} = 0 V		-	1	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 320 \	V, V _{GS} = 0 V, T _J = 125 °C	-	-	10	μA
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 13 A	-	0.14	0.17	Ω
Forward Transconductance	9 _{fs}	V _{DS} = 50 V, I _D = 13 A		-	7.4	-	S
Dynamic				l			
Input Capacitance	C _{iss}	$V_{GS} = 0 \text{ V},$ $V_{DS} = 100 \text{ V},$ f = 1 MHz		-	1707	-	pF
Output Capacitance	C _{oss}			-	177	-	
Reverse Transfer Capacitance	C _{rss}			-	19	-	
Total Gate Charge	Qg		V _{GS} = 10 V I _D = 13 A, V _{DS} = 320 V	-	44	88	nC
Gate-Source Charge	Q _{gs}	V _{GS} = 10 V		-	12	-	
Gate-Drain Charge	Q_{gd}			-	23	-	
Turn-On Delay Time	t _{d(on)}	•			21	42	ns
Rise Time	t _r	Von	$V_{DD} = 320 \text{ V}, I_D = 13 \text{ A},$		57	86	
Turn-Off Delay Time	t _{d(off)}	$V_{GS} = 320 \text{ V}, I_{B} = 13 \text{ A},$ $V_{GS} = 10 \text{ V}, R_{g} = 24.6 \Omega$		-	40	80	
Fall Time	t _f			-	37	74	
Gate Input Resistance	R _g	f = 1 MHz, open drain		-	1.8	-	Ω
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	MOSFET sym showing the	MOSFET symbol showing the		-	24	
Pulsed Diode Forward Current	I _{SM}	integral reverse p - n junction diode		-	-	78	A
Diode Forward Voltage	V _{SD}	T _J = 25 °C, I _S = 13 A, V _{GS} = 0 V		-	-	1.2	V
Reverse Recovery Time	t _{rr}	$T_J = 25 ^{\circ}\text{C}, I_F = I_S = 13 \text{A},$ $dI/dt = 100 \text{A/}\mu\text{s}, V_R = 20 \text{V}$		-	353	-	ns
Reverse Recovery Charge	Q _{rr}			_	4.4	-	μC
Reverse Recovery Current	I _{RRM}			_	24	_	A



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

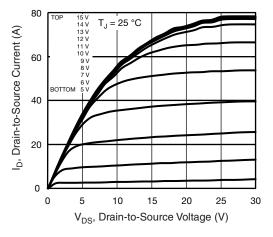


Fig. 1 - Typical Output Characteristics

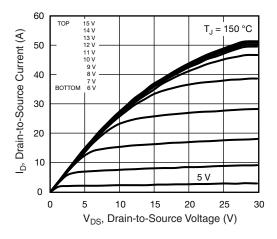


Fig. 2 - Typical Output Characteristics

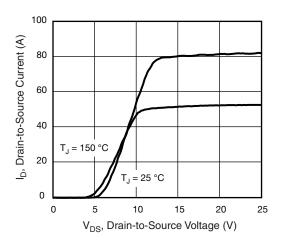


Fig. 3 - Typical Transfer Characteristics

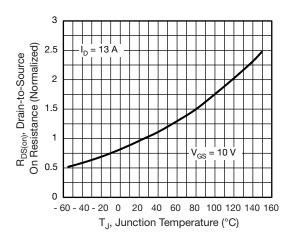


Fig. 4 - Normalized On-Resistance vs. Temperature

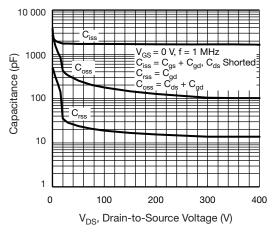


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

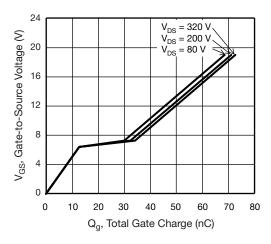


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage



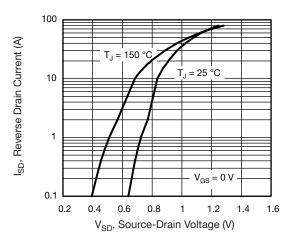


Fig. 7 - Typical Source-Drain Diode Forward Voltage

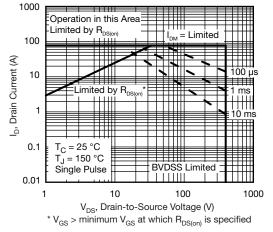


Fig. 8 - Maximum Safe Operating Area

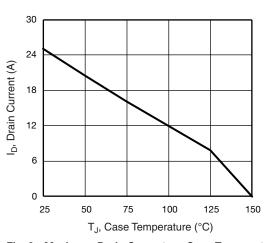


Fig. 9 - Maximum Drain Current vs. Case Temperature

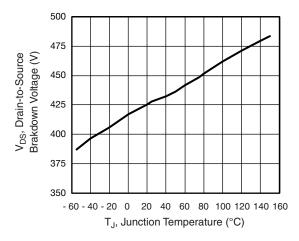


Fig. 10 - Temperature vs. Drain-to-Source Voltage

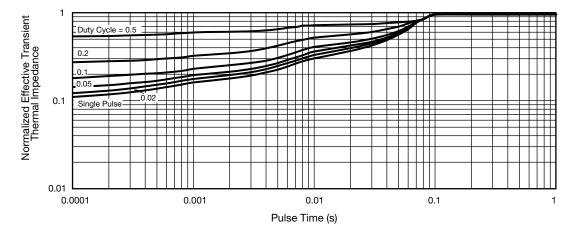


Fig. 11 - Normalized Thermal Transient Impedance, Junction-to-Case



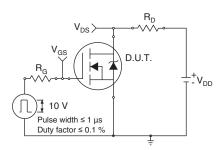


Fig. 12 - Switching Time Test Circuit

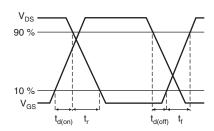


Fig. 13 - Switching Time Waveforms

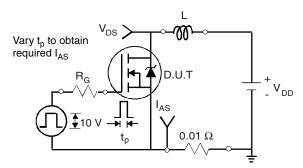


Fig. 14 - Unclamped Inductive Test Circuit

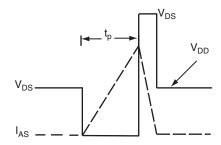


Fig. 15 - Unclamped Inductive Waveforms

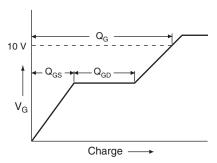


Fig. 16 - Basic Gate Charge Waveform

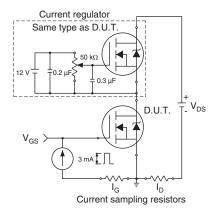
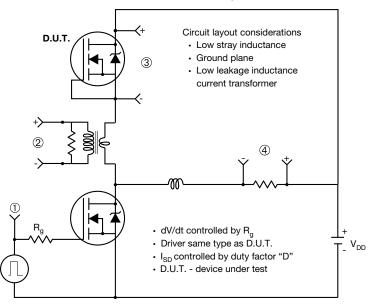


Fig. 17 - Gate Charge Test Circuit



Peak Diode Recovery dV/dt Test Circuit



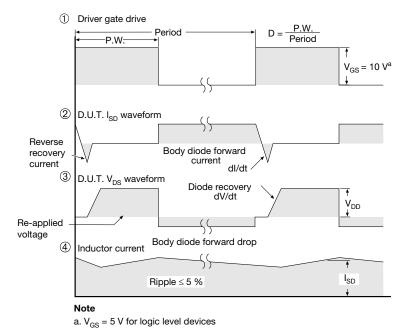


Fig. 18 - For N-Channel

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