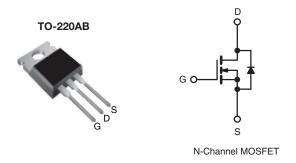
COMPLIANT HALOGEN

FREE



E Series Power MOSFET

PRODUCT SUMMARY				
V _{DS} (V) at T _J max.	550			
R _{DS(on)} max. at 25 °C (Ω)	V _{GS} = 10 V	0.184		
Q _g max. (nC)	92			
Q _{gs} (nC)	10			
Q _{gd} (nC)	19			
Configuration	Single			



FEATURES

- Low figure-of-merit (FOM) Ron x Qq
- Low input capacitance (Ciss)
- · Reduced switching and conduction losses
- Low gate charge (Q_q)
- Avalanche energy rated (UIS)
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

APPLICATIONS

- Computing
 - PC silver box / ATX power supplies
- Lighting
 - Two stage LED lighting
- Consumer electronics
- Applications using hard switched topologies
 - Power factor correction (PFC)
 - Two switch forward converter
 - Flyback converter
- Switch mode power supplies (SMPS)

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free and Halogen-free	SiHP20N50E-GE3

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, un	less otherwis	se noted)		
PARAMETER			SYMBOL	LIMIT	UNIT
Drain-Source Voltage			V_{DS}	500	V
Gate-Source Voltage			V_{GS}	± 30	v
Continuous Drain Current (T _J = 150 °C)	V _{GS} at 10 V	T _C = 25 °C T _C = 100 °C	- I _D	19	
	V _{GS} at 10 V	T _C = 100 °C		12	Α
Pulsed Drain Current ^a			I _{DM}	42	
Linear Derating Factor				1.4	W/°C
Single Pulse Avalanche Energy b			E _{AS}	204	mJ
Maximum Power Dissipation			P _D	179	W
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +150	°C
Drain-Source Voltage Slope	$V_{DS} = 0 V$	V _{DS} = 0 V to 80 % V _{DS}		70	1//22
Reverse Diode dV/dt d			dV/dt	32	- V/ns
Soldering Recommendations (Peak Temperature	e) c for	for 10 s		300	°C

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature.
- b. V_{DD} = 50 V, starting T_J = 25 °C, L = 28.2 mH, R_q = 25 Ω , I_{AS} = 3.8 A.
- c. 1.6 mm from case.
- d. $I_{SD} \leq I_{D}, \; dI/dt = 100 \; A/\mu s, \; starting \; T_{J} = 25 \; ^{\circ}C.$

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	TYP.	MAX.	UNIT	
Maximum Junction-to-Ambient	R_{thJA}	-	62	°C/W	
Maximum Junction-to-Case (Drain)	R_{thJC}	-	0.7	C/VV	



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static					•	l .	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		500	-	-	٧
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference to 25 °C, I _D = 1 mA		-	0.59	-	V/°C
Gate-Source Threshold Voltage (N)	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$		-	4.0	٧
0.1. 0		$V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA
Gate-Source Leakage	I _{GSS}		$V_{GS} = \pm 30 \text{ V}$		-	± 1	μΑ
Zoro Coto Voltago Duoin Current	1	V _{DS} =	= 500 V, V _{GS} = 0 V	-	-	1	μА
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 400 V	/, V _{GS} = 0 V, T _J = 125 °C	-	-	10	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 10 A	-	0.160	0.184	Ω
Forward Transconductance	9fs	V _{DS} = 30 V, I _D = 10 A		-	4.4	-	S
Dynamic							
Input Capacitance	C _{iss}	$V_{GS} = 0 \text{ V},$ $V_{DS} = 100 \text{ V},$ f = 1 MHz		-	1640	-	pF
Output Capacitance	C _{oss}			-	87	-	
Reverse Transfer Capacitance	C _{rss}			-	6	-	
Effective Output Capacitance, Energy Related ^a	C _{o(er)}	V _{DS} = 0 V to 400 V, V _{GS} = 0 V		-	73	-	
Effective Output Capacitance, Time Related ^b	C _{o(tr)}			-	222	-	
Total Gate Charge	Qg			-	46	92	
Gate-Source Charge	Q _{gs}	V _{GS} = 10 V	$V_{GS} = 10 \text{ V}$ $I_D = 10 \text{ A}, V_{DS} = 400 \text{ V}$		10	-	nC
Gate-Drain Charge	Q_{gd}			-	19	-	1
Turn-On Delay Time	t _{d(on)}			-	17	34	ns
Rise Time	t _r	V _{DD} -	V _{DD} = 400 V, I _D = 10 A,		27	54	
Turn-Off Delay Time	t _{d(off)}	$V_{GS} = 10 \text{ V}, R_g = 9.1 \Omega$		-	48	96	
Fall Time	t _f			-	25	50	
Gate Input Resistance	R _g	f = 1 MHz, open drain		-	0.83	-	Ω
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	Is	MOSFET symbol showing the integral reverse p - n junction diode		-	-	19	
Pulsed Diode Forward Current	I _{SM}			-	-	42	- A
Diode Forward Voltage	V _{SD}	$T_J = 25 ^{\circ}\text{C}, I_S = 10 \text{A}, V_{GS} = 0 \text{V}$		-	-	1.2	V
Reverse Recovery Time	t _{rr}	$T_J = 25 \text{ °C, I}_F = I_S = 10 \text{ A,}$ $dI/dt = 100 \text{ A/µs, V}_R = 25 \text{ V}$		-	293	-	ns
Reverse Recovery Charge	Q _{rr}			-	4.0	-	μC
Reverse Recovery Current	I _{RRM}			-	26	_	A

Notes

a. $C_{oss(er)}$ is a fixed capacitance that gives the same energy as C_{oss} while V_{DS} is rising from 0 % to 80 % V_{DSS} . b. $C_{oss(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 % to 80 % V_{DSS} .



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

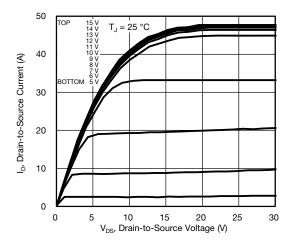


Fig. 1 - Typical Output Characteristics

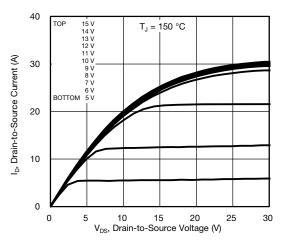


Fig. 2 - Typical Output Characteristics

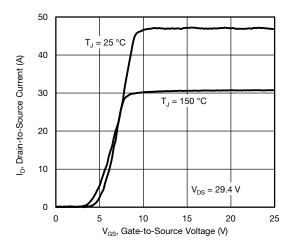


Fig. 3 - Typical Transfer Characteristics

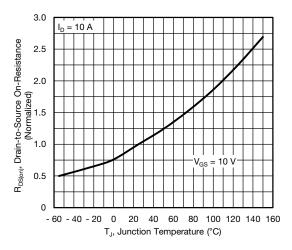


Fig. 4 - Normalized On-Resistance vs. Temperature

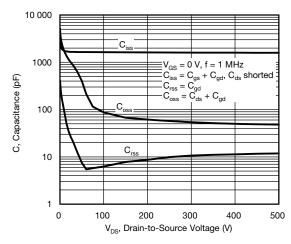


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

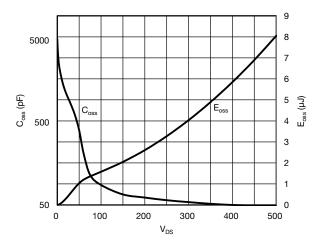


Fig. 6 - C_{oss} and E_{oss} vs. V_{DS}



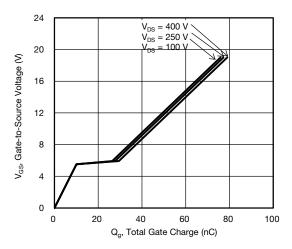


Fig. 7 - Typical Gate Charge vs. Gate-to-Source Voltage

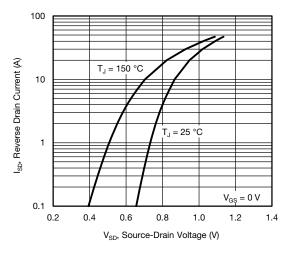


Fig. 8 - Typical Source-Drain Diode Forward Voltage

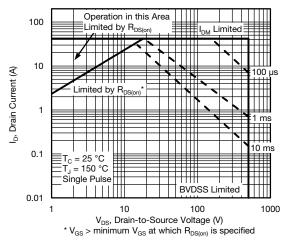


Fig. 9 - Maximum Safe Operating Area

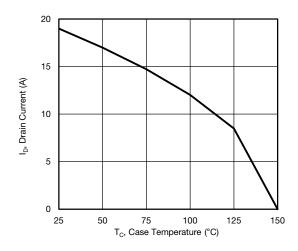


Fig. 10 - Maximum Drain Current vs. Case Temperature

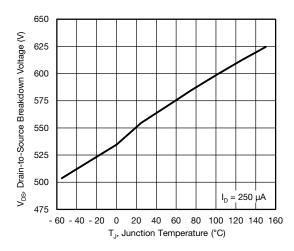


Fig. 11 - Temperature vs. Drain-to-Source Voltage



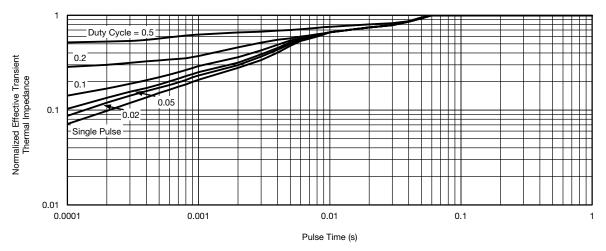


Fig. 12 - Normalized Thermal Transient Impedance, Junction-to-Case

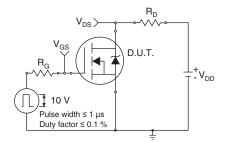


Fig. 13 - Switching Time Test Circuit

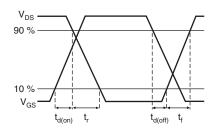


Fig. 14 - Switching Time Waveforms

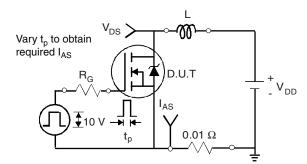


Fig. 15 - Unclamped Inductive Test Circuit

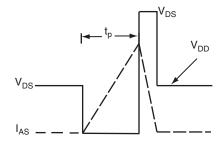


Fig. 16 - Unclamped Inductive Waveforms

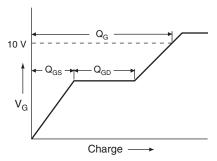


Fig. 17 - Basic Gate Charge Waveform

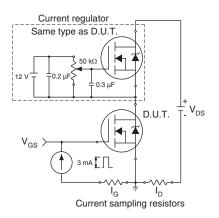
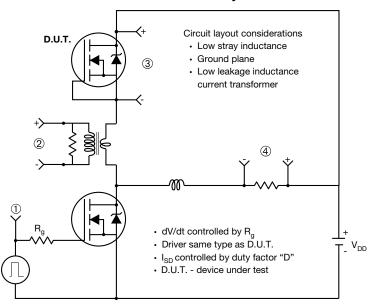


Fig. 18 - Gate Charge Test Circuit



Peak Diode Recovery dV/dt Test Circuit



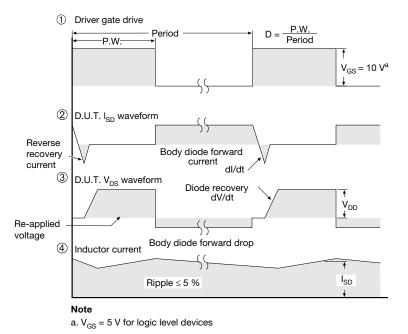


Fig. 19 - For N-Channel

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