SPICE Device Model SiHH26N60E



Vishay Siliconix

E Series Power MOSFET

DESCRIPTION

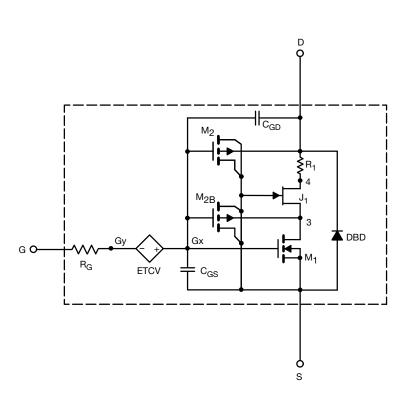
The attached SPICE model describes the typical electrical characteristics of the n-channel vertical DMOS. The subcircuit model is extracted and optimized over the 25 °C to 150 °C temperature ranges under the pulsed 0 V to 15 V gate drive. The saturated output impedance is best fit at the gate bias near the threshold voltage.

A novel gate-to-drain feedback capacitance network is used to model the gate charge characteristics while avoiding convergence difficulties of the switched $C_{gd}\xspace$ model. All model parameter values are optimized to provide a best fit to the measured electrical data and are not intended as an exact physical interpretation of the device.

CHARACTERISTICS

- N-channel vertical DMOS
- Macro model (subcircuit model)
- Level 3 MOS
- · Apply for both linear and switching application
- Accurate over the 25 °C to 150 °C temperature range
- · Model the gate charge

SUBCIRCUIT MODEL SCHEMATIC



Note

This document is intended as a SPICE modeling guideline and does not constitute a commercial product datasheet. Designers should refer to the appropriate datasheet of the same number for guaranteed specification limits.



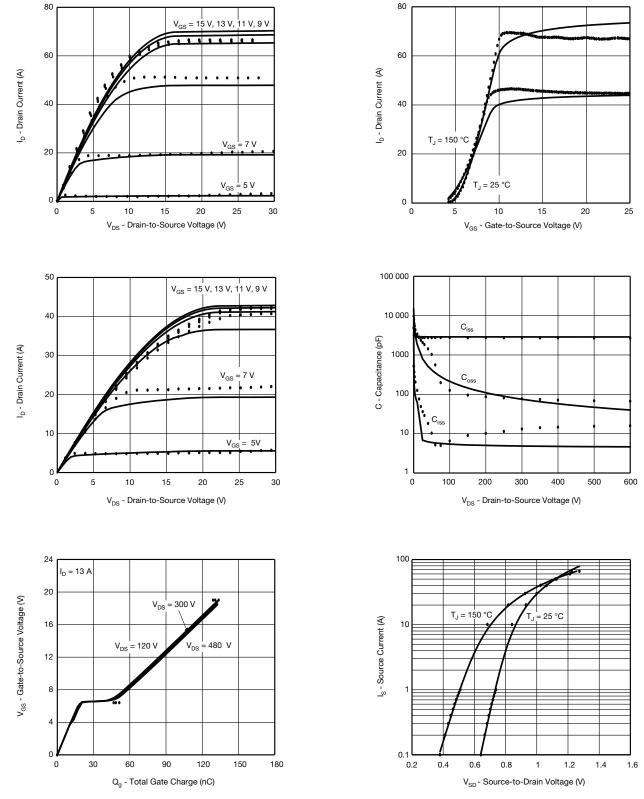
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SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	SIMULATED DATA	MEASURED DATA	UNIT
Static					
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	3	-	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V, I _D = 13 A	0.143	0.117	Ω
Forward Transconductance	g fs	$V_{DS} = 30 \text{ V}, \text{ I}_{D} = 13 \text{ A}$	10	8.6	S
Dynamic					
Input Capacitance	C _{iss}	V_{DS} = 100 V, V_{GS} = 0 V, f = 1 MHz	2870	2815	pF
Output Capacitance	Coss		196	125	
Reverse Transfer Capacitance	C _{rss}		5.4	7	
Total Gate Charge	Qg	V_{DS} = 480 V, V_{GS} = 10 V, I_{D} = 13 A	76	77	nC
Gate-Source Charge	Q _{gs}		18	18	
Gate-Drain Charge	Q _{gd}		33	33	
Drain-Source Body Diode Characteristics					
Diode Forward Voltage	V _{SD}	T_J = 25 °C, I_S = 13 A, V_{GS} = 0 V	0.90	0.90	V
Reverse Recovery Time	t _{rr}	$T_J = 25 \text{ °C}, I_F = I_S = 13 \text{ A},$ dI/dt = 100 A/µs, V _R = 25 V	460	459	ns
Reverse Recovery Charge	Q _{rr}		10.7	7.6	μC



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COMPARISON OF MODEL WITH MEASURED DATA (T_J = 25 °C, unless otherwise noted)



Note

• Dots and squares represent measured data. Copyright: Vishay Intertechnology, Inc.

S16-1676-Rev. A, 29-Aug-16

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Document Number: 91906

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