

# Standard Recovery Diodes, (Stud Version), 70 A



DO-5 (DO-203AB)

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub> 70 A				
Package	DO-5 (DO-203AB)			
Circuit configuration	Single			

#### **FEATURES**

- High surge current capability
- Designed for a wide range of applications



- Stud cathode and stud anode version
- Leaded version available
- Types up to 1600 V V<sub>RRM</sub>
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### TYPICAL APPLICATIONS

- Converters
- Power supplies
- Machine tool controls
- · Battery charges

MAJOR RATINGS AND CHARACTERISTICS					
DADAMETED	TEST CONDITIONS	70H	LINUTO		
PARAMETER	TEST CONDITIONS	10 TO 120	140/160	UNITS	
1		70	70	A	
I <sub>F(AV)</sub>	T <sub>C</sub>	140	110	°C	
I <sub>F(RMS)</sub>		110	110	A	
1	50 Hz	1200	1200	Δ.	
IFSM	60 Hz	1250	1250	A	
²t	50 Hz	7100	7100	A <sup>2</sup> s	
1-1	60 Hz	6450	6450	A-s	
V <sub>RRM</sub>	Range	100 to 1200	1400 to 1600	V	
T <sub>J</sub>		-65 to +180	-65 to +150	°C	

#### **ELECTRICAL SPECIFICATIONS**

VOLTAGE RATINGS					
TYPE NUMBER	VOLTAGE CODE	V <sub>RRM</sub> , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	V <sub>R(BR)</sub> , MINIMUM AVALANCHE VOLTAGE V	I <sub>RRM</sub> MAXIMUM AT T <sub>J</sub> = T <sub>J</sub> MAXIMUM mA
	10	100	200	200	
	20	200	300	300	15
	30	300	400	400	15
	40	400	500	500	
VS-70HF(R)	60	600	720	725	
V3-70HF(N)	80	800	960	950	9
	100	1000	1200	1150	9
	120	1200	1440	1350	
	140 1400 1650 1550		1550	4.5	
	160	1600	1900	1750	4.5



FORWARD CONDUCTION								
PARAMETER	CVMPOL	IPOL TEST CONDITIONS		ADOL TEST CONDITIONS		70HF(R)		LINUTO
FARAIVIE I ER	SYMBOL TEST CONDITIONS		10 to 120	140/160	UNITS			
Maximum average forward current	I <sub>F(AV)</sub>	I <sub>F(AV)</sub> 180° conduction, half sine wave		70		Α		
at case temperature	'F(AV)	100 001144	otion, nan omo		140	110	°C	
Maximum RMS forward current	I <sub>F(RMS)</sub>				110		Α	
		t = 10 ms	No voltage		1200 1250		A	
Maximum peak, one cycle forward,	1	t = 8.3 ms	reapplied	$V_{RRM}$ ied Sinusoidal half wave, initial $T_J = T_J$ maximum ied $V_{RRM}$				
non-repetitive surge current	I <sub>FSM</sub>	t = 10 ms	100 % V <sub>RRM</sub>		1000			
		t = 8.3 ms	reapplied		105	50		
	l <sup>2</sup> t	t = 10 ms	No voltage reapplied		7100		- A <sup>2</sup> s	
Maximum I <sup>2</sup> t for fusing		t = 8.3 ms			6450			
Maximum I-t for fusing		t = 10 ms	100 % V <sub>RRM</sub>		5000			
		t = 8.3 ms	reapplied		4550			
Maximum I <sup>2</sup> √t for fusing	I <sup>2</sup> √t	t = 0.1 ms to 10 ms, no voltage reapplied		71 C	00	A²√s		
Low level value of threshold voltage	V <sub>F(TO)1</sub>	(16.7 % x $\pi$ x $I_{F(AV)}$ < I < $\pi$ x $I_{F(AV)}$ ), $T_J = T_J$ maximum		(16.7 % x $\pi$ x $I_{F(AV)} < I < \pi$ x $I_{F(AV)}$ ), $T_J = T_J$ maximum 0.79		'9	V	
High level value of threshold voltage	V <sub>F(TO)2</sub>	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		1.00		V		
Low level value of forward slope resistance	r <sub>f1</sub>	(16.7 % x $\pi$ x I <sub>F(AV)</sub> < I < $\pi$ x I <sub>F(AV)</sub> ), T <sub>J</sub> = T <sub>J</sub> maximum		(16.7 % x $\pi$ x $I_{F(AV)}$ < I < $\pi$ x $I_{F(AV)}$ ), $T_J = T_J$ maximum 2.33		3	mΩ	
High level value of forward slope resistance	r <sub>f2</sub>	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		1.53		11122		
Maximum forward voltage drop	$V_{FM}$	$I_{pk}$ = 220 A, $T_J$ = 25 °C, $t_p$ = 400 µs rectangular wave			1.35	1.46	V	

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	70H	UNITS		
PANAMETEN	STIVIBUL	TEST CONDITIONS	10 to 120	140/160	ONTO	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +180	-65 to +150	°C	
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	R <sub>thJC</sub> DC operation		0.45		
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth, flat and greased	0.25			
		Not lubricated thread, tighting on nut (1)	3.4 (30)		N · m (lbf · in)	
Maximum allowable mounting torque		Lubricated thread, tighting on nut (1)	2.3 (20)			
(+0 %, -10 %)		Not lubricated thread, tighting on hexagon (2)	4.2 (37)			
		Lubricated thread, tighting on hexagon (2)	3.2	(28)		
Approvimenta usaight			1	7	g	
Approximate weight			0	.6	oz.	
Case style		See dimensions - link at the end of datasheet	DO-	5 (DO-203AB	)	

#### Notes

- (1) Recommended for pass-through holes
- (2) Recommended for holed threaded heatsinks

△R <sub>thJC</sub> CONDUCTION				
CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS
180°	0.08	0.06		
120°	0.10	0.11		
90°	0.13	0.14	$T_J = T_J$ maximum	K/W
60°	0.19	0.20		
30°	0.30	0.30		

#### Note

• The table above shows the increment of thermal resistance R<sub>thJC</sub> when devices operate at different conduction angles than DC

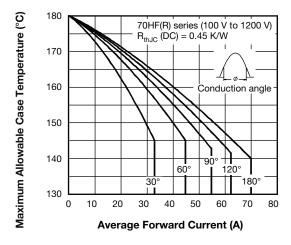


Fig. 1 - Current Ratings Characteristics

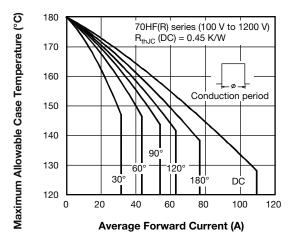


Fig. 2 - Current Ratings Characteristics

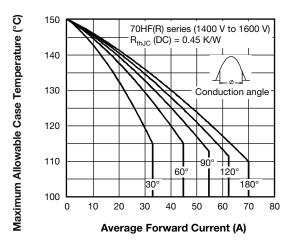


Fig. 3 - Current Ratings Characteristics

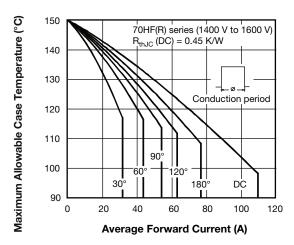


Fig. 4 - Current Ratings Characteristics

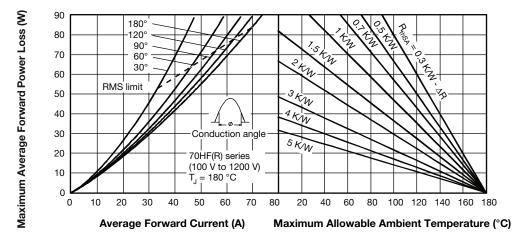


Fig. 5 - Forward Power Loss Characteristics



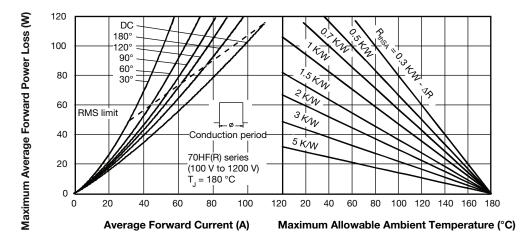


Fig. 6 - Forward Power Loss Characteristics

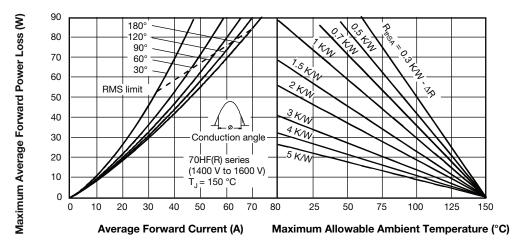


Fig. 7 - Forward Power Loss Characteristics

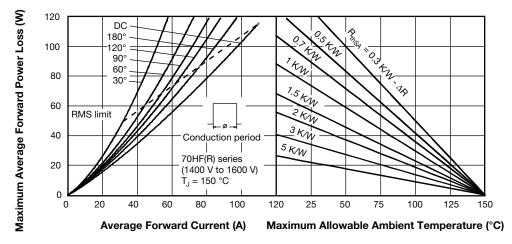


Fig. 8 - Forward Power Loss Characteristics

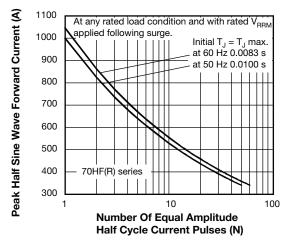


Fig. 9 - Maximum Non-Repetitive Surge Current

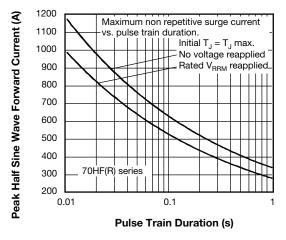


Fig. 10 - Maximum Non-Repetitive Surge Current

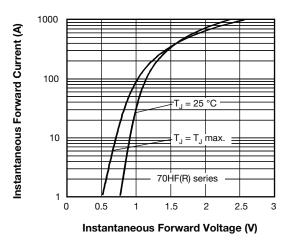


Fig. 11 - Forward Voltage Drop Characteristics

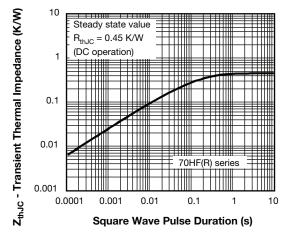


Fig. 12 - Thermal Impedance Z<sub>thJC</sub> Characteristics

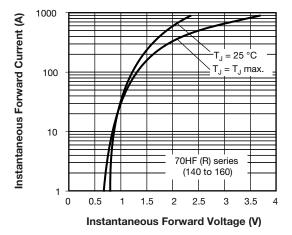


Fig. 13 - Forward Voltage Drop Characteristics



#### **ORDERING INFORMATION TABLE**

1 - Vishay Semiconductors product

2 - 70 = standard device

71 = not isolated lead

72 = isolated lead with silicone sleeve

(red = reverse polarity)

(blue = normal polarity)

- HF = standard diode

• None = stud normal polarity (cathode to stud)

• R = stud reverse polarity (anode to stud)

5 - Voltage code x 10 = V<sub>RRM</sub> (see Voltage Ratings table)

None = stud base DO-5 (DO-203AB) 1/4" 28UNF-2A

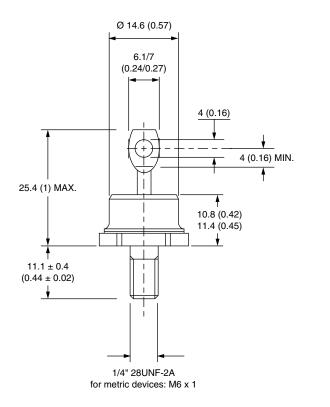
• M = stud base DO-5 (DO-203AB) M6 x 1

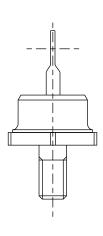
LINKS TO RELATED DOCUMENTS		
Dimensions	www.vishay.com/doc?95343	

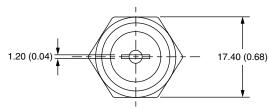


# DO-203AB (DO-5) for 70HF(R) and 71HF(R) Series

## **DIMENSIONS FOR 70HF(R) SERIES** in millimeters (inches)







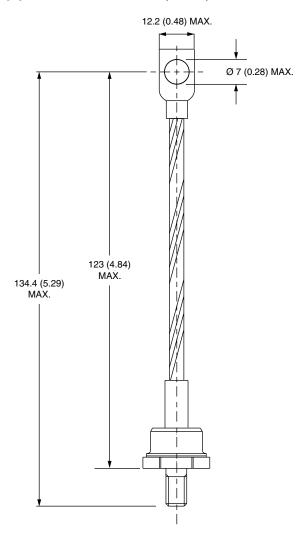
# **Outline Dimensions**

Vishay Semiconductors

DO-203AB (DO-5) for 70HF(R) and 71HF(R) Series



### **DIMENSIONS FOR 71HF(R) SERIES** in millimeters (inches)





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