VS-SD2500C..K Series

Vishay Semiconductors

Standard Recovery Diodes, (Hockey PUK Version), 3000 A



K-PUK (DO-200AC)

PRIMARY CHARACTERISTICS					
I _{F(AV)}	3000 A				
Package	K-PUK (DO-200AC)				
Circuit configuration	Single				

FEATURES

- Wide current range
- High voltage ratings up to 2500 V
- High surge current capabilities
- Diffused junction
- Hockey PUK version
- Case style K-PUK (DO-200AC)
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- Converters
- Power supplies
- Machine tool controls
- High power drives
- Medium traction applications

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
1		3000	A		
I _{F(AV)}	T _{hs}	55	°C		
I _{F(RMS)}		5000	A		
	T _{hs}	25	°C		
I _{FSM}	50 Hz	31 000	٨		
	60 Hz	32 460	— A		
l ² t	50 Hz	4810	——————————————————————————————————————		
I-t	60 Hz	4390	KA-S		
V _{RRM}	Range	1200 to 2500	V		
TJ		-40 to +180	°C		

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS								
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} MAXIMUM AT T _J = 180 °C mA				
	12	1200	1300					
	16	1600	1700					
VS-SD2500CK	20	2000	2100	75				
	24	2400	2500					
	25	2500	2600					

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FORWARD CONDUCTION							
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS	
Maximum average forward current at heatsink temperature	I _{F(AV)}	180° conduction, half sine wave Double side (single side) cooled			3000 (1550) 55 (85)	A °C	
Maximum RMS forward current	I _{F(RMS)}	25 °C heat	sink temperatu	re double side cooled	5000		
	. (t = 10 ms	No voltage		31 000		
Maximum peak, one-cycle forward,		t = 8.3 ms	reapplied	-	32 460	А	
non-repetitive surge current	I _{FSM}	t = 10 ms	100 % V _{RRM}		26 050		
		t = 8.3 ms	reapplied	Sinusoidal half wave, initial T _J = T _J maximum	27 300		
Maximum I ² t for fusing	l ² t	t = 10 ms	No voltage reapplied		4810	kA ² s	
		t = 8.3 ms			4390		
		t = 10 ms	100 % V _{RRM}		3400		
		t = 8.3 ms	reapplied		3100		
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied			48 100	kA²√s	
Low level value of threshold voltage	V _{F(TO)1}	(16.7 % x π x I _{F(AV)} < I < π x I _{F(AV)}), T _J = T _J maximum			0.76	V	
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$			0.97	v	
Low level value of forward slope resistance	r _{f1}	(16.7 % x π x $I_{F(AV)} < I < \pi$ x $I_{F(AV)}$), $T_J = T_J$ maximum			0.16	mW	
High level value of forward slope resistance	r _{f2}	$(I > \pi \times I_{F(AV)}), T_J = T_J maximum$			0.13	11174	
Maximum forward voltage drop	V _{FM}	$I_{pk} = 4000 \text{ A}, T_J = T_J \text{ maximum}$ $t_p = 10 \text{ ms sinusoidal wave}$			1.41	V	

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction operating temperature range	TJ		-40 to +180	°C	
Maximum storage temperature range	T _{Stg}		-55 to +200		
Maximum thermal resistance,	Б	DC operation single side cooled	0.042	K/W	
junction to heatsink	R _{thJ-hs}	DC operation double side cooled	0.020	r\/ VV	
Mounting force, ± 10 %			22 250 (2250)	N (kg)	
Approximate weight			425	g	
Case style		See dimensions - link at the end of datasheet	K-PUK (DO-200AC)		

CONDUCTION ANGLE	SINUSOIDAL C	ONDUCTION	RECTANGULA	R CONDUCTION	TEST CONDITIONS	UNITS	
CONDUCTION ANGLE	SINGLE SIDE	DOUBLE SIDE	SINGLE SIDE	DOUBLE SIDE	TEST CONDITIONS		
180°	0.002	0.002	0.001	0.001		K/W	
120°	0.002	0.002	0.002	0.002			
90°	0.003	0.003	0.003	0.003	$T_J = T_J$ maximum		
60°	0.004	0.004	0.004	0.004			
30°	0.007	0.007	0.007	0.007			

Note

• The table above shows the increment of thermal resistance R_{thJ-hs} when devices operate at different conduction angles than DC



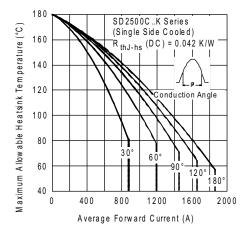


Fig. 1 - Current Ratings Characteristics

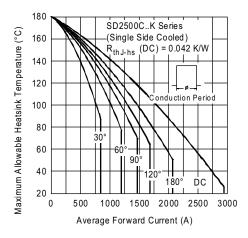


Fig. 2 - Current Ratings Characteristics

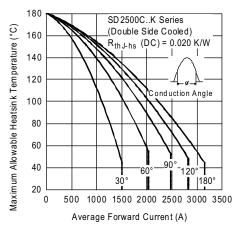


Fig. 3 - Current Ratings Characteristics

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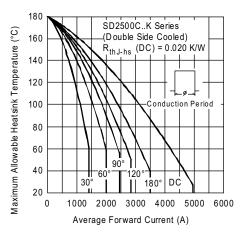


Fig. 4 - Current Ratings Characteristics

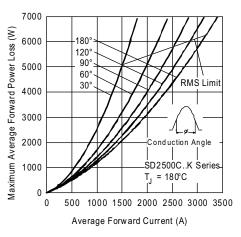


Fig. 5 - Forward Power Loss Characteristics

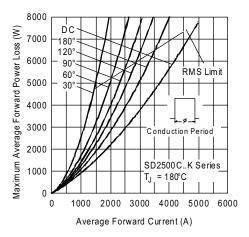


Fig. 6 - Forward Power Loss Characteristics

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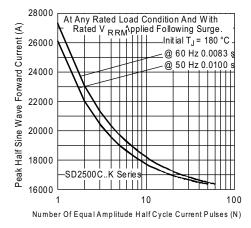


Fig. 7 - Maximum Non-Repetitive Surge Current Single and Double Side Cooled



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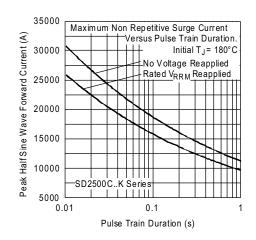


Fig. 8 - Maximum Non-Repetitive Surge Current Single and Double Side Cooled

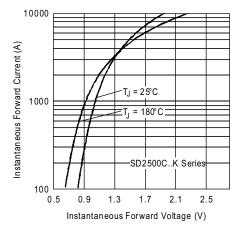


Fig. 9 - Forward Voltage Drop Characteristics

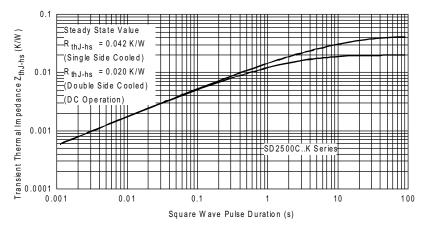


Fig. 10 - Thermal Impedance Z_{thJ-hs} Characteristics

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ORDERING INFORMATION TABLE

Device code	VS-	SD	250	0	С	25	к	
		2	3	4	5	6	7	
	1 -	Vishay Semiconductors product						
	2 -	- Diode						
	3 -	- Essential part number						
	4 -	- 0 = standard recovery						
	5 -	- C = ceramic PUK						
	6 -	Volt	Voltage code x 100 = V _{RRM} (see Voltage Ratings table					
	7 -	K =	PUK ca	ise K-PL	JK (DO-	200AC))	

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95247				

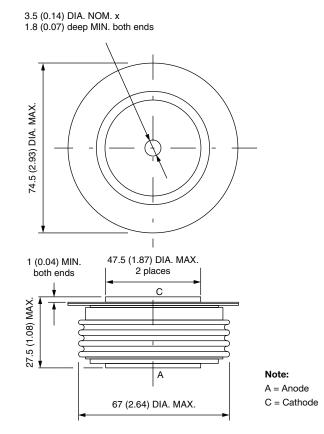
Outline Dimensions





K-PUK (DO-200AC)

DIMENSIONS in millimeters (inches)



Quote between upper and lower pole pieces has to be considered after application of mounting force (see Thermal and Mechanical Specifications)



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