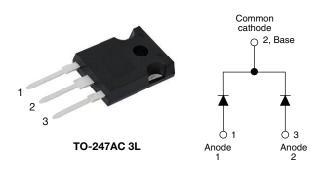
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Ultrafast Rectifier, 2 x 15 A FRED Pt<sup>®</sup>



PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	2 x 15 A							
V <sub>R</sub>	400 V							
V <sub>F</sub> at I <sub>F</sub>	0.93 V							
t <sub>rr</sub> typ.	See Recovery table							
T <sub>J</sub> max.	175 °C							
Package	TO-247AC 3L							
Circuit configuration	Common cathode							

### **FEATURES**

- · Ultrafast recovery time
- · Low forward voltage drop
- 175 °C operating junction temperature
- · Low leakage current
- · Designed and gualified according to JEDEC<sup>®</sup>-JESD 47



HALOGEN

FREE

• Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

FRED Pt® series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Peak repetitive reverse voltage	V <sub>RRM</sub>		400	V					
Average rectified forward currentper leg	I <sub>F(AV)</sub>		15						
total device		Rated V <sub>R</sub> , T <sub>C</sub> = 149 °C	30	•					
Non-repetitive peak surge current per leg	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C, t <sub>p</sub> = 10 ms	200	A					
Peak repetitive forward current per leg	I <sub>FRM</sub>	Rated $V_R$ , $T_C$ = 149 °C, square wave, 20 kHz	30						
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C					

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	400	-	-				
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 15 A	-	1.17	1.25	V			
		I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	0.93	1.12				
	I <sub>R</sub>	$V_{R} = V_{R}$ rated	-	0.3	10				
Reverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	30	500	μA			
Junction capacitance	CT	C <sub>T</sub> V <sub>R</sub> = 400 V		28	-	pF			
Series inductance	Ls	Measured lead to lead 5 mm from package body	-	12	-	nH			

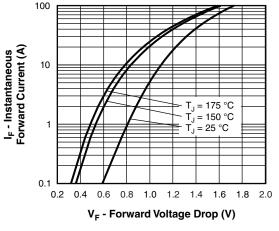
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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS			
Reverse recovery time	t <sub>rr</sub>	$I_F = 1 \text{ A}, \ dI_F/dt = 50$	-	36	60				
		T <sub>J</sub> = 25 °C		-	46	-	ns - A		
		T <sub>J</sub> = 125 °C		-	80	-			
Deels receivers ourrent	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	$I_F = 15 A$	-	3.6	-			
Peak recovery current		T <sub>J</sub> = 125 °C	dI <sub>F</sub> /dt = 200 A/µs V <sub>B</sub> = 200 V	-	8.7	-			
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	11	-	84	-			
		T <sub>J</sub> = 125 °C		-	345	-	nC		

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C				
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	0.8	1.5					
Thermal resistance, junction to ambient per leg		Typical socket mount	-	-	40	°C/W				
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.4	-	-				
Weight			-	6.0	-	g				
weight			-	0.21	-	oz.				
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)				
Marking device		Case style TO-247AC 3L	30CPU04							





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Fig. 1 - Typical Forward Voltage Drop Characteristics

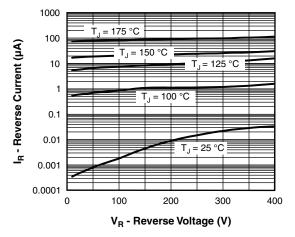


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

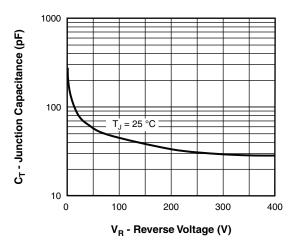


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

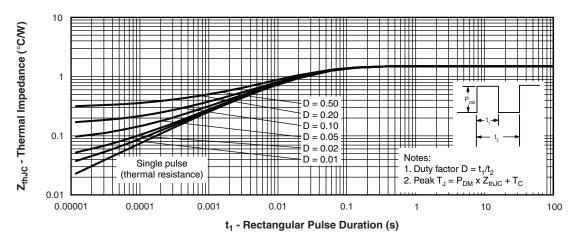
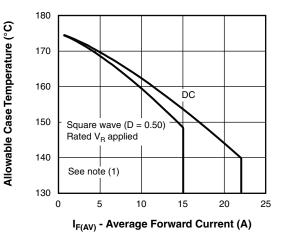


Fig. 4 - Maximum Thermal Impedance  $Z_{\text{thJC}}$  Characteristics

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Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

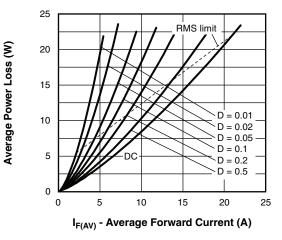


Fig. 6 - Forward Power Loss Characteristics

#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;
- Pd = forward power loss =  $I_{F(AV)} \times V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6); Pd<sub>REV</sub> = inverse power loss =  $V_{R1} \times I_R$  (1 - D);  $I_R$  at  $V_{R1}$  = rated  $V_R$

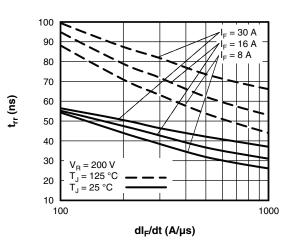
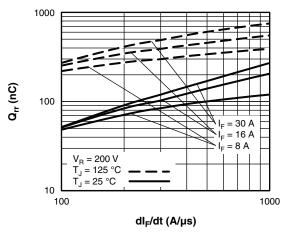
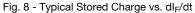


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt





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# VS-30CPU04-N3

## **Vishay Semiconductors**

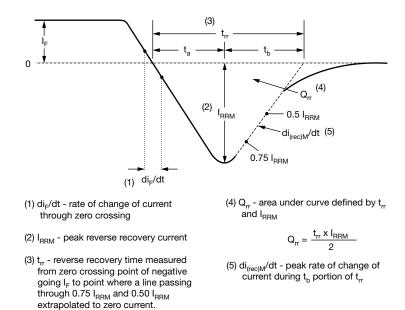
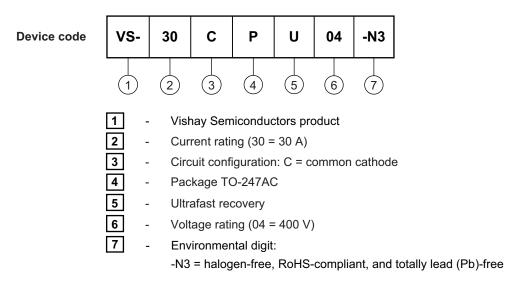


Fig. 9 - Reverse Recovery Waveform and Definitions

### **ORDERING INFORMATION TABLE**

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ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-30CPU04-N3	25	500	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS							
Dimensions <u>www.vishay.com/doc?96138</u>							
Part marking information	www.vishay.com/doc?95007						

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TO-247AC 3L

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDOL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	5 BSC	
b1	0.99	1.35	0.039	0.053			ØК	0.2	254	0.0	)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			ØΡ	3.56	3.66	0.14	0.144	
b5	2.59	3.38	0.102	0.133			Ø P1	-	7.39	-	0.291	
С	0.38	0.89	0.015	0.035			Q	5.31	5.69	0.209	0.224	
c1	0.38	0.84	0.015	0.033			R	4.52	5.49	0.178	0.216	
D	19.71	20.70	0.776	0.815	3		S	5.51	BSC	0.217	' BSC	
D1	13.08	-	0.515	-	4							

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

<sup>(5)</sup> Lead finish uncontrolled in L1

<sup>(6)</sup> Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-247 with exception of dimension Q

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