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AAP Gen 7 (TO-240AA) Power Modules Standard Diodes, 60 A



PRIMARY CHARACTERISTICS						
I _{F(AV)}	60 A					
Туре	Modules - diode, high voltage					
Package	AAP Gen 7 (TO-240AA)					
Circuit configuration	Two diodes doubler circuit, two diodes common cathode, two diodes common anode, single diode					

MECHANICAL DESCRIPTION

The AAP Gen 7 (TO-240AA), new generation of AAP module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

FEATURES

- · High voltage
- Industrial standard package
- · Low thermal resistance
- UL approved file E78996
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- Up to 1600 V
- · High surge capability
- · Easy mounting on heat sink

ELECTRICAL DESCRIPTION

These modules are intended for general purpose high voltage applications such as high voltage regulated power supplies, lighting circuits, temperature and motor speed control circuits, UPS and battery charger.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
1		60	A				
I _{F(AV)}	T _C	114	°C				
I _{F(RMS)}		94					
I _{FSM}	50 Hz	1300	Α				
	60 Hz	1360					
l ² t	50 Hz	8.44	kA ² s				
1-1	60 Hz	7.68	KA-S				
$I^2\sqrt{t}$		84.5	kA ² √s				
V_{RRM}	Range	400 to 1600	V				
T _{Stg}		-40 to +150	°C				
T _J		-40 to +150	°C				

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ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS								
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} MAXIMUM AT T _J = 150 °C mA				
	04	400	500					
	06	600	700					
	08	800	900					
VS-VSK.56	10	1000	1100	10				
	12	1200	1300					
	14	1400	1500					
	16	1600	1700					

FORWARD CONDUCTION						
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS
Maximum average forward current	I _{F(AV)}	180° condu	ction, half sine	60	А	
at case temperature	'F(AV)	100 001100	ction, nan sinc	wave	114	°C
Maximum RMS forward current	I _{F(RMS)}				94	
		t = 10 ms	No voltage		1300	
Maximum peak, one-cycle forward,		t = 8.3 ms	reapplied		1360	Α
non-repetitive surge current	I _{FSM}	t = 10 ms	100 % V _{RRM}		1090	
		t = 8.3 ms	reapplied	Sinusoidal half wave, initial $T_J = T_J$ maximum	1140	
	l ² t	t = 10 ms	No voltage		8.44	
Manipular 124 for fraince		t = 8.3 ms	reapplied		7.68	kA ² s
Maximum I ² t for fusing		t = 10 ms	100 /0 VRRM		5.97	KA ² S
		t = 8.3 ms			5.43	l
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms t	t = 0.1 ms to 10 ms, no voltage reapplied			kA²√s
Low level value of threshold voltage	V _{F(TO)1}	(16.7 % x π	$\times I_{F(AV)} < I < \pi \times I_{F(AV)}$	$I_{F(AV)}$, $T_J = T_J$ maximum	0.74	V
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi \times I_{F(AV)})$	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$			V
Low level value of forward slope resistance	r _{f1}	(16.7 % x π x $I_{F(AV)} < I < \pi$ x $I_{F(AV)}$), $T_J = T_J$ maximum			3.94	mΩ
High level value of forward slope resistance	r _{f2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$			3.43	11152
Maximum forward voltage drop	V_{FM}	$I_{FM} = \pi \times I_{F(I)}$	AV), T _J = 25 °C,	t _p = 400 µs square wave	1.6	V

BLOCKING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum peak reverse leakage current	I _{RRM}	T _J = 150 °C	10	mA
Maximum RMS insulation voltage	V _{INS}	50 Hz	3000 (1 min) 3600 (1 s)	V



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THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS		
Junction and storage temp	erature range	T _J , T _{Stg}		-40 to +150	°C		
Maximum internal thermal resistance, junction to case per leg		R _{thJC}	DC operation	0.33	9CAM		
Typical thermal resistance, case to heat sink per module		R _{thCS}	Mounting surface flat, smooth and greased	0.1	°C/W		
	to heatsink		A mounting compound is recommended and the	4			
Mounting torque ± 10 %	busbar		torque should be rechecked after a period of 3 hours to allow for the spread of the compound.	3	Nm		
Approximate weight				75	g		
Approximate weight				2.7	OZ.		
Case style			JEDEC®	AAP Gen 7	(TO-240AA)		

△R CONDUCTION PER JUNCTION											
DEVICES	8	SINE HALF WAVE CONDUCTION					RECTANGULAR WAVE CONDUCTION				LINUTO
DEVICES	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	UNITS
VSK.56	0.115	0.136	0.173	0.236	0.346	0.09	0.145	0.185	0.243	0.349	°C/W

Note

• Table shows the increment of thermal resistance RthJC when devices operate at different conduction angles than DC

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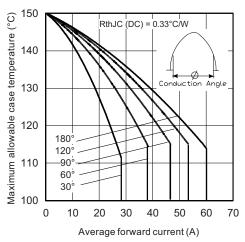


Fig. 1 - Current Ratings Characteristics

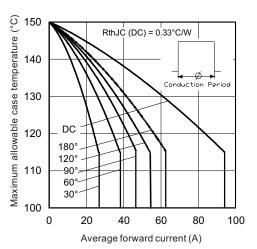


Fig. 2 - Current Ratings Characteristics

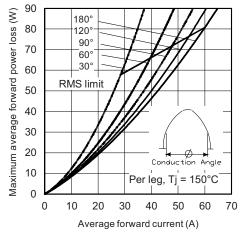


Fig. 3 - Forward Power Loss Characteristics

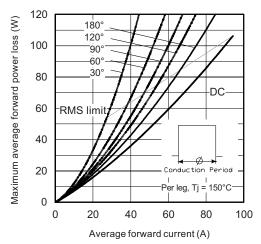


Fig. 4 - Forward Power Loss Characteristics

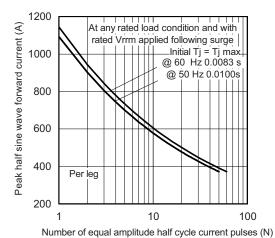


Fig. 5 - Maximum Non-Repetitive Surge Current

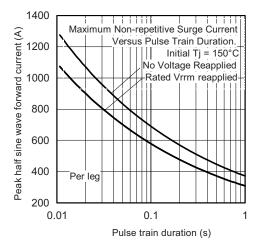


Fig. 6 - Maximum Non-Repetitive Surge Current

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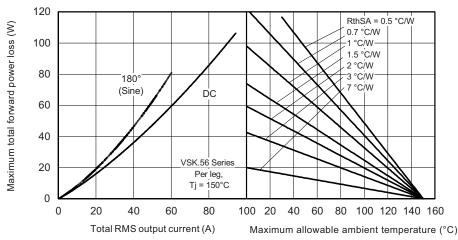


Fig. 7 - Forward Power Loss Characteristics

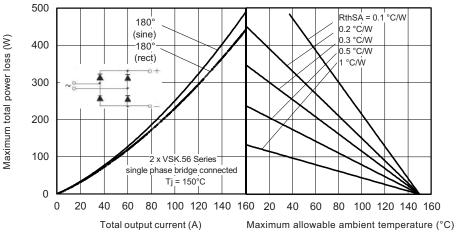


Fig. 8 - Forward Power Loss Characteristics

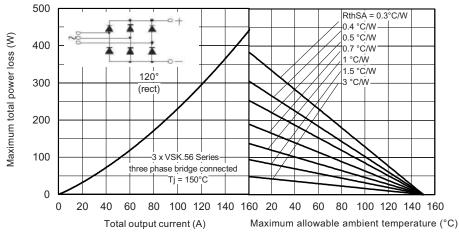


Fig. 9 - Forward Power Loss Characteristics

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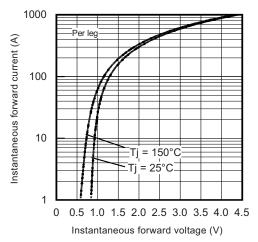


Fig. 10 - Forward Voltage Characteristics

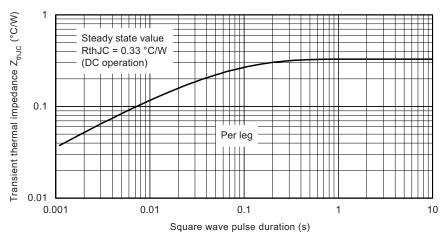
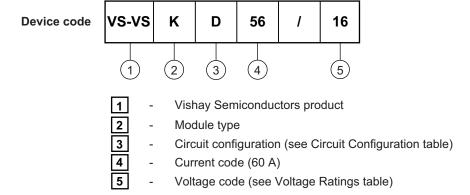


Fig. 11 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE



Note

• To order the optional hardware go to www.vishay.com/doc?95172

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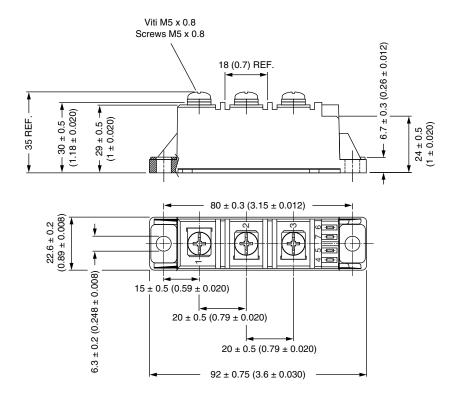
CIRCUIT CONFIGURATION		
CIRCUIT DESCRIPTION	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING
Two diodes doubler circuit	D	VSKD (1) ~ (2) ~ (3)
Two diodes common cathode	С	VSKC (1) - (2) (3)
Two diodes common anode	J	VSKJ (1) - + (2) - (3)
Single diode	E	VSKE (1) 0

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95369			

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ADD-A-PAK Generation VII - Diode

DIMENSIONS in millimeters (inches)





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