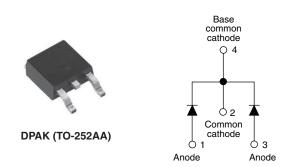
Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 3.5 A



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PRIMARY CHARACTERISTICS			
I _{F(AV)}	2 x 3.5 A		
V _R	60 V		
V _F at I _F	See Electrical table		
I _{RM}	30 mA at 125 °C		
T _J max.	150 °C		
E _{AS}	6 mJ		
Package	DPAK (TO-252AA)		
Circuit configuration	Common cathode		

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Popular D-PAK outline
- Center tap configuration
- Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-6CWQ06FNHM3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	7	A	
V _{RRM}		60	V	
I _{FSM}	t _p = 5 μs sine	490	A	
V _F	$3 A_{pk}, T_J = 25 \ ^{\circ}C \ (per \ leg)$	0.61	V	
TJ	Range	-40 to +150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-6CWQ06FNHM3	UNITS	
Maximum DC reverse voltage	V _R	60	V	
Maximum working peak reverse voltage	V _{RWM}	80	v	

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average forward per leg	50.0% dute quele et T = 100.00 restance de sur seferer			EQ.0/ duty system at T 122 °C restance your second		3.5	
current, see fig. 5 per device	IF(AV)	$I_{F(AV)}$ 50 % duty cycle at T _C = 133 °C, rectangular waveform		7			
Maximum peak one cycle non-repetitive	1	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	490	A		
surge current, see fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse		70			
Non-repetitive avalanche energy per leg E _{AS}		T _J = 25 °C, I _{AS} = 1 A, L = 12 mH		6	mJ		
Repetitive avalanche current per legIARCurrent decaying linearly to zero in 1 μ sFrequency limited by TJ maximum VA = 1.5 x VB typical		1	А				

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V (1)	3 A	T _J = 25 °C	0.61	V
Maximum forward voltage drop per		6 A		0.76	
leg, see fig. 1	V _{FM} ⁽¹⁾	3 A	T 105 %O	0.53	
		6 A	T _J = 125 °C		
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V - Roted V	2	mA
per leg, see fig. 2	IRM ("	$T_J = 125 \text{ °C}$ $V_R = \text{Rated } V_R$		30	ШA
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.38	V
Forward slope resistance	r _t			34.31	mΩ
Typical junction capacitance per leg	CT	V_R = 5 V_{DC} , (test signal range 100 kHz to 1 MHz), 25 °C		145	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 $\,\%$

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature	range T _J ⁽¹⁾ , T _{Stg}		-40 to +150	°C
Maximum thermal resistance, p	ber leg	DC operation	4.70	°C/W
junction to case per c	device R _{thJC}	See fig. 4	2.35	0/10
Approvimento veright			0.3	g
Approximate weight			0.01	oz.
Marking device		Case style DPAK	6CWQ0	06FNH

Note

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink (1)



VS-6CWQ06FNHM3

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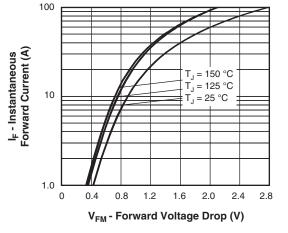


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

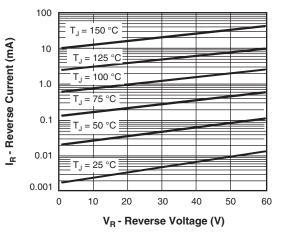


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

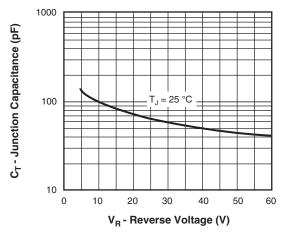


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

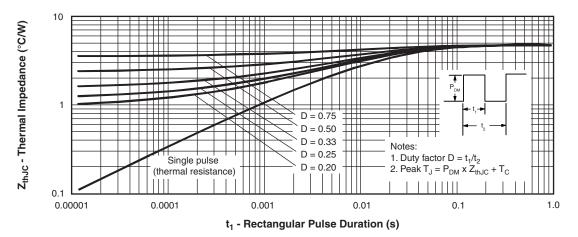
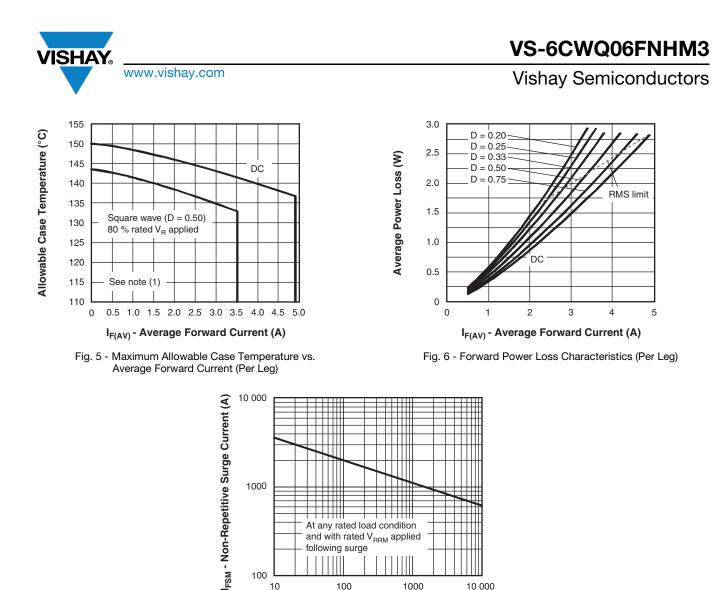


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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t_p - Square Wave Pulse Duration (μs) Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

1000

10 000

At any rated load condition and with rated $\mathrm{V}_{\mathrm{RRM}}$ applied

following surge

100

Note

- (1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;
- Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = 80 % rated V_R

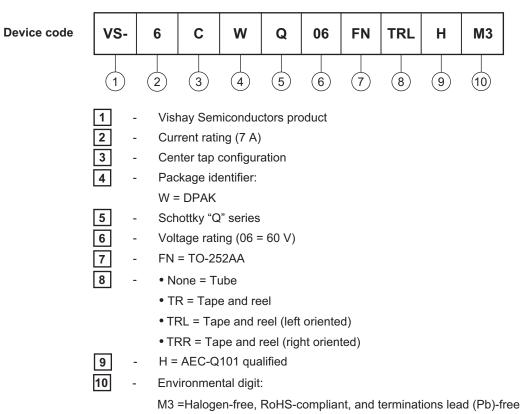
1000

100 10

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ORDERING INFORMATION TABLE

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ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-6CWQ06FNHM3	75	3000	Antistatic plastic tube	
VS-6CWQ06FNTRHM3	2000	2000	13" diameter reel	
VS-6CWQ06FNTRRHM3	3000	3000	13" diameter reel	
VS-6CWQ06FNTRLHM3	3000	3000	13" diameter reel	

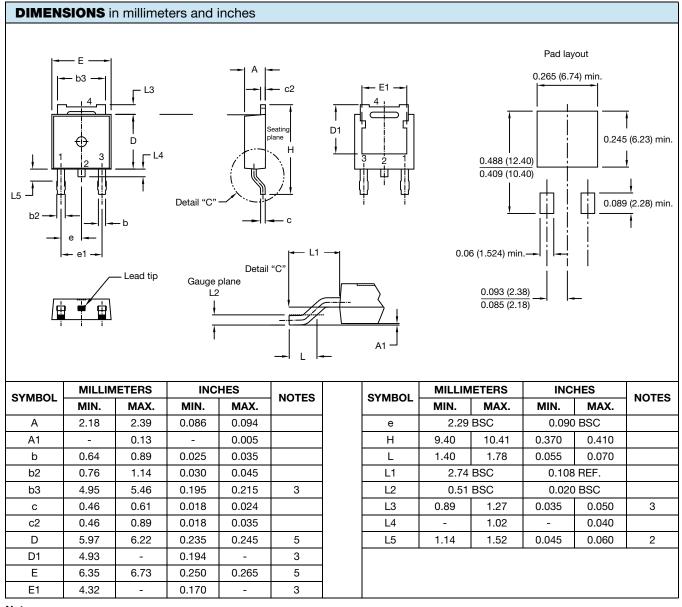
LINKS TO RELATED DOCUMENTS			
Dimensions www.vishay.com/doc?95519			
Part marking information	www.vishay.com/doc?95518		
Packaging information	www.vishay.com/doc?95033		
SPICE model	www.vishay.com/doc?96651		

Outline Dimensions



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DPAK (TO-252AA)



Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁵⁾ Outline conforms to JEDEC[®] outline TO-252AA, except for D1 dimension



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