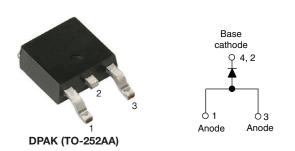


RoHS

COMPLIANT **HALOGEN**

FREE

High Performance Schottky Rectifier, 3.5 A



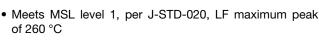
PRIMARY CHARACTERISTICS					
I _{F(AV)} 3.5 A					
V_{R}	100 V				
V _F at I _F	See Electrical table				
I _{RM}	4.9 mA at 125 °C				
T _J max.	150 °C				
E _{AS}	5 mJ				
Circuit configuration	Single				
Package	DPAK (TO-252AA)				

FEATURES

- Low forward voltage drop
- · Guard ring for enhanced ruggedness and long term reliability



- · Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



DESCRIPTION

The VS-30WQ10FNHM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	3.5	Α		
V_{RRM}		100	V		
I _{FSM}	t _p = 5 μs sine	440	Α		
V _F	3 A _{pk} , T _J = 125 °C	0.63	V		
T _J		-40 to +150	°C		

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-30WQ10FNHM3	UNITS			
Maximum DC reverse voltage	V_R	V _R				
Maximum working peak reverse voltage V _{RWM}						

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	BOL TEST CONDITIONS VALUES U				
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 135 °C, rectangular waveform		3.5		
Maximum peak one cycle non-repetitive surge current	l=o	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	440	Α	
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	70		
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 10 mH		5.0	mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		0.5	Α	



ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
		3 A	T _{.1} = 25 °C	0.81			
Maximum forward voltage drop	V _{FM} ⁽¹⁾	6 A	1J=25 C	0.96	.,		
See fig. 1	VFM (')	3 A	T _{.1} = 125 °C	0.63	V		
		6 A	1j = 125 C	0.74	1		
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	1	- mA		
See fig. 2		T _J = 125 °C	V _R = nateu V _R	4.9			
Threshold voltage	V _{F(TO)}	T - T movimum		0.48	V		
Forward slope resistance	r _t	ij = ij maximum	$T_J = T_J$ maximum		m $Ω$		
Typical junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		92	pF		
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		5.0	nΗ		
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs		

Note

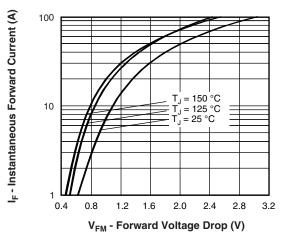
 $^{^{(1)}\,}$ Pulse width $<300~\mu s,$ duty cycle <2~%

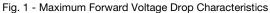
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-40 to +150	°C	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	4.7	°C/W	
Approximate weight			0.3	g	
Approximate weight			0.01	OZ.	
Marking device		Case style DPAK (TO-252AA)	30WQ	10FNH	

Note

(1)
$$\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$$
 thermal runaway condition for a diode on its own heatsink







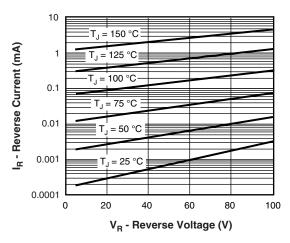


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

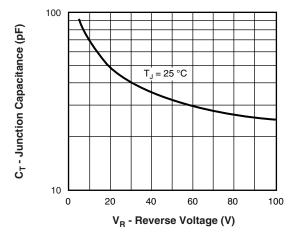


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

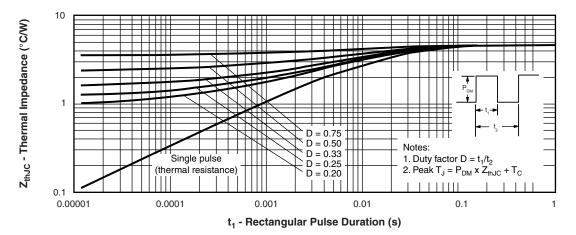


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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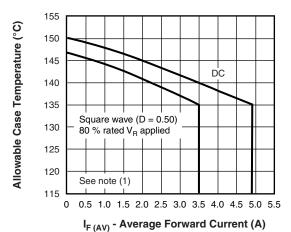


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

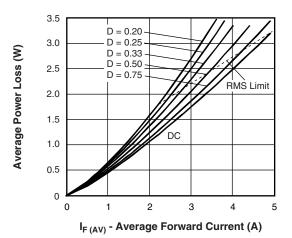


Fig. 6 - Forward Power Loss Characteristics

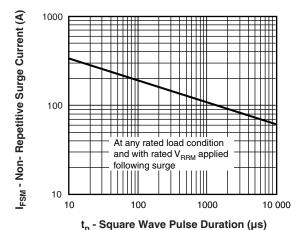


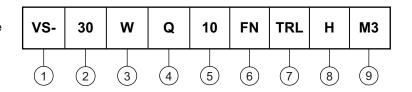
Fig. 7 - Maximum Non-Repetitive Surge Current

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (3.5 A)

Package identifier:

W = DPAK

4 - Schottky "Q" series

Voltage rating (10 = 100 V)

6 - FN = TO-252AA (DPAK)

7 - • None = tube

• TR = tape and reel

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

8 - H = AEC-Q101 qualified

9 - Environmental digit:

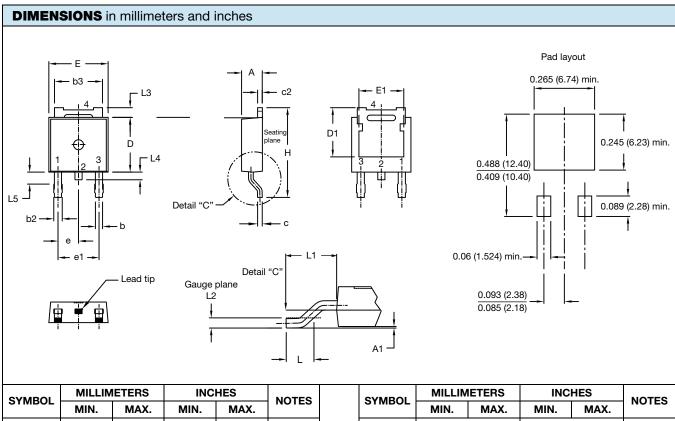
M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-30WQ10FNHM3	75	3000	Antistatic plastic tube			
VS-30WQ10FNTRHM3	2000	2000	13" diameter reel			
VS-30WQ10FNTRRHM3	3000	3000	13" diameter reel			
VS-30WQ10FNTRLHM3	3000	3000	13" diameter reel			

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95519</u>				
Part marking information	www.vishay.com/doc?95518			
Packaging information	www.vishay.com/doc?95033			
SPICE model	www.vishay.com/doc?95650			



DPAK (TO-252AA)



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	2.18	2.39	0.086	0.094	
A1	-	0.13	-	0.005	
b	0.64	0.89	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	3
С	0.46	0.61	0.018	0.024	
c2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	5
D1	4.93	-	0.194	-	3
Е	6.35	6.73	0.250	0.265	5
E1	4.32	-	0.170	-	3

SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
е	2.29	BSC	0.090	BSC	
Н	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74 BSC		0.108	REF.	
L2	0.51	0.51 BSC		BSC	
L3	0.89	1.27	0.035	0.050	3
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (5) Outline conforms to JEDEC® outline TO-252AA, except for D1 dimension



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