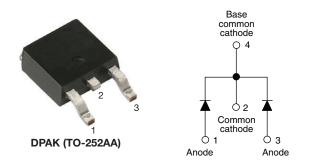
Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 6 A



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PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 6 A			
V _R	100 V			
V _F at I _F	0.65 V			
I _{RM}	4 mA at 125 °C			
T _J max.	150 °C			
E _{AS}	6 mJ			
Circuit configuration	Common cathode			
Package	DPAK (TO-252AA)			

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Popular DPAK outline
- Center tap configuration
- Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-12CWQ10FNHM3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	12	A			
V _{RRM}		100	V			
I _{FSM}	t _p = 5 μs sine	330	А			
V _F	6 A _{pk} , T _J = 125 °C (per leg)	0.65	V			
TJ	Range	-55 to +150	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-12CWQ10FNHM3	UNITS		
Maximum DC reverse voltage	V _R	100	V		
Maximum working peak reverse voltage	V _{RWM}	100	v		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDI	TIONS	VALUES	UNITS
Maximum average	per leg		50 % duty cycle at T_C = 135 °C, rectangular waveform		6	А
	er device	I _{F(AV)}			12	~
Maximum peak one cycle			5 µs sine or 3 µs rect. pulse	Following any rated	330	
non-repetitive surge current per See fig. 7	leg	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	110	A
Non-repetitive avalanche energy per leg E _{AS}		E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 12 mH		6	mJ
Repetitive avalanche current per leg		Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	А	

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 For technical questions within your region: DiodesAsia@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com

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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS		
		6 A	T ₁ = 25 °C	0.80	v		
Maximum forward	V _{FM} ⁽¹⁾	12 A	1j=23 0	0.95			
voltage drop per leg See fig. 1	VFM (*)	6 A	T _J = 125 °C	0.65			
5		12 A	1j = 125 C	0.78			
Maximum reverse	I _{RM} ⁽¹⁾	T _J = 25 °C		1	mA		
leakage current per leg See fig. 2	IRM ("	T _J = 125 °C	V _R = Rated V _R	4			
Threshold voltage	V _{F(TO)}	T T maximum	0.47	V			
Forward slope resistance	r _t	$T_J = T_J maximum$	mΩ				
Typical junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz), 25 °C 183 p			pF		
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 5.0 nH			nH		

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance,	per leg	D	DC operation	3.0	°C/W
junction to case	per device	R _{thJC}	See fig. 4	1.5	0/11
Approvimeto weight				0.3	g
Approximate weight				0.01	oz.
Marking device			Case style DPAK (TO-252AA)	12CWQ	10FNH

Note

⁽¹⁾ $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



VS-12CWQ10FNHM3

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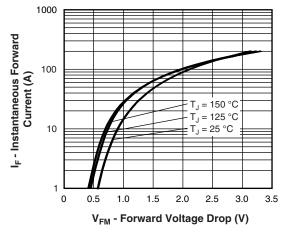
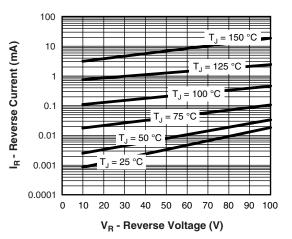
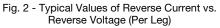


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)





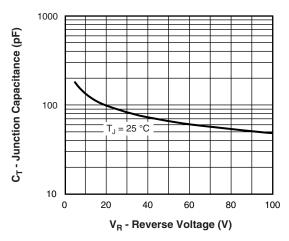
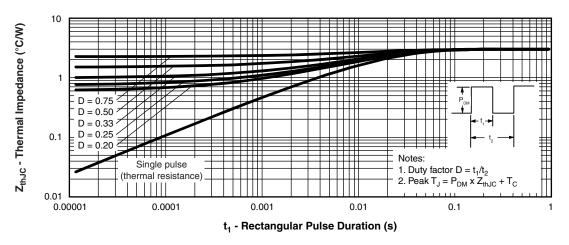
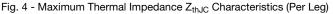


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





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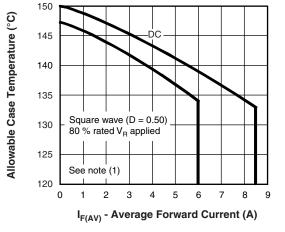
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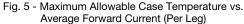
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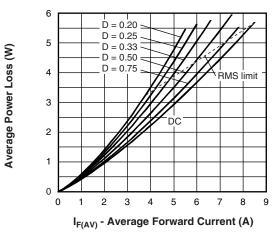


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

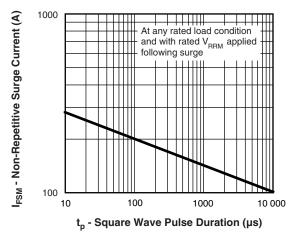


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

 $^{(1)}$ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = inverse power loss = $V_{R1} \times I_R (1 - D); I_R$ at $V_{R1} = 80 \%$ rated V_R

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ORDERING INFORMATION TABLE

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VISHAY

Device code	VS-	12	С	w	Q	10	FN	TRL	Н	М3
	1	2	3	4	5	6	7	8	9	10
	 Vishay Semiconductors product Current rating (12 A) Center tap configuration Package identifier: W = DPAK Schottky "Q" series 									
	5 - 6 - 7 -	Volt FN	age rati = TO-25	ng (10 = 52AA		1				
	8 -	• TF • TF	 None = Tube TR = Tape and reel TRL = Tape and reel (left oriented) TRR = Tape and reel (right oriented) 							
	9 - 10 -	H = Env	AEC-Q	101 qua ntal digit	alified :		-	termina	tions lea	ad (Pb)-fi

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-12CWQ10FNHM3	75	3000	Antistatic plastic tube				
VS-12CWQ10FNTRHM3	2000	2000	13" diameter reel				
VS-12CWQ10FNTRRHM3	3000	3000	13" diameter reel				
VS-12CWQ10FNTRLHM3	3000	3000	13" diameter reel				

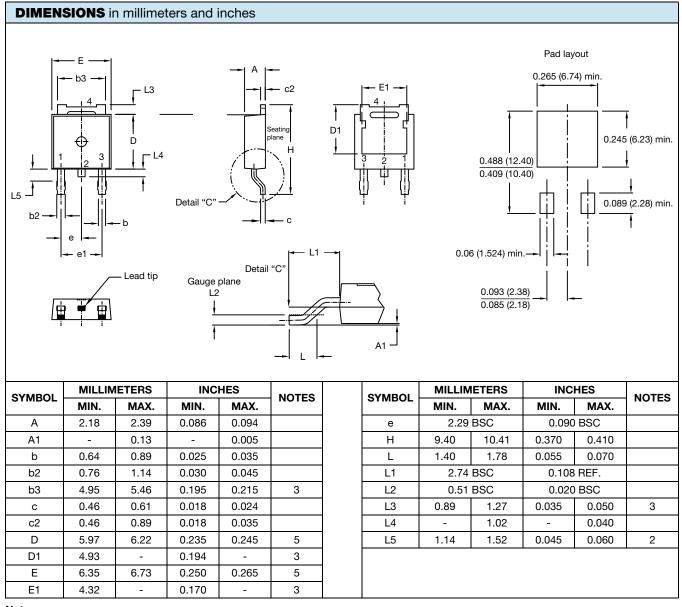
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95519				
Part marking information	www.vishay.com/doc?95518				
Packaging information	www.vishay.com/doc?95033				

Outline Dimensions



Vishay Semiconductors

DPAK (TO-252AA)



Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁵⁾ Outline conforms to JEDEC[®] outline TO-252AA, except for D1 dimension



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