# **VS-HFA70EA120**

### **Vishay Semiconductors**



**HEXFRED**<sup>®</sup> Ultrafast Soft Recovery Diode, 70 A



PRIMARY CHARACTERISTICS					
V <sub>R</sub>	1200 V				
V <sub>F</sub> (typical)	2.2 V				
t <sub>rr</sub> (typical)	48 ns				
$I_{F(DC)}$ at $T_C$ , per module	70 A at 121 °C				
Package	SOT-227				

#### **FEATURES**

- · Fast recovery time characteristic
- Electrically isolated base plate
- Antiparallel diodes
- Large creepage distance between terminal
- · Simplified mechanical designs, rapid assembly
- · Designed and qualified for industrial level
- UL approved file E78996
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **DESCRIPTION / APPLICATIONS**

This SOT-227 modules with HEXFRED® rectifier are in antiparallel configuration. The antiparallel configuration is used for simple series rectifier and high voltage application. The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are intended for general applications such as HV power supplies, electronic welders, motor control and inverters.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V <sub>R</sub>		1200	V		
Continuous forward current, per leg	١ <sub>F</sub>	T <sub>C</sub> = 121 °C	35	٨		
Single pulse forward current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	350	A		
	Р	T <sub>C</sub> = 25 °C	357	W		
Maximum power dissipation, per leg	PD	T <sub>C</sub> = 100 °C	143	vv		
RMS isolation voltage	VISOL	Any terminal to case, t = 1 minute	2500	V		
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C		

<b>ELECTRICAL SPECIFICATIONS</b> ( $T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Cathode to anode breakdown voltage	V <sub>BR</sub>	I <sub>R</sub> = 100 μA	1200	-	-		
Forward voltage, per leg		I <sub>F</sub> = 30 A	-	2.2	3.0		
	V <sub>FM</sub>	I <sub>F</sub> = 60 A	-	2.8	4.0	V	
		I <sub>F</sub> = 30 A, T <sub>J</sub> = 125 °C	-	2.13	-		
		I <sub>F</sub> = 60 A, T <sub>J</sub> = 125 °C	-	2.70	-		
		I <sub>F</sub> = 30 A, T <sub>J</sub> = 150 °C - 2.04		-			
		I <sub>F</sub> = 60 A, T <sub>J</sub> = 150 °C	-	2.65	-		
Reverse leakage current, per leg	I <sub>RM</sub>	$V_{R} = V_{R}$ rated	-	2.0	75	μA	
		$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	1.6	5	m (	
		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	5	10	mA	



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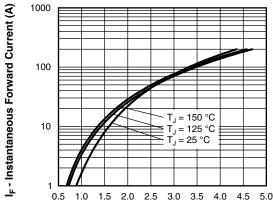


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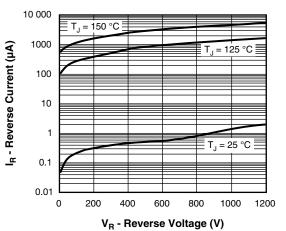
<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1 \text{ A}; \text{ d}I_F/c$	$t = 200 \text{ A/}\mu\text{s}; \text{ V}_{\text{R}} = 30 \text{ V}$	-	48	-	ns
Reverse recovery time, per leg	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 50 A dI <sub>F</sub> /dt = - 200 A/μs V <sub>B</sub> = 200 V	-	145	-	
		T <sub>J</sub> = 125 °C		-	218	-	
Peak recovery current, per leg	1	T <sub>J</sub> = 25 °C		-	13	-	А
Feak lecovery current, per leg	IRRM	T <sub>J</sub> = 125 °C		-	19	-	~
Poverse receivery charge, per leg	0	T <sub>J</sub> = 25 °C	vR – 200 v	-	910	-	nC
Reverse recovery charge, per leg	covery charge, per leg Q <sub>rr</sub>			-	1920	-	no
Junction capacitance, per leg	CT	V <sub>R</sub> = 1200 V		-	27	_	pF

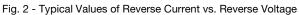
THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	Б		-	-	0.35	
Junction to case, both legs conducting	R <sub>thJC</sub>		-	-	0.175	°C/W
Case to heatsink	R <sub>thCS</sub>	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				S	OT-227	

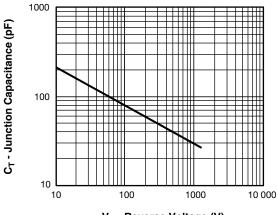


V<sub>FM</sub> - Forward Voltage Drop (V)

Fig. 1 - Typical Forward Voltage Drop Characteristics





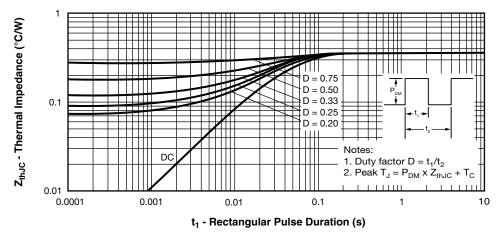


**V<sub>R</sub> - Reverse Voltage (V)** Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

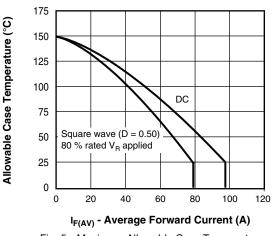
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Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

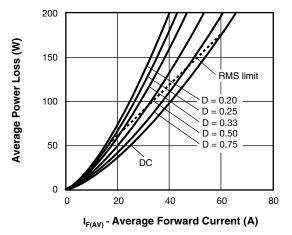
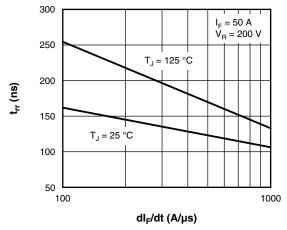


Fig. 6 - Forward Power Loss Characteristics



<sup>1)</sup> Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>;

 $\begin{array}{l} \mbox{Pd} = \mbox{Forward power loss} = I_{F(AV)} \times V_{FM} \mbox{ at } (I_{F(AV)}/D) \mbox{ (see fig. 5);} \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = V_{R1} \times I_R \mbox{ (1 - D); } I_R \mbox{ at } V_{R1} = \mbox{Rated } V_R \end{array}$ 





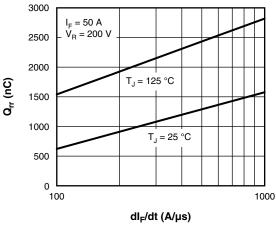


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

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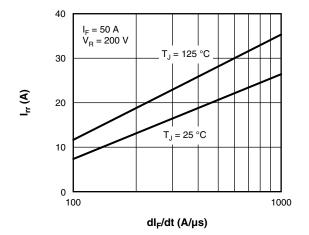


Fig. 9 - Typical Peak Recovery Current vs. dl<sub>F</sub>/dt

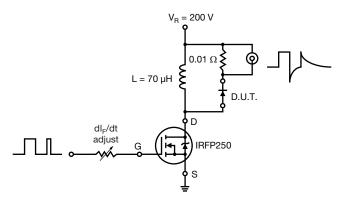
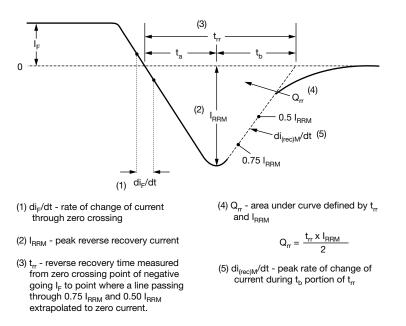
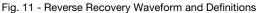


Fig. 10 - Reverse Recovery Parameter Test Circuit



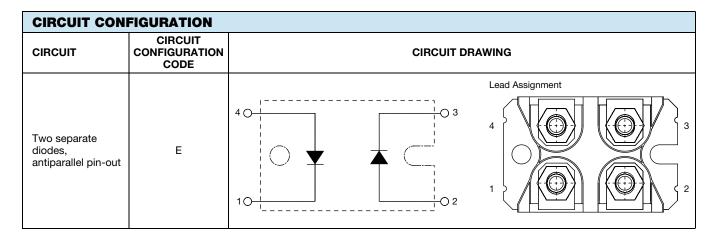






#### **ORDERING INFORMATION TABLE**

Device code	VS-	HF	Α	70	Е	Α	120	
		2	3	4	5	6	7	
	1 -	- Vishay Semiconductors product						
	2 -	HEX	HEXFRED <sup>®</sup> family					
	3 -	Pro	Process designator (A = electron irradiated)					
	4 -	Ave	Average current (70 = 70 A)					
	5 -	Circ	Circuit configuration (two separate diodes, antiparallel pin-out)					
	6 -	Pac	Package indicator (SOT-227 standard insulated base)					
	7 -	Volt	Voltage rating (120 = 1200 V)					



LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?95423						
Part marking information	www.vishay.com/doc?95425					

**Vishay Semiconductors** 



SOT-227 Generation 2

#### **DIMENSIONS** in millimeters (inches)



#### Note

• Controlling dimension: millimeter



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