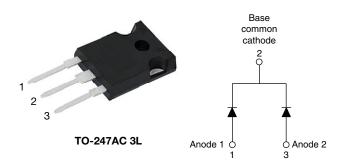


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## Vishay Semiconductors

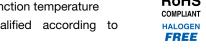
# Ultrafast Rectifier, 2 x 30 A FRED Pt®



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 30 A				
$V_{R}$	600 V				
V <sub>F</sub> at I <sub>F</sub>	1.75 V				
t <sub>rr</sub> typ.	26 ns				
T <sub>J</sub> max.	175 °C				
Package	TO-247AC 3L				
Circuit configuration	Common cathode				

#### **FEATURES**

- · Ultrafast recovery time
- · Low forward voltage drop
- 175 °C operating junction temperature
- Designed and qualified according to JEDEC®-JESD 47



· Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **DESCRIPTIONS/APPLICATIONS**

VS-CPU60... series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Repetitive peak reverse voltage	$V_{RRM}$		600	V		
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 131 °C	60	۸		
Non-repetitive peak surge current per leg	I <sub>FSM</sub>	$T_J = 25  ^{\circ}\text{C},  t_p = 10  \text{ms}$	250	A		
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C		

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	600	-	-		
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 30 A	-	1.4	1.75	V	
	VF	I <sub>F</sub> = 30 A, T <sub>J</sub> = 150 °C	-	1.1	1.4		
Reverse leakage current		V <sub>R</sub> = V <sub>R</sub> rated	-	0.02	30	μΑ	
	I <sub>R</sub>	T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	30	200		
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	20	-	pF	



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST C	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1.0 \text{ A}, dI_F/dt =$	$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		26	34		
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	$I_F = 30 \text{ A}$ $dI_F/dt = -200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	42	-	ns	
		T <sub>J</sub> = 125 °C		-	100	-		
Pook recovery ourrent	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	5	-	Α	
Peak recovery current		T <sub>J</sub> = 125 °C		-	10	-		
Powered recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	125	-	nC	
Reverse recovery charge		T <sub>J</sub> = 125 °C		-	580	-		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C	
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	0.7	1		
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W	
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.5	-		
Weight			-	6.0	-	g	
vveignt			-	0.21	-	OZ.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking device		Case style TO-247AC 3L	CPU6006				



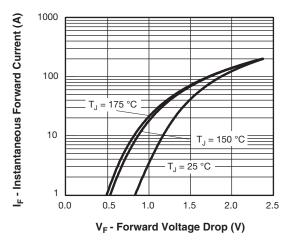


Fig. 1 - Typical Forward Voltage Drop Characteristics

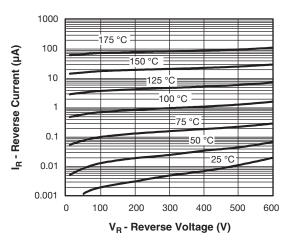


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

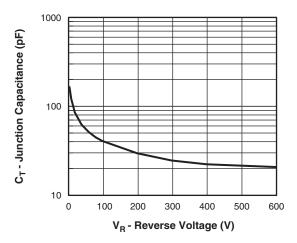


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

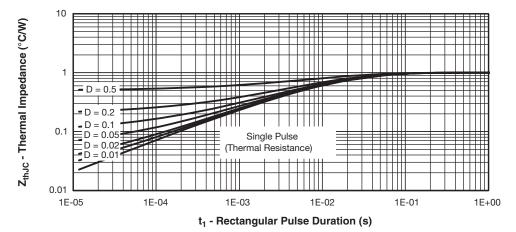


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics



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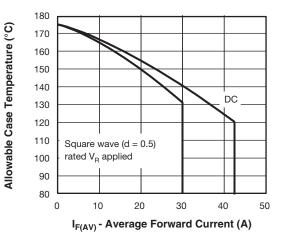


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

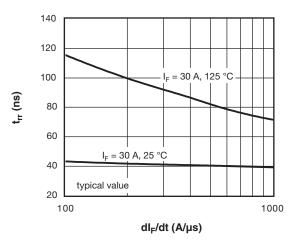


Fig. 6 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

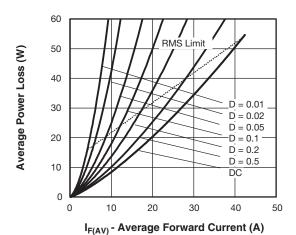


Fig. 7 - Forward Power Loss Characteristics

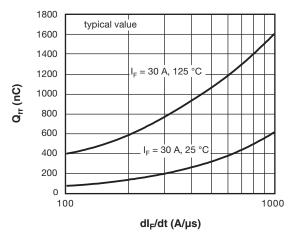
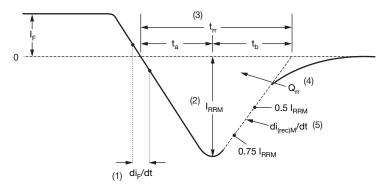


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{B1} \times I_B \text{ (1 - D)}; I_B \text{ at } V_{B1} = \text{rated } V_B \\ \end{array}$ 





- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RBM</sub> and 0.50 I<sub>RBM</sub> extrapolated to zero current.
- (4)  $\mathbf{Q}_{rr}$  area under curve defined by  $\mathbf{t}_{rr}$  and  $\mathbf{I}_{RRM}$

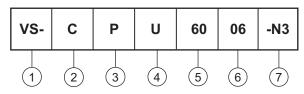
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 9 - Reverse Recovery Waveform and Definitions

#### **ORDERING INFORMATION TABLE**

Device code



- 1 Vishay Semiconductors product
- 2 Circuit configuration:

C = common cathode

- **3** P = TO-247AC
- U = ultrafast recovery time
- 5 Current code (60 = 2 x 30 A)
- 6 Voltage code (06 = 600 V)
- 7 Environmental digit:

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFO	RMATION (Example)		
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-CPU6006-N3	25	500	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?96138</u>					
Part marking information	www.vishay.com/doc?95007				



### **TO-247AC 3L**

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	IETERS	INCHES		NOTES
STWIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.17	1.37	0.046	0.054	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	MILLIMETERS		INCHES		
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
D2	0.51	1.35	0.020	0.053		
E	15.29	15.87	0.602	0.625	3	
E1	13.46	-	0.53	-		
е	5.46	BSC	0.215	BSC		
ØK	0.2	0.254		)10		
L	14.20	16.10	0.559	0.634		
L1	3.71	4.29	0.146	0.169		
ØΡ	3.56	3.66	0.14	0.144		
Ø P1	-	7.39	-	0.291		
Q	5.31	5.69	0.209	0.224		
R	4.52	5.49	0.178	0.216		
S	5.51 BSC		0.217	BSC		

#### **Notes**

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension Q



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Vishay

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