Vishay Semiconductors

Hyperfast Rectifier, 3 A FRED Pt®



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LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I _{F(AV)}	3 A				
V _R	100 V				
V _F at I _F	0.74 V				
t _{rr}	30 ns				
T _J max.	175 °C				
Package	SlimSMA (DO-221AC)				
Circuit configuration	Single				

FEATURES

- Hyperfast recovery time, reduced Q_{rr}, and soft recovery
- 175 °C maximum operating junction temperature
- Specific for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, piezo-injection, as high frequency rectifiers and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

MECHANICAL DATA

Case: SlimSMA (DO-221AC)

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per J-STD-002

Polarity: color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Peak repetitive reverse voltage	V _{RRM}		100	V			
Average rectified forward current	I _{F(AV)}	$T_{\rm C} = 145 \ ^{\circ}{\rm C}^{(1)}$	3	А			
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	85	A			
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C			

Note

⁽¹⁾ Device on PCB with 8 mm x 16 mm soldering lands

ELECTRICAL SPECIFICATIONS (T _J = 25 $^{\circ}$ C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	100	-	-		
Forward voltage	V _F	I _F = 3 A	-	0.86	0.93	V	
		I _F = 3 A, T _J = 125 °C	-	0.74	0.78		
Boyeres lookage ourrent	I _R	V _R = V _R rated	-	-	2		
Reverse leakage current		$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	0.5	8	μA	
Junction capacitance	CT	V _R = 100 V	-	13	-	pF	

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RoHS

COMPLIANT HALOGEN

FREE



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50$	0 A/µs, V _R = 30 V	-	26	-		
Reverse recovery time	+	I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A		-	-	30	ns	
neverse recovery time	t _{rr}	T _J = 25 °C	I _F = 3 A dI _F /dt = 200 A/µs	-	18	-	115	
		T _J = 125 °C		-	26	-		
Peak recovery current		T _J = 25 °C		-	2.5	-	А	
Peak recovery current I _{RRM}	T _J = 125 °C	$V_{\rm B} = 160 \text{ V}$	-	4	-	~		
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	23	-	nC	
	Qrr	T _J = 125 °C		-	50	-	nu	

THERMAL AND MECHANICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS MIN. TYP. MAX.				UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C	
Thermal resistance, junction to mount	R _{thJM}	Device mounted on PCB with 8 mm x 16 mm soldering lands	-	8	10	°C/W	
Thermal resistance, junction to ambient	R _{thJA}	Device mounted on PCB with 2 mm x 3.5 mm soldering lands	-	91	110	0/10	
Approximate Weight				0.032		g	
Approximate weight				0.0011		oz.	
Marking device		Case style SlimSMA (DO-221AC)		31	-11		

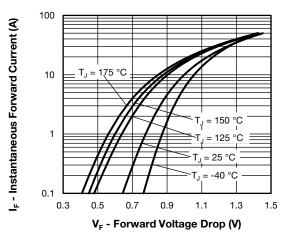


Fig. 1 - Typical Forward Voltage Drop Characteristics

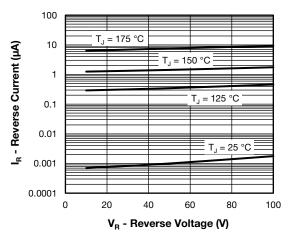
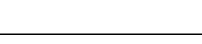
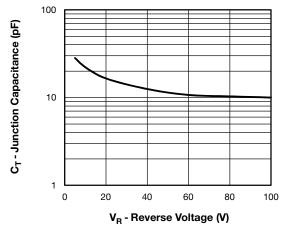


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

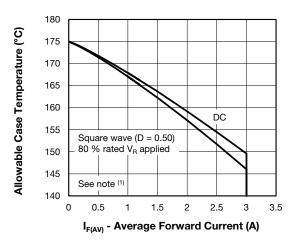


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

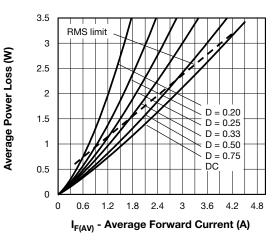


Fig. 5 - Forward Power Loss Characteristics

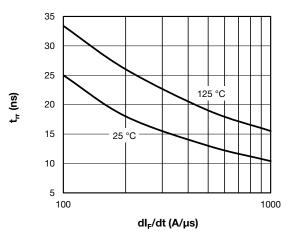


Fig. 6 - Typical Reverse Recovery vs. dl_F/dt

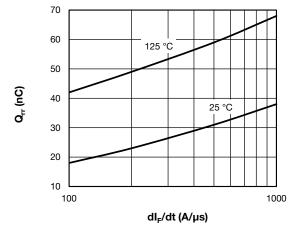


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

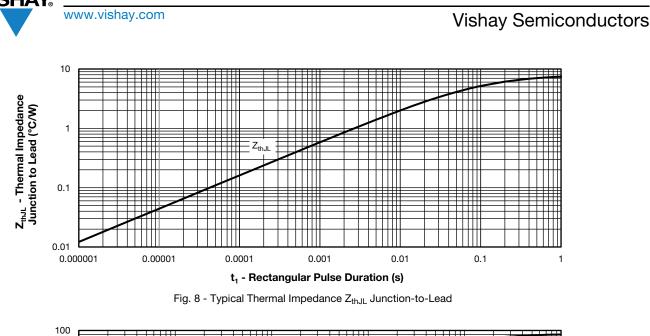
 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ x \ \mathsf{V}_{\mathsf{FM}} \ at \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{Fig.} \ 6); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ x \ \mathsf{I}_{\mathsf{R}} \ (1 - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ at \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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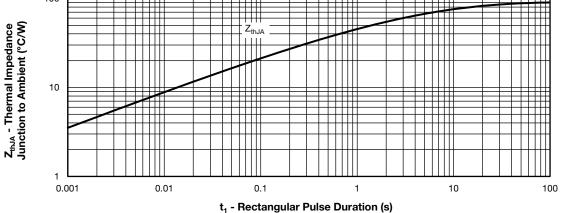


Fig. 9 - Typical Thermal Impedance ZthJA Junction-to-Ambient

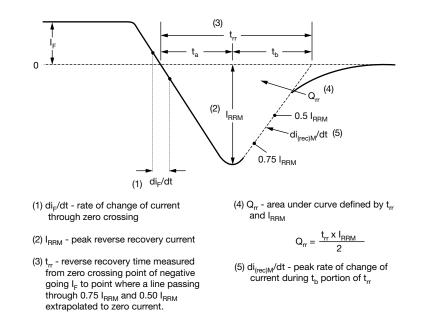


Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code	VS-	3	Е	J	н	01	н	М3
		(2)	(3)		(5)	6		(8)
			U	(1)	J	U	()	U
	1 -	· Visl	nay Sen	nicondu	ctors pro	oduct		
	2 -	- Cur	rent rati	ng (3 =	3 A)			
	3 -	· Circ	uit conf	iguratio	า:			
		E =	single of	diode				
	4 -	. J=	SlimSM	A packa	age			
	5 -	· Pro	cess typ	be,	-			
		H =	hyperfa	ast recov	/ery			
	6 -	· Volt	tage coo	de (01 =	100 V)			
	7 -	. Н=	AEC-Q	101 qua	lified			
	8 -			en-free,		complia	nt, and	termina
			0	,			-	

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-3EJH01HM3/6A	3500	3500	7"diameter plastic tape and reel				
VS-3EJH01HM3/6B	14 000	14 000	13"diameter plastic tape and reel				

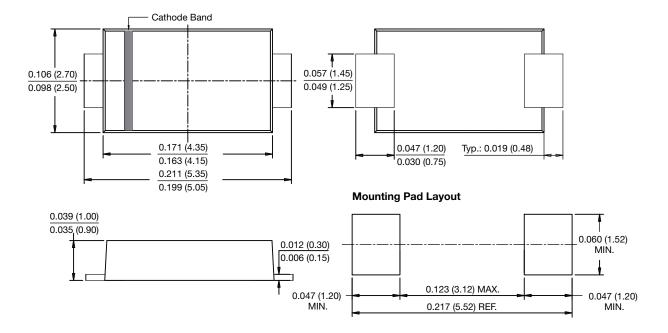
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95571				
Part marking information	www.vishay.com/doc?95562				
Packaging information	www.vishay.com/doc?88869				
SPICE model	www.vishay.com/doc?96050				



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DO-221AC (SlimSMA)

DIMENSIONS in inches (millimeters)





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