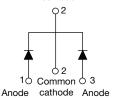
www.vishay.com

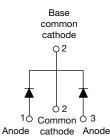
Vishay Semiconductors

# High Performance Schottky Rectifier, 2 x 20 A





SHAY



VS-40CTQ045S-M3

VS-40CTQ045-1-M3

PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	2 x 20 A							
V <sub>R</sub>	45 V							
V <sub>F</sub> at I <sub>F</sub>	0.48 V							
I <sub>RM</sub> typ.	115 mA at 125 °C							
T <sub>J</sub> max.	150 °C							
E <sub>AS</sub>	20 mJ							
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA							
Circuit configuration	Common cathode							

### FEATURES

- 150 °C T<sub>J</sub> operation
- Center tap configuration
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS										
SYMBOL	MBOL CHARACTERISTICS VALUES									
I <sub>F(AV)</sub>	Rectangular waveform	40	A							
V <sub>RRM</sub>		45	V							
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1240	A							
V <sub>F</sub>	20 $A_{pk}$ , $T_J$ = 125 °C (per leg)	0.48	V							
TJ	Range	-55 to +150	C°							

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-40CTQ045S-M3 VS-40CTQ045-1-M3	UNITS					
Maximum DC reverse voltage	V <sub>R</sub>	45	V					
Maximum working peak reverse voltage	V <sub>RWM</sub>	45	V					

RoHS COMPLIANT HALOGEN FREE

Revision: 21-Dec-2021



www.vishay.com

## Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS									
PARAMETER		SYMBOL	TEST COND	VALUES	UNITS				
Maximum average	per leg			20					
forward current See fig. 5	per device	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 116 °C	40	А				
Maximum peak one cycle no	e non-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated load	1240	A			
surge current per leg See fig. 7		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse condition and with rated V <sub>RRM</sub> applied		350				
Non-repetitive avalanche en	ergy per leg	E <sub>AS</sub>	$T_J = 25 \text{ °C}, I_{AS} = 3 \text{ A}, L = 4.40 \text{ mH}$		20	mJ			
Repetitive avalanche curren	t per leg	I <sub>AR</sub>	Current decaying linearly to zer Frequency limited by $T_J$ maxim		3	А			

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST C	ONDITIONS	VALUES	UNITS			
		20 A	– T <sub>.1</sub> = 25 °C	0.53				
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	40 A	1 = 25 0	0.68	V			
	VFM V	20 A	T <sub>.1</sub> = 125 °C	0.48	V			
		40 A	1j=125 C	0.67				
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	3	mA			
Maximum reverse leakage current per leg		T <sub>J</sub> = 125 °C		150				
Typical reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	115	mA			
Threshold voltage	V <sub>F(TO)</sub>			0.27	V			
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		8.72	mΩ			
Maximum junction capacitance per leg	CT	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal ran	2800	pF				
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 n	8.0	nH				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs			

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to 150	°C			
Maximum thermal resistance, junction to case per leg		Р	DC aparation	2.0				
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	1.0	°C/W			
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth, and greased	0.50				
Approvimate weight				2	g			
Approximate weight				0.07	oz.			
Mounting torque	minimum			6 (5)	kgf ⋅ cm			
Mounting torque	maximum			12 (10)	(lbf · in)			
Marking davias			Case style D <sup>2</sup> PAK (TO-263AB)	40CTC	045S			
Marking device			Case style TO-262AA	40CTQ	045-1			

Revision: 21-Dec-2021

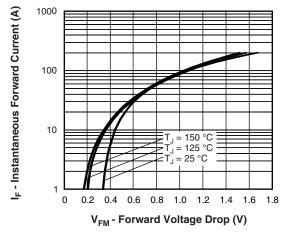
Document Number: 94935

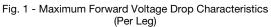
For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



## VS-40CTQ045S-M3, VS-40CTQ045-1-M3

**Vishay Semiconductors** 





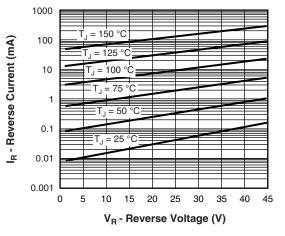


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

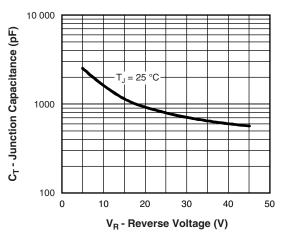


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

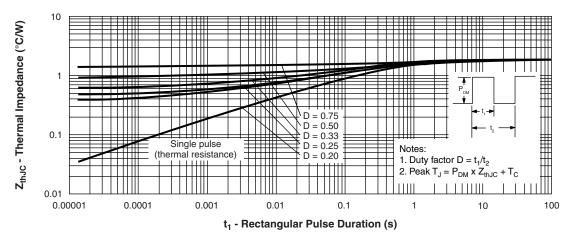
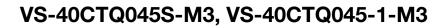


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

 Revision: 21-Dec-2021
 3
 Document Number: 94935

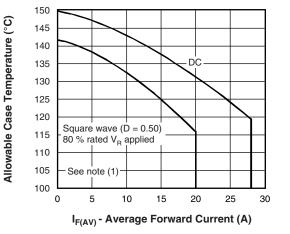
 For technical questions within your region: <a href="mailto:DiodesAmericas@vishay.com">DiodesAsia@vishay.com</a>, <a href="DiodesAsia@vishay.com">DiodesEurope@vishay.com</a>

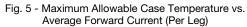
 THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <a href="mailto:www.vishay.com/doc?91000">www.vishay.com/doc?91000</a>





**Vishay Semiconductors** 





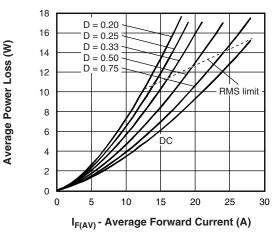


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

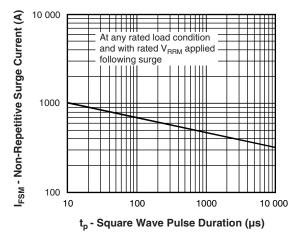


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

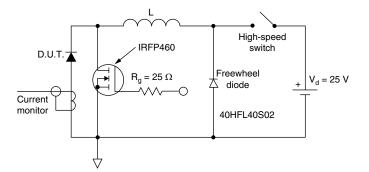


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

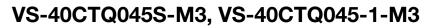
<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

Pd = forward power loss =  $I_{F(AV)} \times V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6); Pd<sub>BEV</sub> = inverse power loss =  $V_{B1} \times I_B (1 - D)$ ;  $I_B$  at  $V_{B1} = 10 V$ 

Revision: 21-Dec-2021

4

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



**Vishay Semiconductors** 

### **ORDERING INFORMATION TABLE**

www.vishay.com

SHAY

Device code	VS-	40	С	т	Q	045	S	TRL	-M3
		(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)
		$\bigcirc$	$\bigcirc$	$\cup$	0	$\bigcirc$	$\bigcirc$	$\bigcirc$	$\bigcirc$
			•	niconduo	•	oduct			
	2	- Cur	rrent rating (40 A)						
	3	- Circ	rcuit configuration: C = common cathode = TO-220						
	4	- T=	TO-220	)					
	5	- Sch	ottky "G	)" series					
	6	- Vol	tage rati	ng (045	= 45 V)	)			
	7	- •s	= D <sup>2</sup> PA	К (ТО-2	263AB)				
		• -1	= TO-2	62AA					
	8	- • N	one = tu	ıbe					
		• T	RL = tap	be and r	eel (left	oriented	d - for D	<sup>2</sup> PAK (	ГО-263
		• T	RR = ta	pe and r	eel (rigł	nt orient	ed - for	D <sup>2</sup> PAK	(TO-26
	9			gen-free					

ORDERING INFORMATION									
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION							
VS-40CTQ045S-M3	50	Antistatic plastic tubes							
VS-40CTQ045STRL-M3	800	13" diameter plastic tape and reel							
VS-40CTQ045STRR-M3	800	13" diameter plastic tape and reel							
VS-40CTQ045-1-M3	50	Antistatic plastic tubes							

LINKS TO RELATED DOCUMENTS								
Dimensions	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96164						
Dimensions	TO-262AA	www.vishay.com/doc?96165						
Part marking information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444						
Part marking information	TO-262AA	www.vishay.com/doc?95443						
Packaging information		www.vishay.com/doc?96424						

## **Outline Dimensions**



D<sup>2</sup>PAK

### **DIMENSIONS** in millimeters and inches

www.vishay.com

SHA



SYMBOL	MILLIM	MILLIMETERS		HES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Datum A and B to be determined at datum plane H

<sup>(6)</sup> Controlling dimension: inch

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-263AB

Revision: 08-Jul-15

1

## **Outline Dimensions**



**Vishay Semiconductors** 

**TO-262** 

#### **DIMENSIONS** in millimeters and inches



Lead tip -



E1 Plating



Е

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. А 4.06 4.83 0.160 0.190 2.03 A1 3.02 0.080 0.119 b 0.51 0.99 0.020 0.039 b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 1.14 1.73 0.045 0.068 4 b3 0.38 0.74 0.015 0.029 С 0.38 0.58 0.015 0.023 4 c1 1.14 1.65 0.045 0.065 c2 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2, 3 E1 7.90 8.80 0.311 0.346 3 0.100 BSC 2.54 BSC е L 13.46 14.10 0.530 0.555 L1 \_ 1.65 0.065 3 \_ 3.36 0.132 0.146 L2 3.71

3. - Anode

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC TO-262 except A1 (maximum), (6) b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

Revision: 11-Jul-2019

Document Number: 95419

1 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000



Vishay

## Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2025 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED

Revision: 01-Jan-2025

1