RoHS

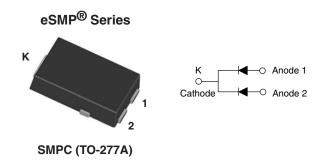
COMPLIANT

HALOGEN FREE



Vishay Semiconductors

Hyperfast Rectifier, 2 x 5 A FRED Pt®



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 5 A			
V_{R}	200 V			
V _F at I _F	0.75 V			
t _{rr (typ.)}	25 ns			
T _J max.	175 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Common cathode			

FEATURES

- \bullet Hyperfast recovery time, reduced $\mathsf{Q}_{\text{rr}},$ and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyper fast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyper fast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, as high frequency rectifiers and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating

Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage		V _{RRM}		200	V
Average rectified forward current	per device	I _{F(AV)}	T _{Sp} = 155 °C	10	
Average rectilled forward current	per diode			5	۸
Non repetitive peak aurae aurant	per device	1	T 05 %C	130	А
Non-repetitive peak surge current per diode		IFSM	T _J = 25 °C	70	
Operating junction and storage tem	peratures	T _J , T _{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	200	-	-	
Famurand violations are disale	M	I _F = 5 A	-	0.92	0.98	V
Forward voltage, per diode	V _F	I _F = 5 A, T _J = 150 °C	-	0.75	0.82	
Reverse leakage current, per diode	I _R	V _R = V _R rated	-	-	2	μA
neverse leakage current, per diode		T _J = 150 °C, V _R = V _R rated	-	6	80	μΑ
Junction capacitance	C _T	V _R = 200 V	-	17	-	pF



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	25	-	
Poverse receivery time		$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		-	-	25	
Reverse recovery time t _{rr}	чrr	T _J = 25 °C		-	18	-	ns
		T _J = 125 °C		-	28	-	
Peak recovery current I _{RRM}	T _J = 25 °C	$I_F = 5 A$ $dI_F/dt = 200 A/\mu s$ $V_B = 160 V$	-	2	-	Α	
	T _J = 125 °C		-	3.8	-	A	
Reverse recovery charge Q _{rr}	T _J = 25 °C	"	-	18	-	nC	
	T _J = 125 °C		-	53	-	IIC	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C
Thermal resistance, junction to mount, per leg	R _{thJM}		-	2.5	3.5	°C/W
Thermal resistance, junction to ambient, per leg	R _{thJA}		-	80	-	°C/W
Approximate weight				0.1		g
Approximate weight			0.0035		OZ.	
Marking device		Case style SMPC (TO-277A)		SC	H2	

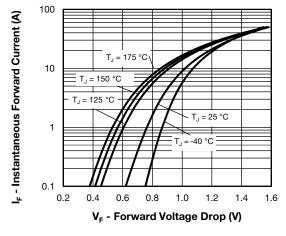


Fig. 1 - Typical Forward Voltage Drop Characteristics

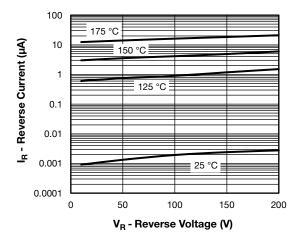


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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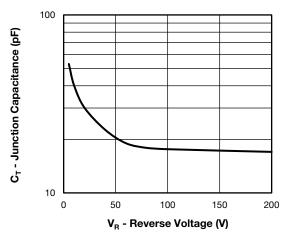
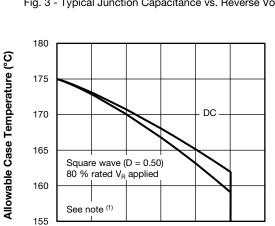


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



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Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

3

I_{F(AV)} - Average Forward Current (A)

4

5

6

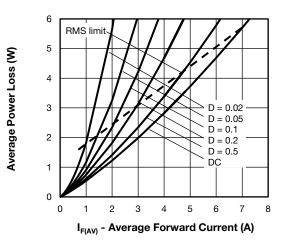


Fig. 5 - Forward Power Loss Characteristics

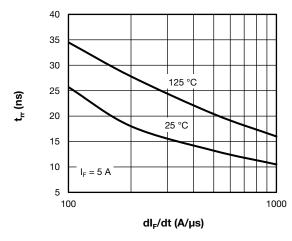


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt

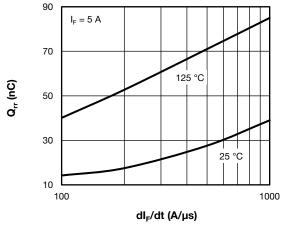


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

 $^{(1)}$ Formula used: $T_{C} = T_{J}$ - (Pd + Pd_{REV}) x $R_{thJC};$ Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 5); Pd_{REV} = inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = rated V_R

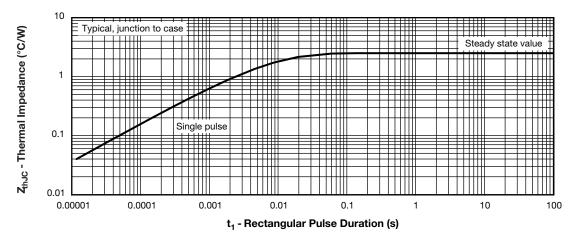


Fig. 8 - Typical Transient Thermal Impedance, Junction to Case

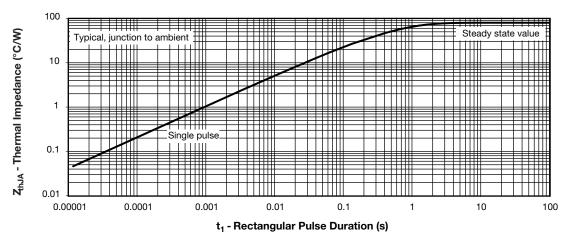
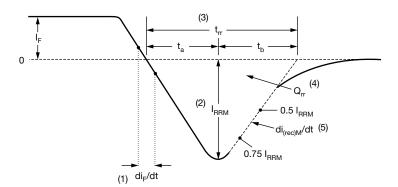


Fig. 9 - Typical Transient Thermal Impedance, Junction to Ambient



- di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

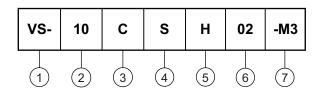
Fig. 10 - Reverse Recovery Waveform and Definitions



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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (10 = 10 A)

3 - Circuit configuration:

C = common cathode

- S = SMPC package

5 - Process type,

H = hyper fast recovery

6 - Voltage code (02 = 200 V)

7 - -M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-10CSH02-M3/86A	1500	1500	7" diameter plastic tape and reel		
VS-10CSH02-M3/87A	6500	6500	13" diameter plastic tape and reel		

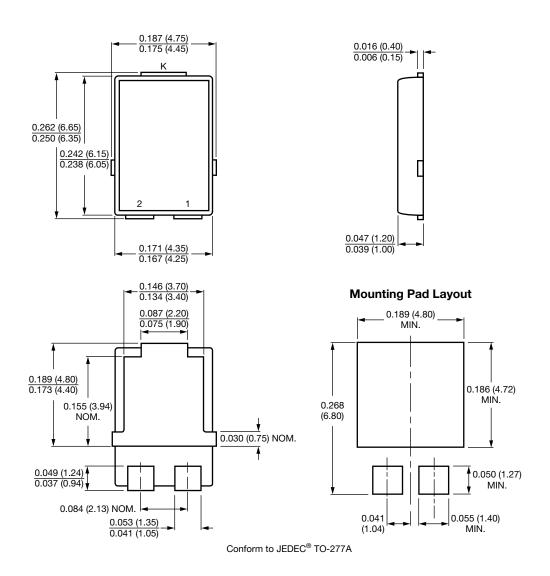
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95570			
Part marking information	www.vishay.com/doc?95565			
Packaging information	www.vishay.com/doc?88869			
SPICE model	www.vishay.com/doc?96095			



Vishay Semiconductors

SMPC (TO-277A)

DIMENSIONS in inches (millimeters)





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