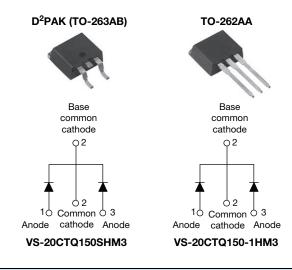


Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 10 A



PRINMARY CHARACTERISTICS								
Package	D ² PAK (TO-263AB), TO-262AA							
I _{F(AV)}	2 x 10 A							
V _R	150 V							
V _F at I _F	0.66 V							
I _{RM} max.	5.0 mA at 125 °C							
T _J max.	175 °C							
E _{AS}	1.0 mJ							
Diode variation	Common cathode							

FEATURES

- 175 °C T_J operation
- · Center tap configuration
- Low forward voltage drop
- High frequency operation



- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified meets JESD-201 class 1A whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	VALUES	UNITS					
I _{F(AV)}	Rectangular waveform	20	A					
V _{RRM}		150	V					
I _{FSM}	t _p = 5 μs sine	1030	A					
V _F	10 A _{pk} , T _J = 125 °C (per leg)	0.66	V					
TJ	Range	-55 to +175	°C					

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-20CTQ150SHM3 VS-20CTQ150-1HM3	UNITS					
Maximum DC reverse voltage	V _R	150	V					
Maximum working peak reverse voltage	V _{RWM}	150	V					

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDI	TEST CONDITIONS						
Maximum average forward per	· .								
current See fig. 5 per dev	ice I _{F(AV)}	50 % duty cycle at $T_{\rm C}$ = 154 °C	50 % duty cycle at $T_C = 154$ °C, rectangular waveform						
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	1030	A				
non-repetitive surge current per leg See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	180					
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 2 mH		1.0	mJ				
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero Frequency limited by T_J maxim		1	А				

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ELECTRICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CO	TYP.	MAX.	UNITS					
Maximum forward voltage drop per leg See fig. 1		10 A	T.I = 25 °C	0.80	0.88					
	V _{FM} ⁽¹⁾	20 A	1j=25 0	0.90	1.0	V				
	VFM (*)	10 A	T.I = 125 °C	0.63	0.66					
		20 A	1j = 125 C	0.73	0.77					
Maximum reverse leakage current per leg	. (1)	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	3.0	25	μA				
See fig. 2	I _{RM} ⁽¹⁾	T _J = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	2.7	5.0	mA				
Typical junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range	ge 100 kHz to 1 MHz), 25 °C	-	280	pF				
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body			8.0	nH				
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs				

Note

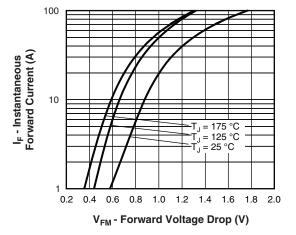
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

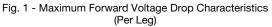
THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +175	°C			
Maximum thermal resistance,	per leg	Р	DC exerction	2.0				
junction to case	per package	R _{thJC}	DC operation	1.0	°C/W			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased (Only for TO-262)	0.50				
Approvimeto weight				2	g			
Approximate weight				0.07	oz.			
Mounting torque	minimum			6 (5)	kgf · cm			
Mounting torque	maximum			12 (10)	(lbf · in)			
			Case style D ² PAK (TO-263AB)	20CTQ	150SH			
Marking device			Case style TO-262	20CTQ1	I50-1H			



VS-20CTQ150SHM3, VS-20CTQ150-1HM3

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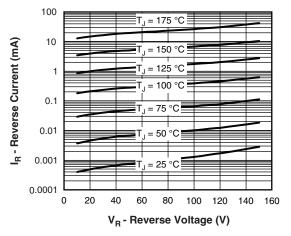


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

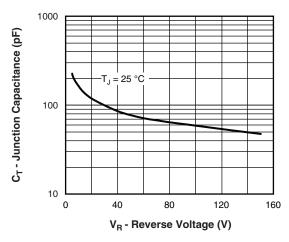
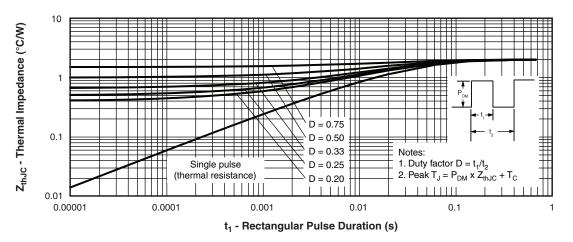
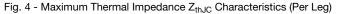


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





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 Document Number: 95739

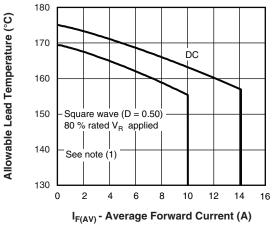
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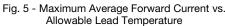
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VS-20CTQ150SHM3, VS-20CTQ150-1HM3

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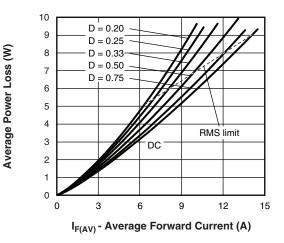


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

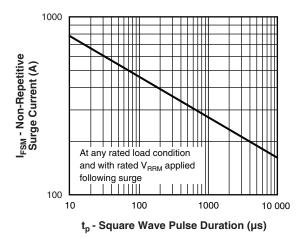


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

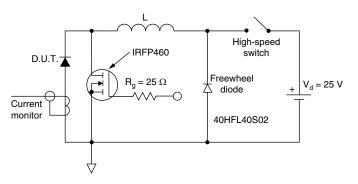


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code	VS-	20	С	т	Q	150	S	TRL	н	МЗ		
		2	3	4	5	6	7	8	9	10		
	1 - Vishay Semiconductors product											
	2											
	3											
	4	- T = TO-220										
	5	- Sch	ottky "G)" series								
	6	- Voli	tage rati	ing (150	= 150 \	/)						
	7	- • S	= D ² PA	K								
		• -1	= TO-2	262								
	8	- • None = tube										
		• TI	 TRL = tape and reel (left oriented - for D²PAK only) 									
		• TI	 TRR = tape and reel (right oriented - for D²PAK only) 									
	9	- H=	AEC-Q	101 qua	alified							
	10	- M3	= halog	en-free,	RoHS-	complia	nt, and	termina	ation lea	ıd (Pb)-fı		

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-20CTQ150SHM3	50	1000	Antistatic plastic tubes						
VS-20CTQ150STRLHM3	800	800	13" diameter reel						
VS-20CTQ150STRRHM3	800	800	13" diameter reel						
VS-20CTQ150-1HM3	50	1000	Antistatic plastic tubes						

LINKS TO RELATED DOCUMENTS								
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?95046						
	TO-262AA	www.vishay.com/doc?95419						
Port marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444						
Part marking information	TO-262AA	www.vishay.com/doc?95443						
Packaging information		www.vishay.com/doc?95032						

Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches

www.vishay.com

SHA



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

1

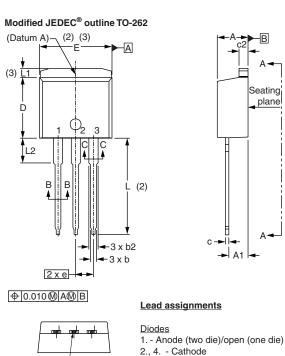
Outline Dimensions



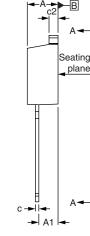
Vishay Semiconductors

TO-262

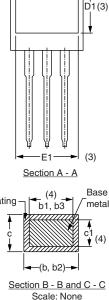
DIMENSIONS in millimeters and inches



Lead tip -



E1 Plating



Е

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. А 4.06 4.83 0.160 0.190 2.03 A1 3.02 0.080 0.119 b 0.51 0.99 0.020 0.039 b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 1.14 1.73 0.045 0.068 4 b3 0.38 0.74 0.015 0.029 С 0.38 0.58 0.015 0.023 4 c1 1.14 1.65 0.045 0.065 c2 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2, 3 E1 7.90 8.80 0.311 0.346 3 0.100 BSC 2.54 BSC е L 13.46 14.10 0.530 0.555 L1 _ 1.65 0.065 3 _ 3.36 0.132 0.146 L2 3.71

3. - Anode

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

Outline conform to JEDEC TO-262 except A1 (maximum), (6) b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

Revision: 11-Jul-2019

Document Number: 95419

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