# **VS-QA250FA20**

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**Vishay Semiconductors** 

# Insulated Gen 2 Schottky Rectifier Module, 250 A



PRIMARY CHARACTERISTICS						
$I_{F(AV)}$ per module at $T_C = 106 \ ^{\circ}C$	250 A					
V <sub>R</sub>	200 V					
$V_{FM}$ at 200 A, $T_C = 25 \ ^\circ C$	1.0 V					
Package	SOT-227					
Circuit configuration	Two separate diodes, parallel pin-out					

### **FEATURES**

- Max. T<sub>.1</sub> = 175 °C
- Two fully independent diodes
- Fully insulated package
- Trench MOS Barrier Schottky technology
- Ultra low forward voltage drop
- · Optimized for power conversion: welding and industrial SMPS applications
- · Easy to use and parallel
- · Industry standard outline
- UL approved file E78996
- Designed and qualified for industrial level
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### DESCRIPTION

The VS-QA250FA20 insulated modules integrate two state of the art Trench MOS Schottky technology rectifiers in the compact, industry standard SOT-227 package.

These devices are thus intended for high frequency converters and switching power supplies.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
VF	T <sub>J</sub> = 125 °C	1.09	V		
TJ	Range	-55 to +175	°C		

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_C = 25 \text{ °C}$ unless otherwise specified)					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum average forward current per module	I <sub>F(AV)</sub>	T <sub>C</sub> = 106 °C	250	А	
Maximum cathode to anode voltage	V <sub>R</sub>		200	V	
Maximum continuous forward current per diode	١ <sub>F</sub>	T <sub>C</sub> = 95 °C	183	٨	
Maximum single pulse forward current per diode	I <sub>FSM</sub>	$T_{C}$ = 175 °C, t = 6 ms, square	900	A	
Maximum power dissipation per diode	PD	T <sub>C</sub> = 95 °C	182	W	
Non-repetitive avalanche energy per diode	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 19 A, L = 10 mH	1800	mJ	
RMS isolation voltage	VISOL	Any terminal to case, t = 1 minute	2500	V	
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C	



COMPLIANT

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<b>ELECTRICAL SPECIFICATIONS PER DIODE</b> ( $T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Cathode to anode breakdown voltage	V <sub>BR</sub>	I <sub>R</sub> = 2 mA	200	-	-		
Forward voltage	V <sub>FM</sub>	I <sub>F</sub> = 200 A	-	1.0	1.2	V	
		I <sub>F</sub> = 200 A, T <sub>J</sub> = 125 °C	-	0.89	1.09		
Povereo lookago ourrent		V <sub>R</sub> = 200 V	-	13	90	μA	
Reverse leakage current	I <sub>RM</sub>	$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	14	-	mA	
Junction capacitance	CT	V <sub>R</sub> = 200 V	-	380	-	pF	

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	+	T <sub>J</sub> = 25 °C		-	54	-	20
neverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 125 °C	l <sub>F</sub> = 50 A dI <sub>F</sub> /dt = 200 A/µs	-	67	-	ns
Peak recovery current	I <sub>RRM</sub>	$T_J = 25 \ ^\circ C$		-	6	-	А
Feak recovery current			$V_{\rm B} = 100 \rm V$	-	8.4	-	~
Poverse recovery charge	Q <sub>rr</sub>	$T_J = 25 \ ^\circ C$	VR = 100 V	-	165	-	nC
Reverse recovery charge		T <sub>J</sub> = 125 °C		-	296	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	Р		-	-	0.44	
Junction to case, both leg conducting	R <sub>thJC</sub>		-	-	0.22	°C/W
Case to heatsink	R <sub>thCS</sub>	Flat, greased surface	-	0.1	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style			SOT-227			



## **VS-QA250FA20**

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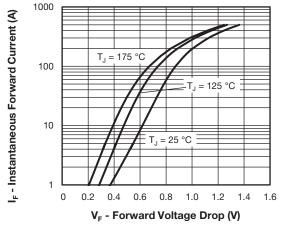


Fig. 1 - Typical Forward Voltage Drop vs. Instantaneous Forward Current (Per Diode)

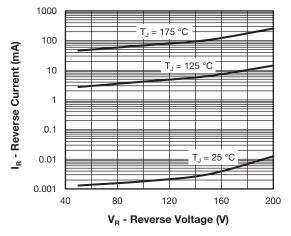


Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Diode)

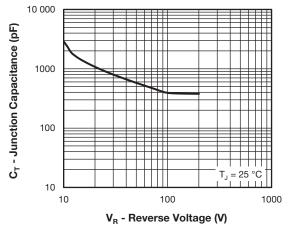


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Diode)

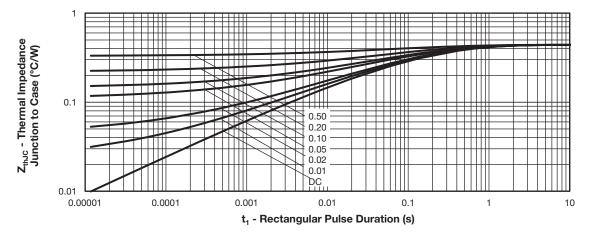


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Diode)

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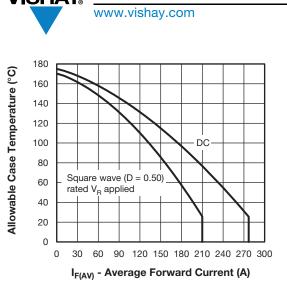


Fig. 5 - Maximum Current Rating Capability (Per Diode)

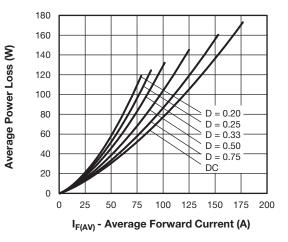


Fig. 6 - Forward Power Loss Characteristics (Per Diode)

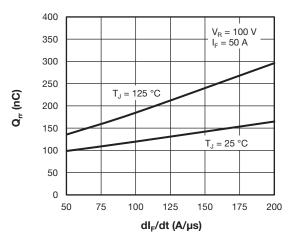


Fig. 7 - Typical Reverse Recovery Charge vs. dl<sub>F</sub>/dt (Per Diode)

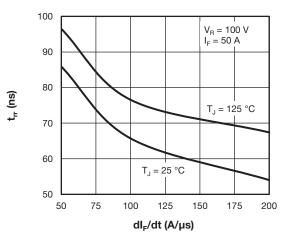


Fig. 8 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt (Per Diode)

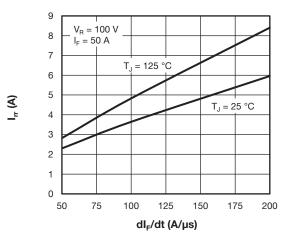


Fig. 9 - Typical Reverse Recovery Current vs. dl<sub>F</sub>/dt (Per Diode)





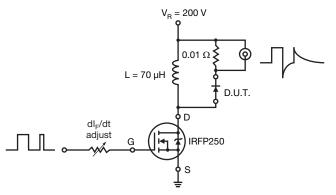


Fig. 10 - Reverse Recovery Parameter Test Circuit

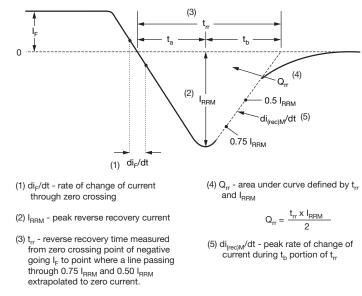


Fig. 11 - Reverse Recovery Waveform and Definitions

#### **ORDERING INFORMATION TABLE**

Device code

2

3

4

5

6

7

ode	VS-	Q	Α	250	F	Α	20	
		2	3	4	5	6	7	

- Vishay Semiconductors product
  - Schottky technologies
  - Present silicon generation
  - Current rating (250 = 250 A)
  - Circuit configuration (2 separate diodes, parallel pin-out)
  - Package indicator (SOT-227 standard insulated base)
  - Voltage rating (20 = 200 V)

Quantity per tube is 10, M4 screw and washer included



CIRCUIT CONFIGURATION							
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING					
2 separate diodes, parallel pin-out	F	Lead Assignment 4 0 0 3 4 1 0 0 2 1 1 0 0 2 1 1 0 0 0 2 2					

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95423			
Packaging information	www.vishay.com/doc?95425			



SOT-227 Generation 2

### **DIMENSIONS** in millimeters (inches)



#### Note

• Controlling dimension: millimeter



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