VS-VSKEF500/06PbF

Vishay Semiconductors

FRED Pt[®] Gen 4 Single Ultrafast Diode, 500 A (INT-A-PAK Power Modules)



INT-A-PAK

PRIMARY CHARACTERISTICS					
V _R	600 V				
I _{F(AV)} at T _C	500 A at 55 °C				
t _{rr} at 25 °C	104 ns				
Туре	Modules - diode, FRED Pt®				
Package	INT-A-PAK				
Circuit configuration	Single diode				

FEATURES

- Gen 4 FRED Pt[®] dices technology
- Ultrasoft reverse recovery characteristics
- Low I_{RRM} and reverse recovery charge
- Very low forward voltage drop
- 175 °C operating junction temperature
- \bullet UL approved file E78996 for application with maximum case temperature up to 140 $^\circ\text{C}$
- Large creepage distances
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

Gen 4 FRED Pt technology, state of the art, ultra low V_F , soft switching optimized for IGBT F/W diode.

The minimized conduction loss, optimized storage charge, and low recovery current, minimized the switching losses and reduce the over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V _R		600	V		
Continuous forward current		$T_{\rm C} = 25 \ ^{\circ}{\rm C}$	772			
Continuous forward current	I _F	$T_{\rm C} = 90 ^{\circ}{\rm C}$	519	А		
Single pulse forward current	I _{FSM}	$t_p = 10$ ms, 50 Hz, sine half wave, initial T _J = 175 °C	4500			
Maximum power dissipation	PD	$T_{\rm C} = 25 \ ^{\circ}{\rm C}$	1363	w		
		$T_{\rm C} = 90 ^{\circ}{\rm C}$	772	vv		
Operating junction temperature range	TJ		-40 to +175	Э°		
Storage temperature range	T _{Stg}		-40 to +150			
RMS insulation voltage	V _{INS}	50 Hz, circuit to base, all terminals shorted, t = 1 s	3500	V		

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Cathode to anode breakdown voltage	V_{BR}	I _R = 500 μA	600	-	-		
Forward voltage drop	V _{FM}	I _F = 250 A	-	1.25	-		
		I _F = 500 A	-	1.45	1.66	V	
		I _F = 250 A, T _J = 150 °C	-	1.23	-		
		$I_F = 500 \text{ A}, \text{ T}_J = 150 ^{\circ}\text{C}$	-	1.0	-		
Reverse leakage current	I _{RM}	V _R = 600 V	-	2.0	200	μA	
		T _J = 150 °C, V _R = 600 V	-	1.8	-	mA	

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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
Poverao recovery time	everse recovery time t _{rr}	T _J = 25 °C	I _F = 150 A dI/dt = 1000 A/μs V _R = 300 V	-	104	-	- ns - Α - μC
Reverse recovery time		T _J = 125 °C		-	193	-	
Deels receivers current		T _J = 25 °C		-	59	-	
Peak recovery current	Irr	T _J = 125 °C		-	122	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	3.5	-	
		T _J = 125 °C		-	13.8	-	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum therm junction to case	,	R _{thJC}	DC operation		-	0.11	K/W
Typical thermal resistance, case to heat sink		R _{thCS}	Mounting surface, flat, smooth and greased	-	0.035	-	r∨ vv
Mounting to heat sink			A mounting compound is recommended and the	4			Nu
torque ± 10 %	busbar		torque should be rechecked after a period of 3 hours to allow the spread of the compound.	4	-	6	Nm
Approximate we	iaht			-	200	-	g
Approximate we	igni			-	7.1	-	oz.
Case style INT-A-PAK		A-PAK					

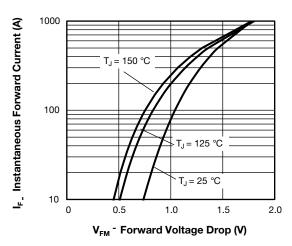


Fig. 1 - Typical Forward Voltage Drop Characteristics

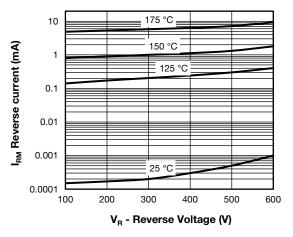


Fig. 2 - Typical Value of Reverse Current vs. Reverse Voltage



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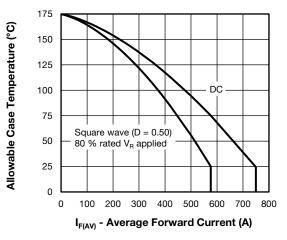


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current

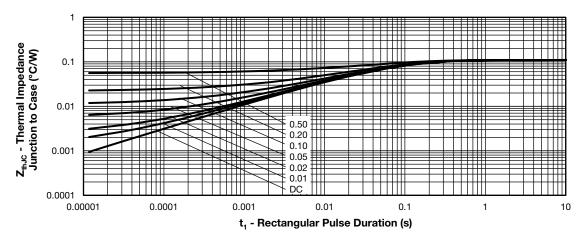


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

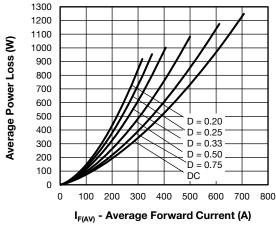
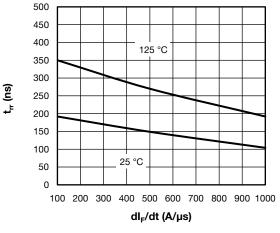
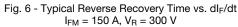


Fig. 5 - Forward Power Loss Characteristics





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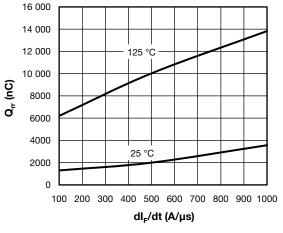


Fig. 7 - Typical Reverse Recovery Charge vs. dl_F/dt $I_{FM} = 150 \text{ A}, V_{R} = 300 \text{ V}$

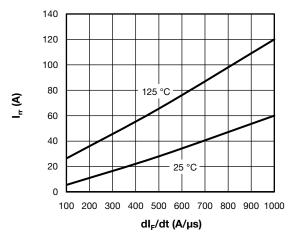


Fig. 8 - Typical Reverse Recovery Current vs. dl_F/dt I_{FM} = 150 A, V_R = 300 V

ORDERING INFORMATION TABLE

Device code	VS-VS	KE	F	500	06	PbF	
	1	2	3	4	5	6	
	1 - 2 - 3 -	Circu	ay Semi iit config RED Pt	uration:	KE = si	ngle dioc	de
	4 - 5 -		ent rating Ige ratin	0 (,		
	6 - PbF = lead (Pb)-free						

CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Single diode	KE	(3) ~ V (1)			

VS-VSKEF500/06PbF

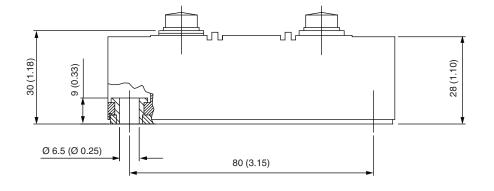
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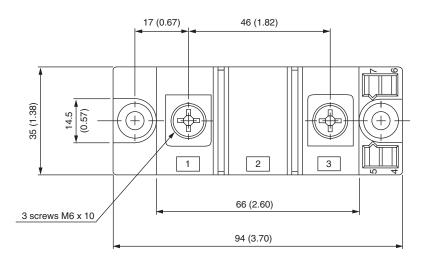


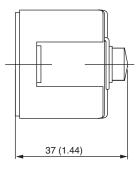
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DIMENSIONS in millimeters (inches)







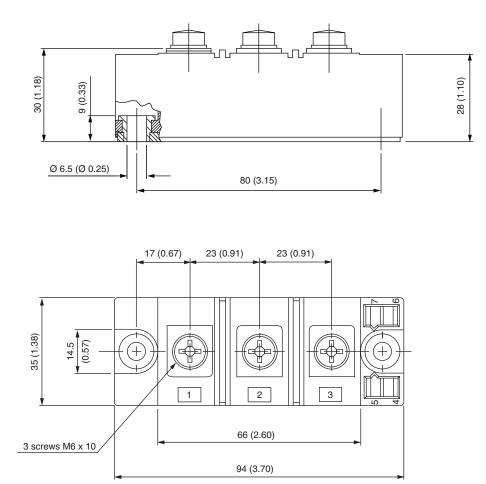


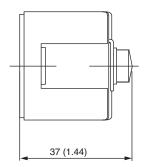
Outline Dimensions

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INT-A-PAK DBC

DIMENSIONS in millimeters (inches)







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