

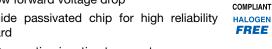
Ultrafast Soft Recovery Diode, 30 A FRED Pt® Gen 4



PRIMARY CHARACTERISTICS						
I _{F(AV)}	30 A					
V_{R}	600 V					
V _F at I _F	1.19 V					
t _{rr} typ.	see Recovery table					
T _J max.	175 °C					
Package	TO-247AD 2L					
Circuit configuration	Single					

FEATURES

- Gen 4 FRED Pt® technology
- Low I_{RRM} and reverse recovery charge
- · Very low forward voltage drop
- · Polyimide passivated chip for high reliability standard



- 175 °C operating junction temperature
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

DESCRIPTION

Gen 4 Fred technology, state of the art, ultralow V_F, soft switching optimized for Discontinuous (Critical) Mode (DCM) and IGBT F/W diode.

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Peak repetitive reverse voltage	V_{RRM}		600	V		
Average rectified forward current	I _{F(AV)}	T _C = 131 °C	30	۸		
Non-repetitive peak surge current	I _{FSM}	$T_C = 25$ °C, $t_p = 8.3$ ms half sine wave	240	A		
Operating junction and storage temperature	T _J , T _{Stg}		-55 to +175	°C		

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V_{BR} , V_{R}	I _R = 100 μA	600	-	-		
Forward voltage		I _F = 30 A	-	1.36	1.6		
	V _F	I _F = 60 A	-	1.6	-	V	
		I _F = 30 A, T _J = 125 °C	-	1.23	-		
		I _F = 60 A, T _J = 125 °C	-	1.5	-		
		I _F = 30 A, T _J = 150 °C	-	1.19	1.35		
		I _F = 60 A, T _J = 150 °C	-	1.48	-		
Reverse leakage current	I _R	V _R = V _R rated	-	-	50		
Reverse leakage current		$T_J = 125$ °C, $V_R = V_R$ rated	-	-	500	μA	
Junction capacitance	C _T	V _R = 600 V	-	18.3	-	pF	



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST C	ONDITIONS	MIN.	TYP.	MAX.	UNITS	
Payersa rasayon, tima	+	T _J = 25 °C	I _F = 30 A dI _F /dt = 1000 A/μs V _R = 400 V	-	65	-	- ns	
Reverse recovery time	t _{rr}	T _J = 125 °C		-	90	-		
Peak recovery current		T _J = 25 °C		-	18	-	Α	
	IRRM	T _J = 125 °C		-	32	-		
Reverse recovery charge	0	T _J = 25 °C		-	850	-	nC	
	Q_{rr}	T _J = 125 °C		_	1850	-		

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Thermal resistance, junction to case	R_{thJC}		-	-	0.9			
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	40	°C/W		
Thermal resistance, case to heatsink	R _{thCS}		-	0.4	-			
Weight			-	6.0	-	g		
vveignt			-	0.21	-	OZ.		
Mounting torque			6.0 (5)	-	12 (10)	kgf · cm (lbf · in)		
Marking device		Case style TO-247AD 2L	E4PU3006L					

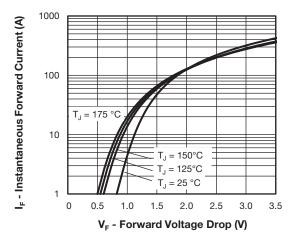


Fig. 1 - Typical Forward Voltage Drop Characteristics

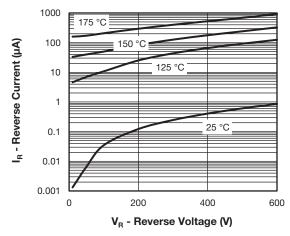


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

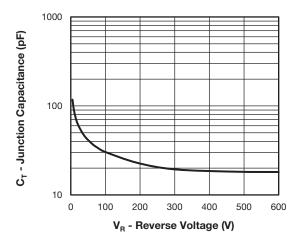


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

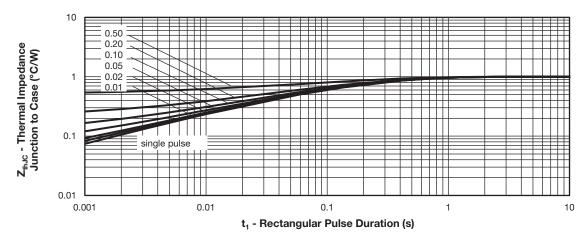


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics

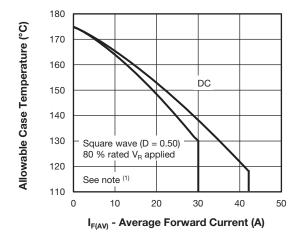


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

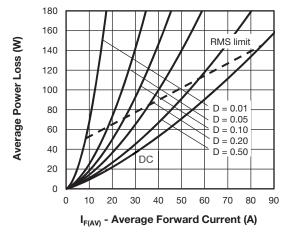


Fig. 6 - Forward Power Loss Characteristics

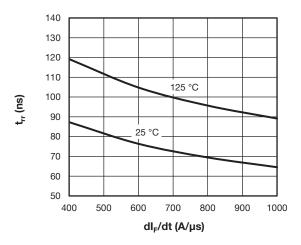
Note

⁽¹⁾ Formula used: T_C = T_J - (P_d + P_{dREV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see Fig.5) P_{dREV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_R = rated V_R



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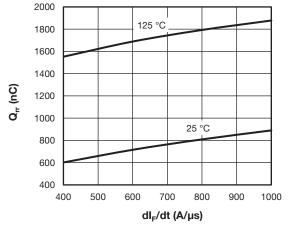


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

Fig. 8 - Typical Stored Charge vs. dl_F/dt

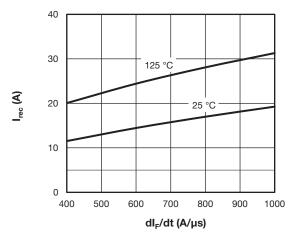
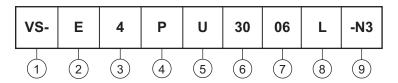


Fig. 9 - Typical Reverse Current vs. dl_F/dt



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Circuit configuration:

E = single diode

3 - FRED Pt Gen 4

4 - P = TO-247 package

5 - Process type:

U = ultrafast recovery

6 - Current rating (30 = 30 A)

7 - Voltage rating (06 = 600 V)

8 - Package: L = long lead

9 - Environmental digit:

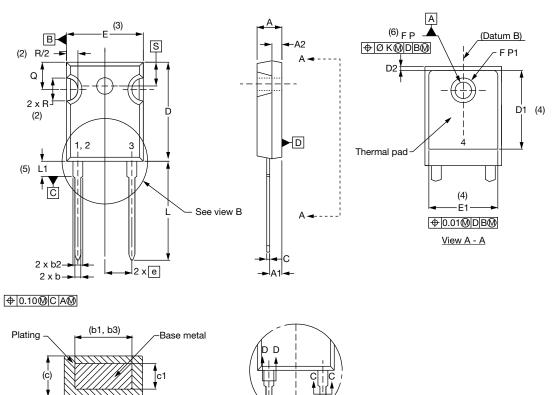
-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-E4PU3006L-N3	25	500	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95536				
Part marking information	www.vishay.com/doc?95648				

TO-247AD 2L

DIMENSIONS in millimeters and inches



View B

CVMDOL	SYMBOL		INC	HES	NOTES
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4
D2	0.51	1.35	0.020	0.053	

Section C - C, D - D

SYMBOL	MILLIN	IETERS	INC	INCHES		
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Е	15.29	15.87	0.602	0.625	3	
E1	13.46	-	0.53	-		
е	5.46	BSC	0.215	BSC		
ØK	0.2	254	0.0	10		
L	19.81	20.32	0.780	0.800		
L1	3.71	4.29	0.146	0.169		
ØΡ	3.56	3.66	0.14	0.144		
Ø P1	-	6.98	-	0.275		
Q	5.31	5.69	0.209	0.224		
R	4.52	5.49	0.178	0.216		
S	5.51 BSC		0.217 BSC			
	•		•	•		

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4



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