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*DD Vishay Semiconductors Italiana SPA

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*This document is intended as a SPICE modeling guideline and does not

*constitute a commercial product datasheet. Designers should refer to the

*appropriate datasheet of the same number for guaranteed specification *limits.

* Electrical spice model @(25°C & 125°C)

.MODEL VS-6ESH01-02 D

- + IS=102.01E-9
- + N=1.6098
- + RS=6.7089E-3
- + IKF=.32499
- + CJO=220.19E-12
- + M=.3333
- + VJ=.75
- + ISR=10.010E-21
- + NR=4.9950
- + BV=250.27
- + IBV=3.2333
- + TT=22.351E-9
- + XTI=-1.4
- + Tikf=0.052
- + Trs1=0.0005

.END

*THERMAL MODEL SUBCIRCUIT

*RC Forster model

- *R are expressed in $\Omega \rightarrow C/W$
- *C are expressed in F -> J/°C

.SUBCKT zth_VS_6ESH01-02_JC Tj Tc

RTHERM1	Tj	1	1.030e+00
RTHERM2	1	2	4.784e-02
RTHERM3	2	3	1.098e+00
RTHERM4	3	Тс	1.422e-02
CTHERM1	Тј	1	3.150e-03
CTHERM2	1	2	6.354e-04
CTHERM3	2	3	2.539e-02
CTHERM4	3	Тс	8.563e-05

.ENDS zth_VS_6ESH01-02_JC



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