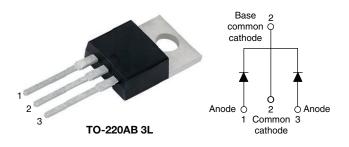
Vishay Semiconductors

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High Performance Schottky Rectifier, 2 x 20 A



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 20 A				
V _R	20 V				
V _F at I _F	0.34 V				
I _{RM} max.	310 mA at 125 °C				
T _J max.	150 °C				
E _{AS}	18 mJ				
Package	TO-220AB 3L				
Circuit configuration	Common cathode				

FEATURES

- 150 °C T_J operation
- Optimized for 3.3 V application
- Ultralow forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier has been optimized for ultralow forward voltage drop specifically for 3.3 V output power supplies. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS VALUES U				
I _{F(AV)}	Rectangular waveform	40	А		
V _{RRM}		20	V		
I _{FSM}	$t_p = 5 \ \mu s \ sine$	1000	А		
V _F	20 A _{pk} , T _J = 125 °C	0.34	V		
TJ		-55 to +150	°C		

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-47CTQ020-M3	UNITS			
Maximum DC reverse voltage	V _R	20	V			
Maximum working peak reverse voltage	V _{RWM}	20	v			

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS					
Maximum average forward per leg		50 % duty cycle at T_{C} = 135 °C, rectangular waveform		20				
current per device	I _{F(AV)}	50% duty cycle at $1_{\rm C} = 135$ C	$_{\rm C}$ = 135 °C, rectangular wavelorm		А			
Maximum peak one cycle non-repetitive		5 µs sine or 3 µs rect. pulse Following any rated load condition and with rated		1000				
surge current per leg	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	250				
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 3 A, L = 3 mH		18	mJ			
Repetitive avalanche current per leg		Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical		3	А			

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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
		20 A	T _{.1} = 25 °C	0.45			
		40 A	1j=25 0	0.51			
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	20 A	– T _{.I} = 125 °C	0.34	V		
Maximum forward voltage drop per leg	VFM (1)	40 A	$1_{\rm J} = 125$ C	0.44	V		
		20 A	T _{.1} = 150 °C	0.31			
		40 A	1j = 150 C	0.42			
	I _{RM} ⁽¹⁾	$T_{\rm J} = 125 \ ^{\circ}{\rm C} \qquad $	V _R = 5 V	60			
			V _R = 3.3 V	45			
Maximum reverse leakage current per leg		T _J = 150 °C	V _R = 10 V	306	mA		
		$T_J = 25 \ ^{\circ}C$	$V_{\rm B}$ = Rated $V_{\rm B}$	3			
		T _J = 125 °C	VR - Haleu VR	310			
Threshold voltage	V _{F(TO)}	$T_J = T_J maximum$		0.188	V		
Forward slope resistance	r _t			5.9	mΩ		
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		3000	pF		
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		5.5	nH		
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs		

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHAN	IICAL SP	ECIFICA	TIONS		
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range)	T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to case per leg		D		1.5	
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	0.75	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth, and greased	0.50	
Approvimate weight				2	g
Approximate weight				0.07	oz.
Mounting torque	minimum			6 (5)	kgf ⋅ cm
Mounting torque	maximum			12 (10)	(lbf \cdot in)
Marking device			Case style 3L TO-220AB	47CT	Q020



VS-47CTQ020-M3

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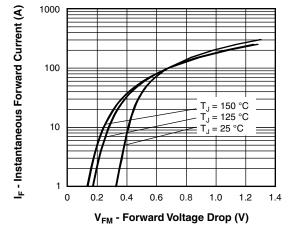


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

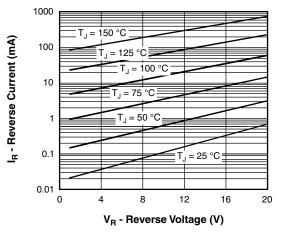


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

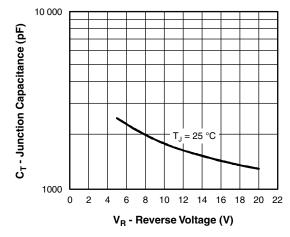
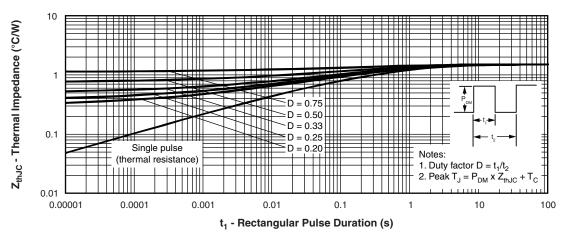
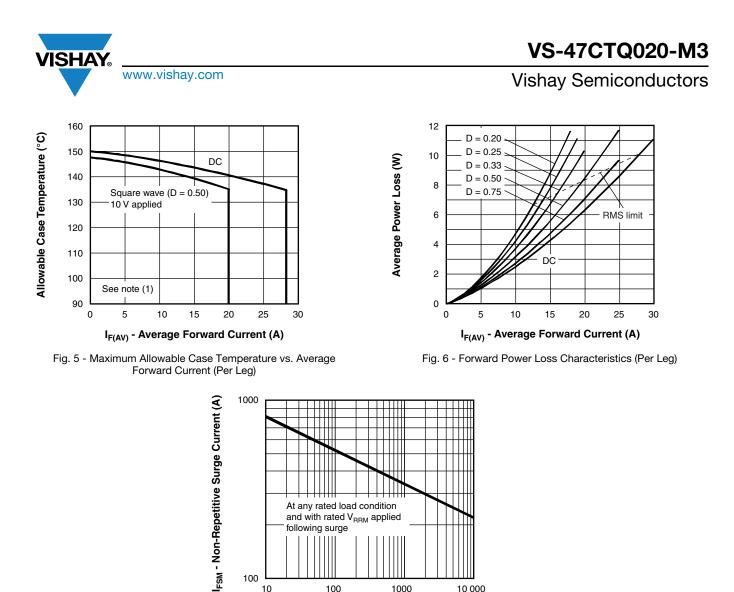


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





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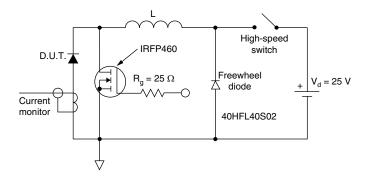


At any rated load condition and with rated V_{RRM} applied

100

following surge

100 10



t_p - Square Wave Pulse Duration (μs) Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

1000

10 000

Fig. 8 - Unclamped Inductive Test Circuit

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = 10 V

Revision: 22-Dec-2021

4

Document Number: 96252

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ORDERING INFORMATION TABLE

Device code	VS-	47	С	т	Q	020	-M3
		2	3	4	5	6	7
1	-	Vishay	Semico	onductor	s produ	ct	
2	-	Curren	t rating	(40 A)			
3	-	Circuit	Circuit configuration				
		C = co	mmon c	athode			
4	-	Packa	ge				
		T = TC	-220				
5	-	Schottl	ky "Q" s	eries			
6	-	Voltage	e rating	(020 = 2	20 V)		
7	-	Enviro	nmental	digit			
		-M3 =	halogen	-free, R	oHS-cor	mpliant,	and ter

ORDERING INFORMATION (Example)					
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION			
VS-47CTQ020-M3	50	Antistatic plastic tubes			

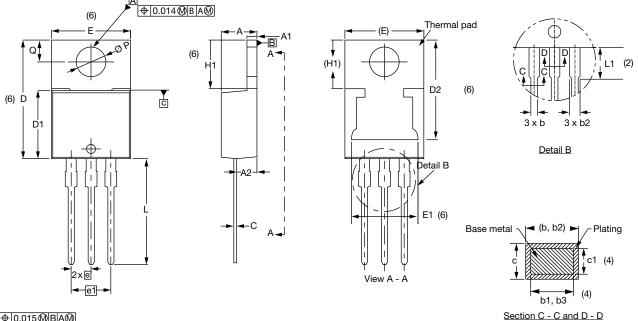
LINKS TO RELAT	ED DOCUMENTS
Dimensions	www.vishay.com/doc?96154
Part marking information	www.vishay.com/doc?95028



Vishay Semiconductors

TO-220AB 3L

DIMENSIONS in millimeters and inches



⊕0.015@BA@



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWDOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.50	2.92	0.098	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.35	0.585	0.604	3
D1	8.38	9.02	0.330	0.355	

MILLIMETERS	INCHES

Conforms to JEDEC[®] outline TO-220AB

SYMBOL			INTOLIEO		NOTES
STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	11.68	13.30	0.460	0.524	6, 7
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØР	3.54	3.91	0.139	0.154	
Q	2.60	3.00	0.102	0.118	

Notes

 $^{(1)}\,$ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension and finish uncontrolled in L1

(3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁴⁾ Dimension b1, b3, and c1 apply to base metal only

(5) Controlling dimensions: inches

⁽⁶⁾ Thermal pad contour optional within dimensions E, H1, D2, and E1

⁽⁷⁾ Outline conforms to JEDEC[®] TO-220, except D2

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1



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