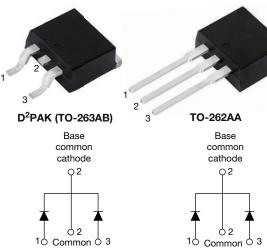
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SHAY

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## High Performance Schottky Rectifiers, 2 x 15 A



Anode cathode Anode VS-MBRB30..CT-M3

Anode cathode Anode VS-MBR30..CT-1-M3

PRIMARY CHARACTE	RISTICS
I <sub>F(AV)</sub>	2 x 15 A
V <sub>R</sub>	35 V, 45 V
V <sub>F</sub> at I <sub>F</sub>	See datasheet
I <sub>RM</sub> max.	100 mA at 125 °C
T <sub>J</sub> max.	150 °C
E <sub>AS</sub>	10 mJ
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA
Circuit configuration	Common cathode

#### FEATURES

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- Center tap D<sup>2</sup>PAK and TO-262 packages
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS	AND CHARACTERISTICS		
SYMBOL	CHARACTERISTICS	VALUES	UNITS
I <sub>F(AV)</sub>	Rectangular waveform (per device)	30	۸
I <sub>FRM</sub>	$T_{\rm C} = 123 \ ^{\circ}{\rm C}$ (per leg)	30	A
V <sub>RRM</sub>		35/45	V
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1020	А
V <sub>F</sub>	20 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.6	V
TJ	Range	-65 to +150	°C

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-MBRB3035CT-M3 VS-MBR3035CT-1-M3	VS-MBRB3045CT-M3 VS-MBR3045CT-1-M3	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	35	45	N/
Maximum working peak reverse voltage	V <sub>RWM</sub>		40	v

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ABSOLUTE MAXIMUM RATI	NGS				
PARAMETER	SYMBOL	TEST CON	DITIONS	VALUES	UNITS
Maximum average per leg	1	$T_{\rm C}$ = 123 °C, rated V <sub>B</sub>		15	
forward current per device	I <sub>F(AV)</sub>	$T_{\rm C} = 123$ C, falled $V_{\rm R}$		30	
Peak repetitive forward current per leg	I <sub>FRM</sub>	Rated V <sub>R</sub> , square wave, 20 k	κHz, T <sub>C</sub> = 123 °C	30	
Non-repetitive peak surge current		• •	ng any rated load condition h rated V <sub>RRM</sub> applied	1020	A
Non-repetitive peak surge current	IFSM	Surge applied at rated load of single phase, 60 Hz	conditions halfwave,	200	
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	$T_{J} = 25 \text{ °C}, I_{AS} = 2 \text{ A}, L = 5 \text{ n}$	mH	10	mJ
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to Frequency limited by $T_J$ max		2	А

ELECTRICAL SPECIFICAT	ONS				
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		VALUES	UNITS
		30 A	T <sub>J</sub> = 25 °C	0.76	
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	20 A	T.I = 125 °C	0.6	V
		30 A	$-1_{\rm J} = 125  {}^{\circ}{\rm C}$	0.72	
Maximum instantaneous	I <sub>BM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	Patad DC valtage	1	mA
reverse current	IRM ("	T <sub>J</sub> = 125 °C	Rated DC voltage	100	ШA
Threshold voltage	V <sub>F(TO)</sub>			0.29	V
Forward slope resistance	r <sub>t</sub>	$T_J = T_J maximum$		13.6	mΩ
Maximum junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal ran	ge 100 kHz to 1 MHz), 25 °C	800	pF
Typical series inductance	L <sub>S</sub>	Measured from top of terr	ninal to mounting plane	8.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL S	SPECIFIC/	ATIONS		
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	TJ		-65 to 150	°C
Maximum storage temperature range	T <sub>Stg</sub>		-65 to 175	U
Maximum thermal resistance, junction to case per leg	R <sub>thJC</sub>	DC operation	1.5	
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50	°C/W
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	50	
Approvimete weight			2	g
Approximate weight			0.07	oz.
Mounting torgue		Non-lubricated threads	6 (5)	kgf · cm
Mounting torque maximum		Non-Iublicated threads	12 (10)	(lbf · in)
Marking daviag		Case style D <sup>2</sup> PAK		3035CT 3045CT
Marking device		Case style TO-262	MBR30 MBR30	35CT-1 45CT-1

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## VS-MBRB30..CT-M3, VS-MBR30..CT-M3

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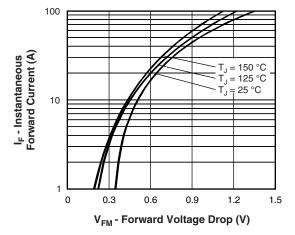


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

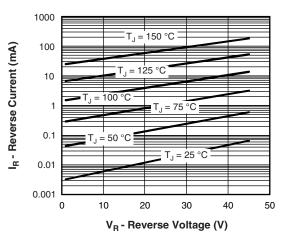


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

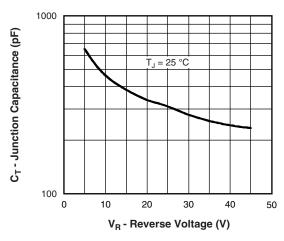
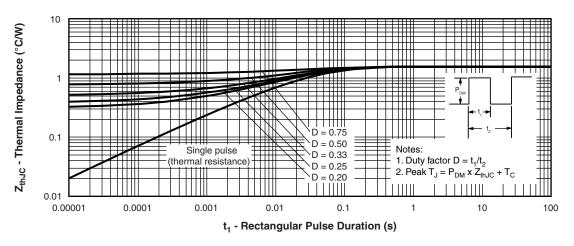


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





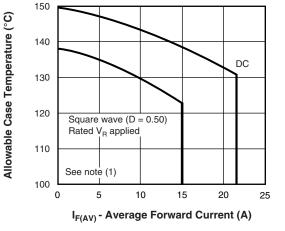
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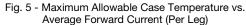
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## VS-MBRB30..CT-M3, VS-MBR30..CT-M3

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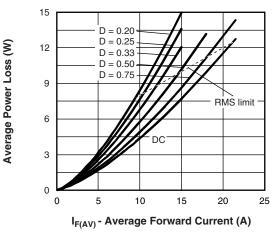


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

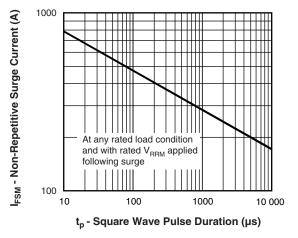


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

#### Note

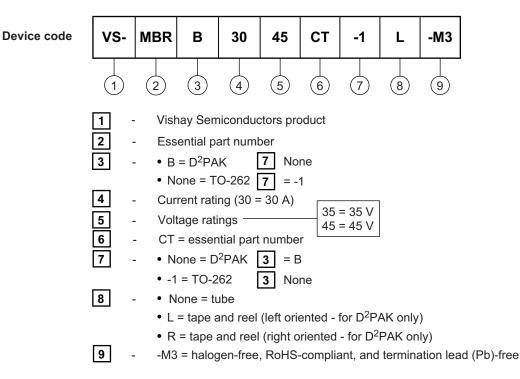
 $^{(1)} \mbox{ Formula used: } T_C = T_J - (Pd + Pd_{REV}) \ x \ R_{thJC}; \\ Pd = \mbox{ forward power loss = } I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (see \ fig. \ 6); \\ Pd_{REV} = \mbox{ inverse power loss = } V_{R1} \ x \ I_R \ (1 - D); \ I_R \ at \ V_{R1} = \ rated \ V_R$ 



# VS-MBRB30..CT-M3, VS-MBR30..CT-M3

## **Vishay Semiconductors**

### **ORDERING INFORMATION TABLE**



<b>ORDERING INFORMATION (Ex</b>	ample)	
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION
VS-MBRB3030CTLL-M3	800	13" diameter plastic tape and reel
VS-MBRB3030CTL-M3	800	13" diameter plastic tape and reel
VS-MBRB3030CTLR-M3	800	13" diameter plastic tape and reel
VS-MBRB3045CTL-M3	800	13" diameter plastic tape and reel
VS-MBRB3045CT-M3	50	Antistatic plastic tubes
VS-MBRB3045CTR-M3	800	13" diameter plastic tape and reel
VS-MBR3045CT-1-M3	50	Antistatic plastic tubes

	LINKS TO RELATED DOCUMENTS	
Dimensions	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96164
Dimensions	TO-262AA	www.vishay.com/doc?96165
Part marking information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444
Part marking information	TO-262AA	www.vishay.com/doc?95443
Packaging information		www.vishay.com/doc?96424

**Vishay Semiconductors** 

D<sup>2</sup>PAK

#### **DIMENSIONS** in millimeters and inches



ota	ted	90	°C
<u>S</u>	cale	<u>ə:</u> 8	:1

SYMBOL	MILLIM	ETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	0.00	0.254	0.000	0.010		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
с	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STNDUL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100 BSC		
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25	BSC	0.010	BSC	
L4	4.78	5.28	0.188	0.208	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

(3) Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

(5) Datum A and B to be determined at datum plane H

(6) Controlling dimension: inches

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-263AB

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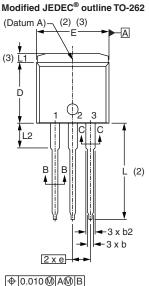
## **Outline Dimensions**

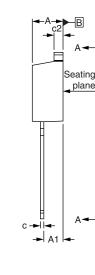


**Vishay Semiconductors** 

**TO-262AA** 

#### **DIMENSIONS** in millimeters and inches





F D1 (3) (3) Section A - A Base (4) Plating b1. b3 metal ≰ c1 (4) -(b, b2)-Section B - B and C - C Scale: None





Diodes 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode

Lead assignments

CVMPOI	MILLIN	IETERS	INC	HES	NOTES
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100	) BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

 <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
 <sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the second dimensioner of the second dimensis and the second dimensioner of the second dimensioner of the the outmost extremes of the plastic body (3)

Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only (5)

Controlling dimension: inches

(6) Outline conform to JEDEC® TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), L1 (max.), L2 (min., max.)

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