**Vishay Semiconductors** 



# Hyperfast Rectifier, 15 A FRED Pt<sup>®</sup>





#### VS-15ETH06FP-N3

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	15 A			
V <sub>R</sub>	600 V			
V <sub>F</sub> at I <sub>F</sub>	1.3 V			
t <sub>rr</sub> typ.	22 ns			
T <sub>J</sub> max.	175 °C			
Package	TO-220 FullPAK 2L			
Circuit configuration	Single			

### **FEATURES**

- · Hyperfast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Single die center tap module
- Fully isolated package (V<sub>INS</sub> = 2500 V<sub>RMS</sub>)
- Designed and qualified according to JEDEC<sup>®</sup>-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V <sub>RRM</sub>		600	V
Average rectified forward current	I <sub>F(AV)</sub>	$T_{\rm C} = 80 \ ^{\circ}{\rm C}$	15	
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	180	А
Peak repetitive forward current	I <sub>FM</sub>		30	
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C

<b>ELECTRICAL SPECIFICATIONS</b> ( $T_J = 25$ °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_{R}$	I <sub>R</sub> = 100 μA	600	-	-	
Forward voltage	V	I <sub>F</sub> = 15 A	-	1.8	2.2	V
Forward voltage V <sub>F</sub>	۷F	I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	1.3	1.6	
Reverse leakage ourrent	1	$V_{R} = V_{R}$ rated	-	0.2	50	
Reverse leakage current I <sub>R</sub>		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	30	500	μA
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	20	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_c = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		I <sub>F</sub> = 1 A, dI <sub>F</sub> /dt = 100 A/μs, V <sub>R</sub> = 30 V		-	22	30	
Reverse recovery time	+	$I_F = 15 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$		-	28	35	
neverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	29	-	ns
		T <sub>J</sub> = 125 °C	I <sub>F</sub> = 15 A dI <sub>F</sub> /dt = 200 A/µs	-	75	-	
Poak rocovary ourrant	eak recovery current	T <sub>J</sub> = 25 °C		-	3.5	-	А
Feak recovery current		IRRM	T <sub>J</sub> = 125 °C	$V_{\rm B} = 390 \text{ V}$	-	7	-
Boyorga racovany abarga	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	VR - 000 V	-	57	-	nC
Reverse recovery charge Q <sub>rr</sub>		T <sub>J</sub> = 125 °C		-	300	-	no
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 125 °C	l <sub>F</sub> = 15 A dl <sub>F</sub> /dt = 800 A/μs	-	51	-	ns
Peak recovery current	I <sub>RRM</sub>			-	20	-	А
Reverse recovery charge	Q <sub>rr</sub>		V <sub>R</sub> = 390 V	-	580	-	nC

THERMAL MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction-to-case	R <sub>thJC</sub>		-	3.0	3.5	
Thermal resistance, junction-to-ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W
Thermal resistance, case-to-heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	2.0	-	g
weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220 FullPAK 2L	15ETH06FP			

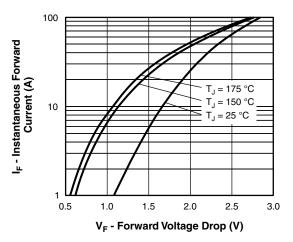


Fig. 1 - Typical Forward Voltage Drop Characteristics

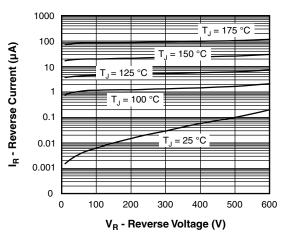


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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### VS-15ETH06FP-N3

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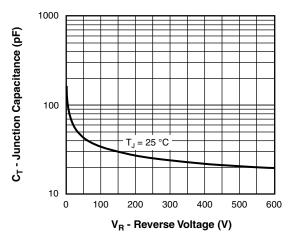


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

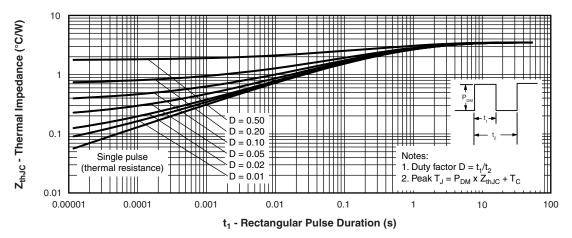
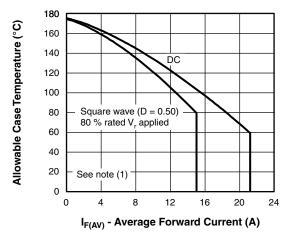
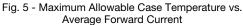
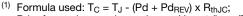


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics





### Note



 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, x \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{5}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, x \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

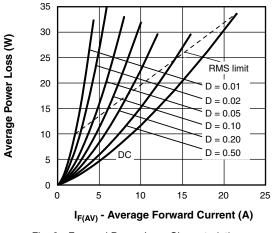


Fig. 6 - Forward Power Loss Characteristics

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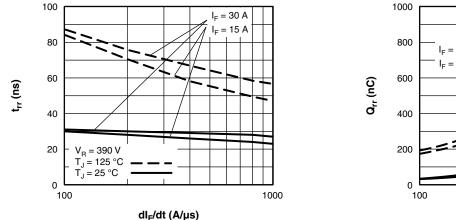


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

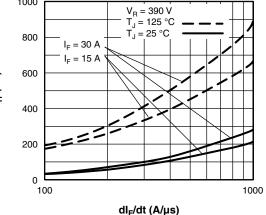


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

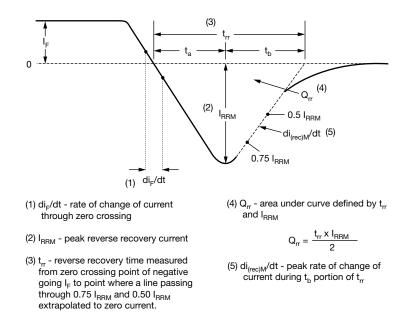
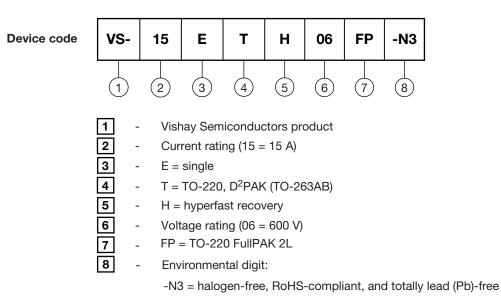


Fig. 9 - Reverse Recovery Waveform and Definitions





#### **ORDERING INFORMATION TABLE**



ORDERING INFORMATION (Example)					
QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
50	1000	Antistatic plastic tube			
	QUANTITY PER T/R	QUANTITY PER T/R MINIMUM ORDER QUANTITY			

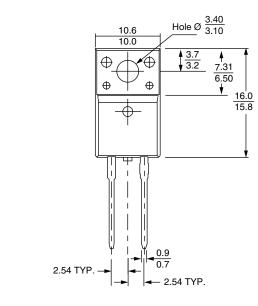
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96157			
Part marking information	www.vishay.com/doc?95392			
SPICE model	www.vishay.com/doc?96618			

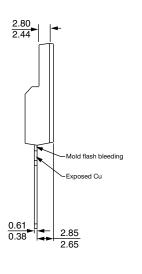


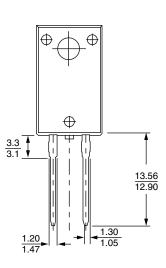
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# 2L TO-220 FullPAK

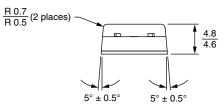
#### **DIMENSIONS** in millimeters







Bottom view





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